| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see Al2 thru Al5 C'ard 2 <br> Result C'ode |
| :---: | :---: | :---: | :---: |
| 0052 | F826 | Overrun detected without the any erson line active. | A 1 |
| 00.52 | 1827 | Overrun detected without the any error line active. | A1 |
| 0052 | F828 | Overrun detected without the any error line active. | A 1 |
| 00.52 | 1830 | Expected and received tape status do not match. | 134 |
| 00.52 | 18840 | Tape unit fails 10 position the $1 B G$ under the head. | CI |
| 0052 | F841 | Tape unit fails to position the 1 BC under the head. | 134 |
| 0052 | 1843 | Tape unit fails to position the 1 BG under the head. | 134 |
| 00.52 | 5849 | Tape unit fails to position the 113 G under the head. | 134 |
| 0052 | F84C | Tape unit fails to position the IBG under the head. | Cl $)^{4}$ A2/ 13 |
| 0052 | F84E | Tape unit fails to position the 1 BC under the head. | Cl |
| 0052 | F850 | Each falure detected by the positioning routine. | i) 1 |
| 0052 | 18851 | Each failure derected by the positioning routine. | 134 |
| 0052 | [853 | Each faiure detected by the positioning routine. | A2/A3 |
| 0052 | F854 | Each failure detected by the positioning routine. | A 1 |
| U052 | F856 | Each failure detected by the positioning routine. | A1 |
| 0052 | F 857 | Each failure detected by the positioning routine. | A 1 |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see Al2 thru Al5 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | 18858 | Each fallure detected by the positioning routine. | A 1 |
| 0052 | 1859 | Each failure detected by the positioning routine. | 134 |
| 00,52 | 1885 | Each fature detected by the positiouing rowitic. | C1 A2/A3D4 |
| 0052 | 1 C 55 | Each failure detected by the positioning routine. | Cl |
| 00.52 | 1860 | Fach failure detected during a data aperation. | I)1 |
| (0)52 | 18861 | Each failure detected during a data operation. | 134 |
| 0052 | 1.863 | Each failure detected during a data operation. | A2/A3 |
| 0052 | 18864 | Fach failure detected during a data operation. | A 1 |
| 0052 | 1866 | Each failure detected during a data operation. | A1 |
| 0052 | 1867 | Each failure detected during a data operation. | A 1 |
| 00.52 | 1868 | Each failure detected during a data operation. | A1 |
| 0052 | 5869 | Each failure detected during a data operation. | 134 |
| 0052 | 186C | Each failure detected dering a data operation. | C1 A2/A3 D4 |
| 0052 | F86E | Each failure detected during a data operation. | C1 |
| 0052 | F870 | Each failure during a format character operation. | D) C4 |
| 0052 | F871 | Each failure during a format character operation. | 134 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see A12 thru 115 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | 1.873 | Each failure during a format character operation. | A2/A3 |
| 00.52 | 1874 | Each failure during a format character operation. | A1 |
| 0052 | 18876 | Each failure during a format character operation. | A1 |
| 0052 | 1887 | Each failure during a format character operation. | A I |
| 0052 | 1.878 | Each falure during a format character operation. | A1. |
| 0052 | 18879 | Each failure during a format character operation. | B4 |
| 0052 | 187C | Each failure during a format character operation. | C1 A2/A3 D4 |
| 0052 | H87E | Each faill.re during a format character operation. | C1 |
| 0052 | 18880 | Received tape unit status does not match expected status. | D3 |
| 0052 | 1881 | Received tape unit status does not match expected status. | B4 B5 |
| 0052 | 1882 | Received tape unit status does not mateh expected status. | B3 |
| 0052 | 18883 | Received tape unit status does not match expected status. | A2/A3 135 B4 |
| 0052 | F885 | Received tape unit status does not match expected status. | 135 |
| 0052 | 1888 | Received tape unit status does not match expected status. | B3 |
| 0052 | F889 | Received tape unit status does not match expected status. | B4 |
| 0052 | F88A | Received tape unit status does not match expected status. | B3 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Delinition | For result code definitions, see Al2 thru A15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | 188C | Received tape unit status does not match expected status. | C1 A2/A3D4 |
| 0052 | F881 | keceived tape unit status does not match expected status. | C1 |
| 0052 | 1890) | Received and expected tape unit status (lo not agres. | 1)3 |
| 0052 | 1891 | Received and expected tape unit status do mot agree. | 134135 |
| 0052 | 1892 | Received and expected tape unit htatur (lo not agree. | 133 |
| 0052 | F893 | Recejed and expected tape unit status do not agree. | A2/A3 135 33 |
| 0052 | 1895 | Receised and expected tape unit status do not agree. | 135 |
| 0052 | 1896 | Recerised and expeeted tape unit status do nor agree. | 133 |
| 0052 | 1898 | Receised and expected tape unit status do not agree. | 133 |
| 0052 | F89 A | Received and expected tape mit status do not agree. | 133 |
| 0052 | 189C | Received and expected tape unit biatur do not agree. | CI A2/A3 D4 |
| 0052 | 1891: | Received and expected tape unit stallus do not agree. | CI |
| 0052 | F8A1 | The ARA ID was detected away from bot on a read. | 134 |
| 0052 | F8A3 | The ARA ID was detected away from but on a read. | A2/A3 |
| 0052 | 1884 | The ARA ID was detected asey from bot on a read. | 131 111 ${ }^{\text {D C1 }}$ |
| 0052 | F8A5 | The ARA ID was detected away from bot on a read. | A5 131 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see A12 thru A15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | F8A6 | The ARA ID was detected away from bot on a read. | A5 B1 |
| 0052 | F8A7 | The ARA ID was detected away from bot on a read. | B1 |
| 0052 | F8A9 | The ARA ID was detected away from bot on a read. | B1 134 |
| 0052 | 18AA | The ARA ID was detected away from bot on a read. | B1 |
| 0052 | F8AC | The ARA ID was detected away from bot on a read. | C1 A2/A3 D 4 |
| 0052 | F8AE | The ARA ID was detected away from bot on a read. | 1)2 C1 F6 |
| 0052 | F8AF | The ARA ID was detected away from bot on a read. | D2 |
| $00 \leq 2$ | F8B0 | Start velocity check-tach speed error. | D1 C1 C4 |
| 0052 | F8C0 | No IBG after the ID burst. | H1 D2 C1 D6 |
| 0052 | F8C1 | No IBG after the ID burst. | B4 136 |
| 0052 | F8C2 | No ! BG after the ID burst. | 133 |
| 0052 | F\&C3 | No IBG after the ID burst. | A2/A3 134 |
| 0052 | F8C4 | No IBG after the ID burst. | A5 131 A1 |
| 0052 | F8C5 | No IBG after the ID burst. | A5 B1 |
| 0052 | F8C6 | No IBG after the ID burst. | B3 A1 |
| 0052 | F8C7 | No ! BG after the ID burst. | B1 A1 |
| 0052 | F8C8 | No IBG after the ID burst. | B3 A1 |
| 0052 | F8C9 | No IBG after the iD burst. | B1 B4 |
| 0052 | F8CA | No iBG after the ID burst. | B3 B1 |


| SRC <br> 1234 | 5678 |  | For result code definitions, <br> see A1 thru A15 Card 2 <br> Destinition |
| :--- | :--- | :--- | :--- |
| 0052 | F8CC | No IBG after the ID burst. | C1 A2/A3 D4 |
| 0052 | F8CE | No IBG after the ID burst. | D2 C1 F6 H1 |
| 0052 | F8CF | No IBG after the ID burst. | D2 |
| 0052 | F8D0 | No IBG after writing or reading the <br> ARA ID. | H1 D2 C1 D6 |
| 0052 | F8D1 | No IBG after writing or reading the <br> ARA ID. | B4 |
| 0052 | F8D4 | No IBG after writing or reading the <br> ARA ID. | A5 B1 A1 |
| 0052 | F8D2 | No IBG after writing or reading the <br> ARA ID. | B3 |
| 0052 | F8D3 | No IBG after writing or reading the <br> ARA ID. | A2/A3 B3 |
| 0052 | F8D5 | No IBG after writing or reading the <br> ARA ID. | A5 B1 |
| 0052 | F8D6 | No IBG after writing or reading the <br> ARA ID. | B3 A1 |
| 0052 | F8D7 | No IBG after writing or reading the <br> ARA ID. | B1 A1 |
| 0052 | F8D8 | No IEG after writing or reading the <br> ARA ID. | B3 A1 |
| 0052 | F8D9 | No IBG after writing or reading the <br> ARA ID. | B4 |
| 0052 | F8DA | No IBG after writing or reading the <br> ARA ID. | B3 B1 |
| 0052 | F8DC | No IBG after writing or reading the <br> ARA ID. | C1 A2/A3 D4 |
| 0052 | F8DE | No IBG after writing or reading the <br> ARA ID. | D2 C1 F6 H1 |


| $\begin{aligned} & \text { SIRC } \\ & 123 . \end{aligned}$ | 5678 | Definition | For result code definitions, see A/2 thru A15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | [8D ${ }^{\text {P }}$ | No IBG after writing or reading the ARA ID. | D2 H1 CI D6 |
| 0052 | F8E0 | IBC check during erase gap. | II1 D2 C1 D6 |
| 0052 | T8EI | 1BG check during erase gap. | 134 |
| 0052 | F8E2 | 1BG check during crase gap. | 133 111 |
| 0052 | T8E3 | IBG check during crase gap. | A2/A3 133 |
| 0052 | 18E4 | IBG check during erase gap. | A1 131 |
| 00.52 | H8E5 | IBG cheek during erase gap. | A5 131 A1 |
| 0052 | F8E6 | IBG check during crase gap. | 133 Al |
| 0052 | F8E7 | 1BS check during erase gap. | 131 A1 |
| 0052 | 18E8 | IBG dieck during erase gap. | 133 A1 |
| 0052 | 18E9 | IBG cheok during erase gap. | 131 134 |
| 0052 | F8EA | IBS check during erase gap. | 133131111 |
| 0052 | F8EC | IBG cheek during erase gap. | 『1 A2/A3 D4 H1 |
| 0052 | $\cdots 8 \mathrm{CE}$ | IBG check during erase gap. | D2 C1F6 H1 |
| 0052 | F8EF | IBG cheek during erase gap. | D2 111 C 1 D 6 |
| 0052 | F8F0 | No readback of ID burst on write operation. | 111 |
| 0052 | F8F1 | No readback of ID burst on write operation. | 324 |
| 0052 | F8F2 | No readback of ID burst on write operation. | P3 1/1 |
| 0052 | F8F3 | No readback of ID burst on write operation. | A2/A3 B3 |
| 0052 | F8F4 | No readback of ID burst on write operation. | A5 B1 |


| $\begin{aligned} & \mathrm{SRC} \\ & 12.34 \end{aligned}$ | 5678 | Delinition | For result code definitions, see A12 Hiru Al5 Card 2 Result Code |
| :---: | :---: | :---: | :---: |
| 00.22 | F 8 F 5 | No readback of ID burst on write operation. | A5 B1 |
| 0052 | F816\% | No readback of iD burst on write operation. | 133 A |
| 0052 | 18857 | No readhack of ID burst on write operation. | B1 AI |
| 0052 | 18188 | No readback of ID burst on write operation. | 133 A1 |
| 00052 | 1889 | No readtack of ID burst on write operation. | 134 H1 |
| 01052 | F8FA | No readback of ID burst on write operation. | [33 B1 |
| 00.52 | F8FC | No readback of ID burst on write operation. | C1 A2/A3 D4 |
| 0052 | F8IFE | No readhack of ID burst on write operation. | D2 Cl F6 III |
| 0052 | F8EA | No readback of iD burst on srite operation. | D2 H1 C1 D6 |
| 0052 | $\mathrm{F900}$ | No readback of ARA burst on write operation. | H1 D2 |
| 00.52 | 1901 | No readback of ARA burst on write operation. | B4 H1 C1 D6 |
| 00.52 | F910 | No readthack of ARA ID burst on write operation. | H1 D2 C1 D6 |
| 0052 | F911 | No) readback of ARA ID burst on write operation. | B4 H1 |
| 00.2 | F912 | No readback of ARA in burst on write operation. | B3 H1 |
| 00.52 | 1913 | No readback of ARA ID burst on write operation. | A2/A3 B3 |
| 00.52 | H914 | No readback of ARA ID burst on write operation. | A5 B1 |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Declinition | For resull code detinitions, see Al2 thru A15 Card 2 Result Code |
| :---: | :---: | :---: | :---: |
| 000.2 | F915 | No reat back of ARA In burst on write operatun. | A5 B1 H1 |
| (10)52 | [916 | No, readtack of ARA 1D burst on write operation. | 133 |
| 10052 | 19918 | No readhack of ARA 1!) burst on write operation. | A2/A3 133 111 |
| 10053 | 1919 | No readhack of ARA 10 hurst on write uperation. | 134111 |
| 0052 | 191A | No readhach of ARA ID burst on write uperation. | 133131111 |
| 00.52 | 1931 C | Non readback of ARA II) burst un write operation. | Cl $12 / \mathrm{A} 3 \mathrm{D} 4 \mathrm{H} 1$ |
| 0052 | F914: | No readhack of $\{$ RA ID burst on write operation. | D) 2186111 |
| 0052 | F91F | No sceadback of ARA 11 burst on write aperation. | 1)2 111 Cl |
| 00052 | 1920 | Incorrect 1D burst witten by tape unit. | 111 D 2 C 1 D 6 |
| 00.52 | F921 | Incorrect id bum writen by tape unit. | 134 I11 |
| 0055 | F922 | Incorrect (1) burst writuen by tape unit. | 133 |
| 0052 | 1992 | Incorreet ID burs written by tape unit. | A2/A3 133 |
| 0052 | 1992 | Incorrect 1D burst writuen by tape unit. | A5 131 |
| 0052 | F925 | Incorrect ID burst written by tape unit. | A5 131 |
| 0052 | F926 | Incorrect ! D liurst written by tape unit. | B3 |
| 0052 | F928 | Incorictit iD burst written by tape unit. | B3 H1] |
| 0052 | F929 | Incorreet ID burst written by tape umit. | 134 |
| 0052 | F92A | Incorrect ID burst written by tape unit. | B3 131 111 |


| $\begin{aligned} & \mathrm{SRC} \\ & 12.34 \end{aligned}$ | 5678 | Definition | For result code definitions, sec A12 thru A 15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | 192C | Ineorrect ID burst written by tape unit. | C1 A2/A3 D 4 H1 |
| 0052 | F92E | Incorrect ID burst written by tape unit. | I)2 C! F6 111 |
| 0052 | F92F | Ineorreet ID burst written by tape unit. | D)2 |
| 0052 | 1930 | Incorrect ARA burst wrillen by tape unit. | 111 |
| 00.52 | F931 | Incorrect ARA burst written by tape unit. | 134 |
| 0052 | 1932 | Incorrect ARA burst written by tape unit. | [33 H1 |
| 0052 | F933 | Incorrect ARA burst written by tare unit. | A2/A3 133 |
| 0052 | [934 | Incorsect ARA burst written by tape unit. | 15131 |
| 00.52 | F935 | Incorrect ARA burst written by tape unit. | A5 131 H1 |
| 00.52 | F936 | Incorrect ARA burst written by tape unit. | B3 |
| 0052 | F937 | Incorrect ARA burst written by tape unit. | B1 |
| 0052 | F938 | Incorrect ARA burst writtes hy tape unit. | B3 H1 |
| 0052 | F939 | Incorrect ARA burst writter by tape unit. | 131 B4 |
| 0052 | F93A | Incorrect ARA burst written by tape unit. | 133 B1 H1 |
| 0052 | F93C | Incorrect ARA burst written by tape unit. | C1 A2/A3 D 4 H1 |
| 00.52 | F93E | Incorrect ARA burst written by tape unit. | D2 C1 F6 H1 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, ser A12 thru A15 Card 2 <br> Resulá Code |
| :---: | :---: | :---: | :---: |
| 0052 | F93F | incorrect ARA burs! written by tape unit. | D2 H1 |
| 0052 | F940 | Incorrect ARA. ID iurst written by tape unit. | H1 |
| 0052 | F941 | Incorrect ARA ID burst written by tape unit. | B4 |
| 0052 | F942 | Incorrect ARA ID burst written by tape unit. | B3 H1 |
| 0052 | F943 | Ircorrect ARA ID burst written by tape unit. | A2/A3 B3 |
| 0052 | 5944 | Incorrect ARA ID burst written by tape unit. | A5 B1 |
| 0052 | F945 | Incorrect ARA ID burst wrizer by tape unit. | A5 131 111 |
| 0052 | F946 | Incorrcct ARA ID burst written by tape unit. | 133 |
| 0052 | F947 | Incorrect ARA ID burst written by tape unit. | [31 |
| 0052 | F948 | Incorrect ARA ID burst written by tape unis. | B3 H1 |
| 0052 | F949 | Incorrect ARA ID burst written by tape unit. | B1 B4 |
| 0052 | F94A | Incorrect ARA ID burst written by tape unit. | $133 \mathrm{BlH1}$ |
| 0052 | F94C | Incorrect ARA ID burst written by tape unit. | Cl A2/A3 D4 H1 |
| 0052 | F94E | Incorrect ARA ID burst written by tape unit. | D2 Cl F6 H1 |
| 0052 | F94F | Incorrect ARA ID burst written by tape unit. | D2 H1 |
| 0052 | F950 | The iape control is not capabic of reading the tape. | HI D2 Cl D6 |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code detinitions, see Al2 larm Al5 Coard 2 Result Code |
| :---: | :---: | :---: | :---: |
| 000.52 | 1955 | The tape control is not capable of reading the tape. | B4 |
| 0052 | F 952 | The tape control is not capable of reading the tape. | B3 111 |
| 0.52 | 1953 | The tape controi is nut capable of reading the tape. | A2/A3 133 |
| 0052 | F 954 | The tape control is not capable of reading the tape. | 15131 |
| 0052 | 1955 | The tape control is not capable of reading the tape. | A5 131 111 |
| 0052 | 1956 | The tape control is not capable of reading the tape. | B3 |
| 00.32 | 1957 | The tape control is not capable of reading the tape. | B] |
| 01052 | 1958 | The tape control is not capable of reading the tape. | B3 H1 |
| 0052 | F959 | The tape control is not capable of reading the tape. | B1 B4 |
| 100.2 | F95A | The tape control is not capable of reading the tape. | 133 131 \|11] |
| 0052 | 1955 | The tape control is not capable of reading the tape. | C1/2/A3 D 4111 |
| 0052 | F95E | The tape control is not capable of reading the tape. | D2 C1 F6 H1 |
| 0052 | F951: | The tape control is not capable of reading the tape. | D)2 [1] |
| 0052 | F961 | Tape control cannot set GCR after reading the ARA. | 134 |
| 0052 | F963 | Tape control cannot set GCR after reading the ARA. | A2/A3 D4 |
| 0052 | F969 | Tape control cannot set GCR after reading the ARA. | B4 |


| $\begin{aligned} & \mathrm{SRC} \\ & 123.4 \end{aligned}$ | 5678 | Delinition | For result code definitions, see Ai 2 thris A15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | 196 | Tape control cannot set GCR after reading the $A R A$. | CI A2/A3 1 ) 4 |
| (0)こ2 | F96I' | Tape control cannot sel GCR alter reading the ARA. | CI |
| 0052 | 19971) | Never detected beginning of block ol ARA burs. | H1 |
| 0052 | 1971 | Never detected begmong of block of ARA burs. | 134 |
| 01052 | 19973 | Never detected beginning of r lock of ARA burnt. | A2/A3133 |
| 010.52 | 1974 | Neser detected begiming of bloch of ARA burst. | A5131 |
| (0)52 | 1995 | Never detected begimming of block of ARA burst. | A5131 H1 |
| 00.52 | 19976 | Never detected beginning of black of ARA burst. | 13.3 |
| 01022 | 11977 | Never aetected beginning of block of ARA bumb. | I31 A1 |
| 00.52 | 1.978 | Never dete ted beginning of block of ARA bumb. | A 1 |
| 00.52 | 1.979 | Never detected beginning of block of ARA burs. | B4Cl |
| 0052 | 197 | Never detected begimning of block of ARA burst. | B1 |
| 0052 | 197C | Never detected heginning of block of ARA burst. | Cl A2/A3 D4 H1 |
| 00.52 | 197E | Never detected beginning of block of ARA burst. | D2 Cl F6 H1 |
| 00.52 | I971 | Never detected beginning of block of ARA burst. | D2 |
| 0052 | 1980 | ARA failed to set read circuits to correct gain. | H1 |

3430 SRC Table

| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see A12 thru A15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | 19881 | ARA failed to set read circuits to correct gain. | B5 B4 |
| 0052 | 19882 | ARA failed to set read circuits to correct gain. | B3 |
| 0052 | F983 | ARA failed to set read circuits to correct gain. | A2/A3 B3 |
| 0052 | F988 | ARA failed to sei read circuits to correct gain. | 133 |
| 0052 | F 989 | ARA failed to set read circuits to correct gain. | B4 |
| 0052 | F98A | ARA failed to set read circuits to correct gain. | B3 |
| 0052 | 1988 C | ARA failed to set read circuits to correct yain. | C1 A2/A3 D4 H1 |
| 0052 | F98E | ARA failed to set read circuits to correct gain. | D2 C1 F6 H1 |
| 0052 | F98F | ARA falled to set read circuits to correct gain. | D2 |
| 0052 | F990 | Unable to write a tape mark. | H1 |
| $005 ?$ | 19991 | Unable to write a tape mark. | B4 |
| 0052 | F992 | Unable to write a tape mark. | B3 |
| 0052 | F 993 | Unable to write a tape mark. | A2/A3 B3 |
| 0052 | F994 | Unable to write a tape mark. | A5 B1 |
| 0052 | F995 | Unable to write a tape mark. | A5 B1 |
| 0052 | F996 | Unable to write a tape mark. | B3 |
| 0052 | F997 | Unable to write a tape mark. | B1 |
| 0052 | F998 | Unable to write a tape mark. | B3 |

El4 Card 1

| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Deffinition | For result code delinitions ser Al2 lhru A15 ('and 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0055 | F999 | Umable to write a tape marh. | 134111 |
| 0052 | 1:99A | Unable to write a tape man". | 133 131 |
| 005: | F99C | Whate lowrite a lape manh. | (1) \2/ 13 1)4 |
| 0052 | F991: | Unable lo write a tape marh. | 1)2 21 ll 6 HI |
| 00.52 | F99\% | Whathe th write a bape math. | 1)2 |
| 0002 | F9A0 | Write tape math uperation finled. | 111 |
| 0052 | F9, ${ }^{\text {P1 }}$ | Write lape matk uperation hated. | 134 |
| 0052 | F9:12 | Write tape matk operation fancol. | 133 |
| 0052 | F9, 13 | Write lape mark uperation lubleat. | 12/13133 |
| (1)152 | $\mathrm{F9} 44$ | Write bape mark uperatum fateod. | 15131 |
| 0052 | 1985 | Write tape marh uperation failed. | 1231 |
| 0052 | F9A6 | Write tape mark uperation fater. | 133 |
| 0052 | 19947 | Write lape mark operatum thled. | 131 |
| 00.52 | 19988 | Write tape mon .,neratun faiket. | 133 |
| 0052 | F9, 9 | Write tape mark uppration failed. | 131 134 |
| 0052 | F9AA | Write tape marh operation tuled. | 133131 |
| 0052 | F9AC | Write tape mark unctatio miled. | C 1 12/13 D 4111 |
| 0052 | F9AE | Write tape mark operation failed. | I) 2 C1 166 |
| 0052 | F9AF | Write tape mark uperation failed. | I) 2 |
| 0052 | F9B0 | W'rite operation failed - no read back data. | [1] |
| 0052 | F9B1 | Write operation foiled no real hack data. | E4 |


| $\begin{aligned} & \mathrm{S} 186^{\circ} \\ & 12.14 \end{aligned}$ | $56 \% 8$ | Dellinition | Fior result code definitions， sere 112 thro 115 （ard 2 <br> Resull Coble |
| :---: | :---: | :---: | :---: |
| い以？ | 19132 | Write uperation fitiled－no read batek dilta． | 13.3 |
| （1105？ | 19133 | Write operation falded moread bach data． | 12／13133 |
| （1705？ | $1 \cdot 9134$ | Write uperation falided moread back data． | \5i31 |
| （6）い2 | 19135 | Write aperation lated－no read back dilta． | Asinl |
| （0）？ | $1: 9136$ | Write operamon falded numed back dalt | 133 |
| （1002 | 199137 | Write operation fated now read back dita． | i31 |
| （10） | 19138 | Nrite pleration fated－s．a read bate dallal． | 133 |
| （00152 | 19139 | Write aperation falked no read back dilli． | 131134 |
| （0）15？ | 1：913． | Write oncataton failed no read bath dal．， | 133131111 |
| 00.52 | $1913 C$ | Write operation failed no read hack data． | C1 12／：31）4111 |
| （0） 02 | 1913E | Write aperation Fialed nor read back data． | 1）2 C1F6 |
| （0）5． 2 | 19131 | Write uperation falled－no read bach data． | 1）2 |
| 0052 | 19）（1 | Data end was activated before command out on write． | 134 |
| （）05こ | F9C4 | Data end was activated before command out on write． | 132 131 15 A1 |
| 0052 | 199 5 | Data end was activated before command out on write． | 132 131 A5 |
| （）0．5．2 | 1906 | Data end was activated before command out on write． | 132 A1 |


| $\begin{aligned} & \mathrm{sRC} \\ & 12.34 \end{aligned}$ | 5678 | Dectinition | For result code definitions, see A12 lom 115 Card? Resull Code |
| :---: | :---: | :---: | :---: |
| 010512 | $19 \mathrm{C7}$ | Datal cod was atcivand belore command out on write. | $132 \mathrm{B1}$ A1 |
| 0052 | 190 | 1) ata end was activated before command out on write. | B1 134 |
| 0012 | I9CA | Data end was atervated berone command out on write. | 13213141 |
| 010.2 | 191)0 | 1B6; detected before data end during reas. | $111122 \mathrm{Cl} \mathrm{D6}$ |
| 0052 | 191)1 | IBG detectad before data end during reall. | 134 |
| 10052 | 191)2 | IBGi detened before data end during read. | B3 |
| (10)2 | 1093 | IBG deteded before data end during sead. | 12/A3 133 |
| (10) ${ }^{\text {a }}$ | 191)4 | 1B6 i detected before data end during tead. | A5 B1 132 |
| $0102 ?$ | 19015 | HBG detected before data end during reard. | $15 \mathrm{B1} 132$ |
| 01052 | 191)6 | 1336 detected before data end durins; read. | 133132 |
| (1)0,5 | 190) | IBG detected hefore data end during reat | B1 |
| 005.5 | F91)8 | ABG detected before data end during rad. | 133 |
| 005.2 | F909 | BBG detected before data end during read. | 131134 |
| ()0,52 | F90A | IBC deteded before data end during read. | 133131132 |
| 00.52 | F91) C | IBG detected before data end during read. | C1 A2/A3 D) |
| 0052 | F9DE | IBG detected before data end during read. | D2 CIF6 H1 |


| $\begin{array}{ll} \mathrm{SRC} \\ 12.34 & 5678 \end{array}$ | Delinition | Fior result code definitions， see A12 Airn A1s Carr＇ 2 <br> Resulf Code |
| :---: | :---: | :---: |
| （0）52［190）1 | 1BG deteled br fife data end during read． | D）2 |
| 0052 l 9 OLO | No BOI demem daring read back of ARA． | I 22 CI |
| 005210031 | No B（）I deroyed daring read back of ARA． | 134 |
| （10．52 1091：3 | No lBill deacted during read back of ARA． | A2／A3 |
| （1052 19919） | No is（）l detectal during reat back ai ARA． | 134 |
| 0052 HOEC | F．，BOO Wetceted during read back wl \RA． | CI $12 / 131) 4$ |
| 0 0．52 H93li | NoBOI detseted dmiang read hack of $\therefore$ RA． | CI lig |
| 10．52 19013 | Nolblimanderected after a writo． がal．ar ヶpace． | 11112 ClD |
| （0）［7） | NoIBC was derected after a write read．or spale | 134 |
| 0052［91：2 | No 1BG，wias detected after a write． read．or spalce． | 133 |
| 0052 19013 | No IBG was detected after a write， reat．wr－pice． | A2／A3134 |
| $0052 \mathrm{FOT4}$ | No lBG was detected after a write． reat，or upace． | A5 B1 A1 |
| 0052 1995 | No IBG was detected after a write， real，or space． | A5 131 |
| 0052 F9F6 | No lBG was detected after a write． read，or space． | 133 A I |
| 0052 19917 | No lBG was detected after a write， read，or space． | B1 Al |
| $0052 \quad 39 \mathrm{C}$ | No IBC was detected after a write， reald，or space． | 133 A 1 |


| $\begin{array}{\|l\|l\|} \hline \mathrm{SRC} \\ 123.34 \end{array}$ | 5678 | Definition | For result code definitions, see A12 thrin A15 Card 2 Resinlt Code |
| :---: | :---: | :---: | :---: |
| 0052 | F080 | Bad parity on channel out lines. | C2 A1 |
| 0052 | F084 | Bad parity on channel out lines. | A1 |
| 0052 | F086 | Bad parity on channel out lines. | Ai |
| 0052 | F087 | Bea! parity on chamal out lines. | A1 |
| 0052 | F088 | Bad party on chamad nut lines. | A1 |
| 0052 | F091 | Format error in channel adapter card error reg. | 134135 |
| 0052 | F093 | Format erem in channel adapter card error res. | 135 |
| 0052 | F094 | Format crror in channel adapter card crror reg. | 11 |
| 0052 | F095 | Format error in channel adapter card ertor reg. | 135 |
| 0052 | F096 | Format erron in channel adepier card error reg. | $1113+135$ |
| 0052 | F097 | Format error in channel adapter card error reg. | A1 |
| 0052 | F098 | Format error in channel adapler card error icg. | A1 |
| 0052 | F099 | Format error in channel adapter card error reg. | B4 |
| 0052 | F0A1 | Number of read back bytes do not match number received from cyitem | B5 |
| 0052 | F0A2 | Number of read back bytes do not match number received from sustem. | B3 |
| 0052 | F0A3 | Number of read back bytes do not match number received from syitem. | A2/A3 B4 : 11 D 2 |
| 0052 | F0A4 | Number of read back bytes do not mateh namber received from swiem. | 133135 |


| $\begin{aligned} & \mathrm{SRC} \\ & 12.34 \end{aligned}$ | 5678 | Definition | for result code dedinitions. see AI2 llan A15 (ard 2 <br> Resull Code |
| :---: | :---: | :---: | :---: |
| (0)52 | $100^{15}$ | Number of read back bytes do not matheh number receised from system. | 132135 |
| 0052 | FO, ${ }^{\text {a }} 6$ | Number of read back byte do molt match number received fonin sybum. | A1133135 |
| $00 \% 2$ | 10.17 | Number of read back byes do mot match number received from system. | AI |
| 0052 | 10.18 | Number of real back bytes do not match number received from syatem. | A1133 |
| 00052 | FOAA | Number of read back bytes do nod mateh number received from syblem. | 133132 |
| 002? | 10134 | Number of meal back beter dich not matel number wecived from untern. | 132 \1 |
| (0) i2 | 10136 | Number of rad back byich do nol match number aceived from shtem. | $132131 \therefore 1 \quad 15$ |
| 00.52 | 10137 | Number of read back byers do not match mamber receined from sybtem. | 13211 |
| 0052 | 10138 | Namber of read back byte do now match number received from syatom. | \! |
| 00052 | IOI3, | Number of read back byter do not mateh number received from syintem. | 132 |
| 0053 | HOCl | Format error in adapter cand error reginter. | 135134 |
| 0015 | FOC3 | Format error in addpter cand error register. | A2/13135 |
| 00152 | 10(5) | Fommat cron in adapter cand emon register. | 135 |
| 010.52 | 171)(9) | Format error in adapter cadrd error register. | 134 |
| 0052 | FOCC | Fomat crror in adanter cand ermo register. | 12/13 |
| 0002 | F()I) 1 | Format eror in adapter card error reg. | 135134 |


| $\begin{aligned} & \mathrm{S} 120 \\ & 123.4 \end{aligned}$ | 5678 | Dedinition | For result code delinitions． we Al2 thru A：5（and 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| （0） 22 | JOI）？ | 1－mmat ermo in adapter cand ernor reg． | 12／13135 |
| 0002 | l（0） 5 | I omat ermor in adapter cand ermor reg． | 135 |
| 0105 | 1（0）${ }^{(1)}$ | I mmat crom in adapter cand erand reg． | 134 |
| 0002 | F（1） C | Format crom in idapter card crar reg． | 12／1．3 |
| （1052 | H01：2 | Pantity corn on tape unit bus out． | 13.3 |
| （）0）？ | $1 \% 015$ | Panty corn on tape unit bus out． | $12 / 13133$ |
| 1105 | Forit |  | 131 |
| 0012 | 10105 | Parisy crou an tape unit has out． | 131 |
| 10192 | H（）1：6 | Panls collor an lipe unit bus out． | 133 |
| 010）？ | H（0）：8 | Parity coror onl tape unit hus omt． | 133 （11．12／13133 |
| 01012 | lolic） | Patits cram an tape unit hus rut． | 131 |
| （0） 0 2 | 101） 1 | Parat error un tape urat bus out． | 131 |
| 01052 | Holic | Parily mor an tape mat hus but． | （11 $12 / 131) 4$ |
| （11） 2 ） |  | Parity craor an tape unit bus out． | C I |
| 0052 | 1013 | Parily erom an derice adapler read <br>  | 12／13 |
| 0052 | H01： | Partity ermon on device adapley wad ど口が datal bus． | C 1 |
| 00052 | JIllic | lantly dame on device dapter tead senme data bus． | \2／ 13 B 4 |
| （10）5？ | 1103 | lape unit bue in parity eror． | 12／13 |
| 00.52 | 110C | fape unit bus in patily ermor． | C1 A2／A31）4 |
| 00.52 | 110） | lape unt bus in parity erma． | Cl |


| $\begin{aligned} & \mathbf{4} 126^{\circ} \\ & 12.34 \end{aligned}$ | 5678 | Definition | For result code delinitions, see Al2 thrin A15 Card? <br> Result Code |
| :---: | :---: | :---: | :---: |
| 00532 | l'111 |  - micropencessor ciard. | 135134 |
| 0055 | 1113 | Patly com on B Fan fom microptoceshor card. | A2/A3135 |
| 1192 | 1115 | Parlite coman an bar from matoproceshor calld. | 135 |
| (0)52 | 1119 | Patils ermor on B bun lam microprocemor card. | B4 |
| (10)5! | F110 | Parity come on $B$ has fomm mictoproceronr cand. | 12/13 |
| 10152 | 1120 | Ifed to enable driver for more than Whe lipe unit. | 13 |
| 0015 | 11130 | Ificel to chable drisers for more that <br>  | \2 |
| (11) 52 | 1141 |  | 135134 |
| 01052 | 1142 | Fonmat enor in write catd cimm regivier. | 133 |
| 00022 | 1143 | 1-nmat ernor in write cand ermoregister. | 133135 |
| 00.22 | 1144 | Firmat char in write ciald eron teginter. | 132 |
| 00152 | 1145 | Fommat error in write card error register. | 132135 |
| 00.52 | 1146 | Somat error in write card error register. | 133132 |
| 00052 | 1147 | 1 omat error in wrte catd ermor register. | 132 |
| 00.52 | 1148 | 1 orman erno in write card enot register. | B3 |
| (id) 2 | 1149 | fommat ermer in srite cand ernor reginter. | 134 |
| 00.52 | 1141 | Format eran in write card ernor reginter. | 132 |
| 0032 | F 151 | Wrate cadd crom during read operation. | 134 135 |
| 000.52 | 1152 | Write cand error during read operation. | 133 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see Al2 thru A15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | F153 | Write card error during read operation. | B3 135 |
| 0052 | F156 | Write card error during read operation. | I33 |
| 0052 | F158 | Write card error during read operation. | B3 |
| 0052 | F159 | Write card error during read operation. | 134 |
| 0052 | F15A | Write card error during read operation. | 133 |
| 0052 | F160 | Write data overrun. | C3 D5 |
| 0052 | F162 | Write data overrun. | B3 |
| 0052 | F163 | Write data overrun. | B3 |
| 0052 | F164 | Write data overrun. | A1 |
| 0052 | F166 | Write data overrun. | 133 A1 |
| 0052 | F167 | Write data overrun. | A 1 |
| 0052 | F168 | Write data overrun. | A1 |
| 0052 | F16A | Write data overrun. | B3 |
| 0052 | F170 | Write trigger parity error. | I33 |
| 0052 | F182 | Write data parity error. | B3 |
| 0052 | F184 | Write data parity error. | A1 |
| 0052 | F186 | Write data parity error. | A1 B3 |
| 0052 | F187 | Write data parity error. | A1 |
| 0052 | F188 | Write data parity error. | A1 B3 |
| 0052. | F18A | Write data parity error. | B3 |
| 0052 | F192 | Write card internal error. | B3 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see A12 thru A15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | $1 \cdot 194$ | Write card internal error. | A 1 |
| 0052 | F196 | Write card internal error. | 133 A1 |
| 0052 | 1197 | Write card internal error. | A1 |
| 0052 | F198 | Write card internal error. | 133 A1 |
| 0052 | F19A | Write card internal error. | [33 |
| 0052 | F1A1 | Control unit logic card error. | A4134135 |
| 0052 | H 1 A 2 | Control unit logie card error. | 133 |
| 00.52 | F1A3 | Control unit logic card error. | $135133 \wedge 2 / A 3$ |
| 00.52 | F1A4 | Control unit logic card error. | A1132 |
| 0052 | F1A5 | Control unit logic card error. | 135132 |
| 00.52 | F 1 A 6 | Control unit logic card error. | A1132133 |
| 0052 | F 1 A 7 | Control unit logic catd error. | B4 A4 A1 132 |
| 0052 | F1A8 | Control unit logic card error. | B4A4 A1133 |
| 0052 | F1A9 | Control unit logic card error. | 134 |
| 0052 | FlAA | Control unit logic card error. | B2 B3 |
| 0052 | F 1 AC | Control unit logic card ertor. | A2/A3 |
| 0052 | F1130 | Multitrack error. | HI |
| 0052 | F1B1 | Multitrack error. | B4 |
| 0052 | F1B2 | Multitrack error. | B3 |
| 0052 | F1B3 | Multitrack error. | A2/A3153 |
| 0052 | F1B4 | Multitrack error. | A5 B1 132 |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see A12 thru Al5 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | F1135 | Multitrack crror. | A5 B1 B2 |
| 0052 | F1136 | Multitrack error. | B3 132 |
| 0052 | F1137 | Multitrack crrer. | B1 B2 |
| 0052 | F1138 | Multitrack crror. | B3 |
| 00,52 | F139 | Multitrack error. | B1 134 |
| 00552 | F1BA | Multitrack crror. | 133131132 |
| 0052 | F113C | Multitrack error. | D4 |
| 0052 | F1BE | Multitrack error. | D2 C1 F6 H1 |
| 0052 | F1BF | Multitrack error. | D2 |
| 0052 | 11C2 | Tag lines service in and halt service are active. | B2 |
| 0052 | F1C3 | Tag lines service in and halt service are active. | 132 |
| 0052 | Fic4 | Multitrack error. | A1 |
| 0052 | F1C6 | Multitrack error. | B3 A1 |
| 0052 | F1C7 | Multitrack error. | A1 133 |
| 0052 | F1C8 | Multitrack error. | B3 A1 |
| 0052 | F1CA | Multitrack error. | A1 |
| 0052 | F1D4 | ECC card internal error. | B2 B1 A5 |
| 0052 | F1D5 | ECC card internal error. | B1 A5 B2 |
| 0052 | F1D6 | ECC card internal error. | B2 |
| 0052 | F1D7 | ECC card internal crror. | B2 B1 |
| 0052 | F1D9 | ECC' card internal error. | B1 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see A12 thru A15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | F1DA | ECC card internal error. | B2 B1 |
| 0052 | F1E4 | Skew card internal error. | B1 B2 |
| 0052 | F1E5 | Skew card internal crror. | B1 B3 |
| 0052 | F1E6 | Skew card internal crror. | B2 |
| 0052 | F1E7 | Skew card internal errer. | B1 B2 |
| 0052 | F1E9 | Skew card internal error. | B1 |
| 0052 | F1EA | Skew card internal error. | B1 B2 |
| 0052 | FIT:0 | False end mark. | H1 |
| 0052 | $\mathrm{H1F2}$ | False end mark. | B3 |
| 0052 | F1F3 | False end mark. | A2/A3 B3 |
| 0052 | F1F4 | False end mark. | A5 B1 B2 |
| 0052 | F1F5 | False end mark. | B2 |
| 0052 | F1F6 | False end mark. | B3 B2 |
| 0052 | F1F7 | False end mark. | B1 B2 |
| 0052 | F1F8 | False end mark. | B3 |
| 0052 | F1F9 | False end mark. | B1 |
| 0052 | F1FA | False end mark. | B3 B1 B2 |
| 0052 | F1FC | False end mark. | C1 A2/A3 D4 |
| 0052 | FIFE | False end mark. | F6 |
| 0052 | F200 | Excessive skew check. | H1 |
| 0052 | F202 | Excessive skew check. | B3 |


| $\begin{aligned} & \mathrm{SRC} \\ & 123.4 \end{aligned}$ | 3678 | Deflimition | For result code delimitions, see Al2 hrm Al5 C'ard 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 00.52 | I:203 | Excessise sew check. | 12/13133 |
| 00.5 | F 204 | Excensibe skew check. | A5 |
| 0052 | F205 | Excemine skew check. | A5 31132 |
| 0052 | F206 | Excessine kew check. | 133 132 |
| 00.52 | 1:207 | Excessine skew check. | 131132 |
| 00.52 | $1: 208$ | Excessite skew check. | B3 |
| 00.52 | F209 | Excessine skew check. | B1 |
| 00.52 | F 20 A | Exceniic nkew check. | B3 131 B2 |
| 00.52 | F20C | Excessive skew cheok. | C1 A2/A3 1)4 |
| 0052 | F20E | Execsive bew check. | D) $\mathrm{Cl}^{\text {F6 }} \mathrm{H} 11$ |
| 0052 | F20F | Excessive skew check. | D)2 |
| 0052 | F220 | Entelope luss error. | H1 |
| 00552 | F221 | Enselope loss error. | 134 |
| 00052 | 1222 | Envelope loss crror. | 33 |
| 0052 | F223 | Envelope loss error. | A2/A3 133 |
| 0052 | F224 | Envelope loss error. | A5 B1 B2 |
| 0052 | F225 | Envelope loss error. | A5 B1 B2 |
| 0052 | F226 | Envelope loss crror. | B3 B2 |
| 0052 | F227 | Envelope loss error. | B1 132 |
| 0052 | F228 | Envelope loss crror. | B3 |
| 0052 | H229 | Envelope loss crror. | B11 134 |


| $\begin{aligned} & 12 C^{\circ} \\ & 1236 \end{aligned}$ | 5678 | Detinition | For result code delinitions, see A12 liru A15 Ciard 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0015 | $1 \cdot 221$ | linselope lows error. | 133 131 132 |
| 00052 | 122 C | Emselape low error. | CI $12 / 131) 4$ |
| 0052 | 122 E | 1.medrpe low error. | $1) 2 \mathrm{CIIG}$ |
| 0005 | $1.221^{\circ}$ | 1:ncelope low error. | 1)2 |
| 01052 | [231 | CRC error during a PEram. | 134135 |
| (1)にこ | 1233 | CRe crom during a Ple read. | 135 |
| 1055 | 1.234 | CRC crorr during a Pt, read. | 132 |
| 60.52 | 1235 | CRC error during a PE read. | 132135 |
| 0005 | +236 | CRC corme during a Ple read. | 132 |
| (0) 2 | 1.237 | CRC ermor during a PFeread. | 132 |
| 005 | 1239 | (RC crmaduring a PEEmad. | 134 |
| 005 ? | 123.1 | CRC error during a PEx read. | 132 |
| 0052 | 1240 | CRC ernar whhout a VRC error. | 132 |
| 00.52 | 1.250 | VRC (mmarrectabie) error. | IH1)2 C1 106 |
| 00.52 | $1 \cdot 251$ | VRC (uncorrectable) error. | 134 |
| 0052 | H 252 | VRC (uncorrectable) error. | 133 |
| 0052 | $1 \cdot 253$ | VRC (uncorrcctable) error. | A2/A3133 |
| 00.52 | 1254 | VRC (uncorrectable) error. | A5 B1 132 |
| 00.52 | 1255 | VRC (uncorrectable) eror. | A5 B1 132 |
| 0052 | 12.36 | Y R C (uncorrectabie) error. | 133132 |
| 0052 | 1237 | VRC (uncorrectable) eraor. | 131 132 |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code delanitions, see A12 lhru A15 (iard 2 Resull Coose |
| :---: | :---: | :---: | :---: |
| 0052 | F258 | VRC (uncorrectable) error. | 133 |
| 0052 | $1 / 259$ | VRC (uncorrectable) error. | 131184 |
| 0052 | F25A | VRC (uncorrectablu) error. | $133 \quad 13182$ |
| 00.52 | F 25 C | V'RC (uncorrectable) error. | C 1 A2/A3 D +111 |
| 0052 | F25E | VRC (uncorrectable) error. | 1)2 C1 160 |
| 0052 | 1251 | VRC (nricorrectable) error. | $1) 2111$ (1) ${ }^{\text {1 }}$ |
| 00052 | F271 | Data byte sent to channel is not accepted in time for next byte. | 135134 |
| 00.52 | F273 | Data byte sent to channel is not aceepted in time for next byte. | 135 |
| 0052 | 「274 | Data byte sent to channel is not accepted in time for next byte. | 132 131 |
| 00.52 | F275 | Data byte sent to channel is not accepted in time for nex by te. | 131132135 |
| 000.52 | 1276 | Data byte sent to channet is not accepted in time for next byte. | 132 Al |
| 00.5 | 1277 | Data byte sent to channel is not accepted in time for next byie. | 13213184 |
| 0052 | F278 | Data byte sent to channel is not accepted in time for next byte. | AI |
| 00.52 | F279 | Data byte sent to channel is not aceepted in time for next byte. | 13184 |
| 00.52 | F27A | Data byte sent to channet is not accepted in time for next byte. | 132 B1 |
| 0052 | F2BI | Format error in microcode storage card crror reg. | 135 B4 |
| 0052 | F2B3 | Format error in microcode storage card crro" reg. | 135 |


| $\begin{aligned} & 318 \mathrm{C} \\ & 1234 \end{aligned}$ | 5678 | Delinition | For result code definitions, see Al2 thru A15 Card 2. <br> Result code |
| :---: | :---: | :---: | :---: |
| (0)52 | 12135 | Format erom in microconde storage caral ctar reg. | 135 |
| $0: 152$ | 1.2139 | Fommat ermor in manomede sharage catad error reg. | 13. 4 |
| $005: 1$ | 122 l | Internal in microcode sharage cand. | 135 |
| (0) 32 | 121)1 | Bus 16 lamster parity erome (tranofer 82 ) | 135134 |
| (0) 5 ? | $1: 21) 3$ |  | 12/13 135 |
| 00.52 | 1:21)5 | Bus in tramber parity erron (1ansior \$2). | 135 |
| 0057 | F2139 | Bus in tramier parity error (tramser 8 ? ) | 134 |
| 00152 | 12 C | Bus in Hamser parity eror (1anafer 82). | CI $12 / 131) 4$ |
| 00052 | 1:21) | Bus in trameter parity crom (tamefer 82 ) | C1 |
| 00.52 | $1 \cdot 2 \mathrm{El}$ | $\mathrm{K} W$ in ramser parity error (tramfer (S) | 135134 |
| 00.52 | $1 \times 212$ | R W in ramster parity error (ranster (SX) | 133 |
| 00.52 | 1:21:3 | R W in transter party error (transfor 88). | 132 135 |
| 00.52 | $1 \times 2 \mathrm{C}$, | R W in tramser parity cror (transfer 88). | 132 |
| 00.52 | 1.215 | R W in transer parnty error (transer 88). | 132 |
| 00052 | 12 L 6 | Bus in transfer parity error (tansfer \$2). | 132133 |
| (0)52 | $1 \cdot 21: 7$ | R, W in transfer parity error (transfer 88). | 132 |
| 0052 | F2L8 | $R$ W in transfer parity error (transfer 88). | 133 |
| 0052 | 12 L 9 | R/W i: Uransfer parity error (transfer 881. | 134 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see A12 thrm A15 Card 2 Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | r2EA | R/W in transfer parity error (transter 88). | 132 B3 |
| 0052 | F2F1 | Clock and detection transfer parity error (transfer 90). | 135 134 |
| 0052 | F2F3 | Clock and detection iransfer parity error (transfer 90). | B5 |
| 0052 | F2F5 | Clock and detection transier parity error (transfer 90). | A5 135 |
| 0052 | F2F9 | Clock and detection transfer parity error (transfer 90). | B4 |
| 0052 | F301 | Channel bus out transfer parity error (transfer A01. | A4 135 |
| 0052 | F303 | Channel bus out transfer parity error (trinsfcr A0). | 135 |
| 0052 | F304 | Channel bus out transfer parity error (transfer A0). | A 1 |
| 0052 | F305 | Channel bus out transfeı parity error (transfer A0). | 135 |
| 0052 | F306 | Channel bus out transfer parity error (transfer A0). | A1 |
| 0052 | F307 | Channe! bus out transfer parity erfor (transfer A0). | A1 A4 |
| 0052 | F308 | Channel bus out transfer parity error (transfer A0). | A1 A4 |
| 0052 | F310 | Data check with write sense check. | H1 D2 D6 Cl |
| 0052 | F320 | Data check along with driver control failure. | D1 C5 C4 Cl |
| 0052 | F330 | Data check with velocity check. | D1 C5 C4 Cl |
| 0052 | F340 | Tape unit reject along with write sense check. | H1 D2 C1 D6 |


| $\begin{aligned} & \mathrm{SRC} \\ & 12.34 \end{aligned}$ | 5678 | Detinition | For result code definitions, see A12 linu Al5 Card 2 <br> Resuit Code |
| :---: | :---: | :---: | :---: |
| 0052 | 13350 | Tape unit reject with driver control tailure. | I) $1 \mathrm{C} 5 \mathrm{C4} \mathrm{Cl}$ |
| 0052 | 1360 | Tape unit reject with velocity check. | D) C 5 C 4 Cl |
| 00.52 | 1-370 | Subuybtem diagnostic error. | D2 C5 C4 |
| 0052 | $1 ; 371$ | Subsybte $n$ diagnoutic error. | 135 134 A4 |
| (0)52 | $1 \cdot 372$ | Subuyblent dixgmostic error. | 133 |
| 0052 | 11373 | Subyyntem diagnostic error. | A2/A3 [33 135 |
| (005 ? | 13374 | Subuybtem diagnostic error. | A5 B1 B2 A1 |
| 00.52 | 1375 | Subsylem diagnostic error. | A5 B1 132 135 |
| 00.52 | $1 \div 376$ | Subsyctem diagnostic error. | P3 132 A1 |
| 00.52 | 13377 | Subsystem diagnostic error. | 13132 A1 A4 |
| 00.52 | 1378 | Subuyntem diagnostic error. | B3 A1 A4 |
| 0052 | 1.370 | Subsystem diagnostic error. | A4 B4 |
| 00.52 | $1 \cdot 37.1$ | Subuyctem? diagnostic error. | B3 B1 B2 |
| 0052 | 1.37C | Subsystem diagnostic error. | Cl A2/A3 D4 |
| 0052 | F37E | Subsystem diagnostic error. | D2 C1 F6 |
| 0052 | F375 | Subsystem diagnostic error. | D2 |
| 0052 | F410 | Nonteportable error detected by microcode. | B4 B5 Al A4 |
| 0052 | I. 420 | Internal nonreportable crror. | B4 |
| 0052 | F430 | Nonreportable error in microcode storage card. | B4 B2 Bí A5 |
| 0052 | I 440 | External nonreportable error. | B4 B5 |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For resull code definitions, see A12 llom Al5 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| (0)52 | $1 \cdot 450$ | Bant parity from patede modules. | 135134 |
| 00.52 | [i460 | Stamace internal nonreportable crome | 134135 A 112 |
| 00.2 | I'470 | Storage evternal nonreportable error. | 134135 |
| 00.52 | 1480 | Channel card nonreportable error (internal). | 113135 |
| 00.22 | F490 | Chamel came evernal nonmportable ertor. | 134135113 112 |
| 00.52 | 1540 | Interface lag ertar with me supporting inlonmation. | 134135113 |
| 00.52 | F4130) | Internal nonreportable error. | 134135 H12 |
| 00.52 | $\mathrm{V}+\mathrm{C} 0$ | Internal monreportable error. | 134135112 |
| 00.52 | F4D) | Internal monteportable corm: | A1B4 A4 |
| 00.52 | $1-4 E 0$ | Examand noncepatahle coron. | B4 $\ 1$ |
| 00.52 | F-4F0 | Interface tag ermor. | A1 C3 13+133 |
| 00.52 | 1500 | Hiferface tag ermor. | A1C3 134133 |
| 0052 | 1510 | Intertace tag error-op in still active. | A1 C3 B4133 |
| 0052 | F520 | Interface tag error - selective rene active. | A1C3 134133 |
| 00.52 | F530 | Nonreportable error. | A4B4 B5 |
| 0052 | 15540 | Nonreportable error. | 134135 |
| 0052 | F781 | Sense data indicates that an attached unit is not attached. | 134135 |
| 0052 | F783 | Sonse data indicates that an attached unit is not attached. | A2/A3 134 |
| 0052 | 1.785 | Sence data indicates that an attached unit is not attached. | 135 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \quad 5678 \end{aligned}$ | Definition | For result code delinitions, see A12 thro Al5 Card 2 <br> Result Code |
| :---: | :---: | :---: |
| $0052 \quad 1789$ | Sense data indicates that an attached unit is not attached. | 134 |
| 0052 F78C | Sense data indicates that an attached unit is not attached. | $C 114 \pm 12 / 134$ |
| 0052 F78E | Sense data indicates that an attached unit is not attached. | CI |
| 0052 F790 | Tape unit dropped ready. | E2 I2 |
| 0052 F791 | Tipe unit dropped ready. | 134135 |
| 0052 F793 | Tape unit dropped ready. | A2/A3134 |
| 0052 F799 | Tape unit dropped ready. | 134 |
| 0052 F79C | Tape unit dropped ready. | C1 A2/A3 D4 |
| 0052 F79E | Tape unit dropped ready. | CI |
| 0052 F 7 A 0 | Invalid command from channel or DSE not chained. | C2C3 1)5 |
| 0052 F7A1 | Invalid command from channel or DSE not chained. | B5 134 |
| 0052 F7A3 | Invalid command from channel or DSE not chained. | B: A2/A3 |
| 0052 F7A4 | Invalid command from channel or DSE not chained. | A1 135 |
| 0052 F7A5 | Invalid command from channcl or DSE not chained. | 135 |
| $00,52 \quad \mathbf{F 7 A 6}$ | Invalid command from channel or DSE not chained. | Al |
| 0052 F7A7 | Invalid command from channel or DSE not chained. | Al |
| 0052 F7A8 | Invalid command from channel or DSE not chained. | A1 |


| $\begin{array}{\|l\|l\|} \hline \text { SRC } \\ 1234 \end{array}$ | 5678 | Definition | For result code definitions, see A12 Hirli A15 Card 2 <br> Resull C'ode |
| :---: | :---: | :---: | :---: |
| 0052 | F7A9 | Invald command from chammel or DSE not chained. | 134 |
| 0052 | F7AC | Invalid command from chamel of DSE not chained. | A2/A3 A1 B5 134 |
| 0052 | F7130 | Command out aetive during service in service out. | A1 C2/C3 D5 |
| 0052 | F7134 | Command out active during service in service our. | AI |
| 0052 | 177136 | Command wat ative during service in service out. | A1 |
| 0052 | 177137 | Command out active during hervice in service out. | A 1 |
| 005: | F7138 | Command out active during service in service out. | A 1 |
| $00 \leq 2$ | F7C0 | No data relieved from channel during service in service out. | A1 133 C3 D5 |
| 0052 | 17C2 | No data relieved from channel during serviec in/service out. | B3 C3 D5 |
| 0052 | F7C4 | No data relieved from channel during service in/service out. | A1 |
| 0052 | F7C6 | No data relieved from channel during service in/service out. | B3 |
| 0052 | F7C7 | No data relieved from channel during service in /service out. | A1 |
| 0052 | 117 C 8 | No data relieved from channel during service in /service out. | B3 |
| 0052 | F7CA | No data relieved from channel during service in/service out. | B3 |
| 0052 | F7D0 | ARA ID burst not detected during GCR read. | H1 D2 C1 D6 |
| 0052 | F7D1 | ARA ID burst not detected during GCR read. | B4 B5 |


| $\begin{aligned} & \mathrm{SRC} \\ & 12.4 \end{aligned}$ | 5678 | Definition | For result code deflinitions， see A12 limi Al5 Card 2 Resull Code |
| :---: | :---: | :---: | :---: |
| 0052 | F7D3 | ARA ID burst not detected during GCR read． | A2／A3 |
| （0）5， | ドロ）4 | ARA II burst not detected during GCR read． | 111 A5 BI |
| （1）52 | F71）5 | ARA ID burs not detected during GCR read． | A5 131 |
| 0052 | F71）7 | ARA ID burst not detected during GCR read． | B1 |
| 01052 | F71）9 | ARA ID bumb not detected during GCR read． | B1 134 |
| （1）052 | F71） | ARA ID burst at detected during GCR read． | 131 |
| （）052 | F71）C | ARA ID burst not detected during GCR read． | C1 A2／A3 1） 4 |
| 1905 | 17\％ | ARA 1 D burst not detected during GCR read． | 1）2（1） 4 |
| 0052 | 177） | ARA ID burst not detected during GCR reald． | D2 |
| 0052 | F7E0 | Gap control from tape unit not activated during write． | C4 |
| 0055 | F7E1 | Gap control from tape unit not activated during write． | 134135 |
| 0052 | F7E3 | Gap control from tape unit not activated during write． | A2／A3 B5 |
| 0052 | F7E9 | Gap control from tape unit not activated during write． | 34 |
| 005？ | F7EC | Gap control from tape unit not activated during write． | A2／A3 D4 |
| 0052 | F820 | Overrun detected without the any error line active． | C3 D5 |
| 0052 | F824 | Overrun detected without the any error line active． | A1 |

## 3203 SRC Table ( $\mathrm{OU}=40,41$ )

Use pouition $5,6,7,8$, 1 the SRC to find the Result Cole.
Use the Renult Code and Probability to determine the action or parts required.

| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see C2 thrit C4 Card I <br> Resull Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| ()0) $\mathrm{x} \times$ | 0000 | Pawer on off falifure | $\begin{aligned} & \text { MK60 CSio PR10 } \\ & \text { PB05 PD05 CF } 05 \\ & \text { CR04 S201 } \end{aligned}$ |
| 004 x | 3203 | Channel crrors | S399 |
| 00 xx | 3205 | Command reject (IOC) | $\begin{aligned} & \text { S530 S325 S015 J } 110 \\ & \text { A009 S605 E005 S201 } \end{aligned}$ |
| (0) xX | 4020 | Adapter CSA parity error | J145 A040 CW10 E005 |
| 00 xx | 4034 | Intalid disconnect | S340 S040 J120 |
| 00 xx | 4038 | Adapter deteet invalid OU | J160S330 S010 |
| 00 xx | 4040 | IOC internal parity error | J160 A030 CW10 |
| 00xx | 4080 | IOC DBI parity error | A060, J140 |
| 00.x | 4101 | $\mathrm{FOB}^{\text {time-out }}$ | $\begin{aligned} & \mathbf{J 1 2 5 ~ A 0 2 0 ~ S 3 2 0 ~ S 5 1 5 ~}^{\text {MJ }} 10 \text { S109 S201 } \\ & \hline \end{aligned}$ |
| 00:x | 4102 | Invalid adapter response | J130S330 S025 A015 |
| 00 x | 4103 | Error on startup | $\begin{aligned} & \mathbf{J} 145 \mathrm{~A} 025 \mathrm{~S} 320 \mathrm{CW} 05 \\ & \mathrm{~S} 005 \\ & \hline \end{aligned}$ |
| $00 \times \mathrm{x}$ | 4110 | Error un read sense | $\begin{aligned} & \text { CE35 MI20 A015 } \\ & \text { MJ15 CD05 J105 } \\ & \text { E005 } \end{aligned}$ |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see C2 thru C4 Card ) <br> Resull Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 00xx | 4111 | Error on read IOC sense, read line total, or read error $\log \mathrm{FOB}$ | $\begin{aligned} & \mathbf{J 1 4 5} \mathbf{A 0} 05 \mathbf{S 3 2 0}, \\ & \text { CW0.5 E005 } \end{aligned}$ |
| 00xx | 4450 | Invalid SCS | S785 A005 J J 05 S605 |
| 00xx | 4452 | FOB busy (invalid channel sequence) | J135 S035 S330 |
| 00xx | 5437 | Intervention required - forms jam | MH99 |
| 00xx | 7004 | Interface check | MI30 CE25 A020 MJIo LO08 CD05 RE01 Slol |
| 00xx | 7006 | Incorrect length | A030 MI30 J 120 CE10 CD05 E005 |
| 00xx | 7010 | 1.6-ms timer failure | J150 S335 A0i5 |
| 00xx | 7014 | Command reject from 3203-5 | $\text { A025 MI25 CE20 J } 120$ <br> CD05 E005 |
| 00xx | 7030 | Intervention required - stacker full/jam | SF98 MJ02 |
| 00xx | 7101 | Equipment check - miscellaneous | MA70 MJ30 |
| 00xx | 7102 | Equipment check - hammer fire or coil protect | MB70 MJ30 |
| 00xx | 7103 | Lquipment check - controller check | MC60 MJ 20 E020 |
| 00xx | 7104 | Data check - unprintable character | $\begin{aligned} & \mathrm{S} 790 \mathrm{MID} 06 \mathrm{MJ} \mathrm{C}_{2} \\ & \mathrm{~S}_{602} \end{aligned}$ |
| 00xx | 7106 | Data cheek - line position check | ME70 MJ30 |
| 00xx | 7108 | Buffer parity check - PLB | MF70 MJ30 |
| 00xx | 7109 | Buffer parity check - UCSB | MG70 MJ30 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see C'2 thru C4 Card 1 <br> Result Codes and IProbability \% |
| :---: | :---: | :---: | :---: |
| 00xx | 7120 | Load check - load FCB command | A025 MI25 J120 Cli20 CD05 E005 |
| 00xx | 7121 | Load check-write (ICSB command (invalid train image) | A025 MI25 J120 CE20 CD05 E005 |
| 00xx | 7122 | Bus out check | $\begin{aligned} & \text { M130 CE25 A020 J110 } \\ & \text { E010 CD } \end{aligned}$ |
| 00 xx | 9000 | Operation program error | 2199 |
| 00xx | 0998 | Channel busy | 7199 |
| $00 \mathrm{xx}$ | All <br> other <br> codes | Not a valid error code | /999 |

## 3203 RC Table

| Result (ode | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| \0 | Printer adapter card at location 01AAI P2 or () 1 A 132 M 2 | 8328100 |
| (1) | If the printer adapter is at 01AAI P2, Matcable at 01AA1 P2 X (Bus) or 01AAI P2 Y (Tag) <br> If the printer alapter is at 01 AB 2 M 2 , matcable at 0) AB2 M2 X (Bus) or (0) AB2 M2 Y (Tag) | $\begin{aligned} & 2549917 \\ & 2549916 \end{aligned}$ |
| CE | External Bus of Tay cable | 5353920 |
| CI | Cable O1BAI fit in system power controller |  |
| CR | If 3203 O(140 is failing, power control cable from relay hoard to system tailgate 01DA3 A3 <br> If 3203 OU4 is failing, power control cable from relay board to system taigate 01DA3 B3 |  |
| CS | Power control cable from 3203 to system tailgate at 01 DA 3 A 3 or 01 DA 3 B 3 | $53511 \% 8$ |
| CW | Crossover at location 01AAI P2 W or 01AB2 M2 W | 1794410 |
| E) | Electrostatic discharge |  |
| J 1 | Printer athachment lOC card at location 01 AAI Q2 or 01AB2 L2 | 4361306 |
| M | 3203-5 MLM page 0-220 entry point 3 (mise equipment check) |  |
| M13 | 3203-5 MLM page 0-220 entry point 3 <br> (hammer fire/coil check) |  |
| MC | 3203-5 MLM page 0-220 entry point 3 (controller chack) |  |


| Result Code | Definition | $P / N$ |
| :---: | :---: | :---: |
| M ) | 3203-5 MLM page 0-220 entry point 3 (unprintable char check) |  |
| ME | 3203-5 MLM page 0-220 entry point 3 (line position check) |  |
| $\mathrm{MH}^{\text {F }}$ | 3203-5 MLM page ()-220 entry point 3 (PLB parity check) |  |
| MG | 3203-5 MLM page 0-220 entry point 3 <br> (UCSB parity check) |  |
| MII | 3203-5 MLM page 0-220 entry point 15 (forms jam) |  |
| M I | 3203-5 MLM page 0-220 entry point 9 (interface problems between system and printer) |  |
| MJ J | 3203-5 MLM page 0-220 entry point 3 (mise 3203-5 problem) |  |
| MK | 3203-5 MLM page (0-220 entry point 3 <br> (320.3-5 power problems) |  |
| PB | Feature control card 01BA1 B4 in system power controller. | 4176357 |
| P1) | Base logic card 01BA! D2 in system power controller | 4176537 |
| PR | Feature control relay board in system power controller | 5564869 |
| RE | Error caused by the other 3203/4245 Printer |  |
| S0 | System problem |  |
| S 1 | System 1.63-millisecond clock |  |
| S2 | System control adapter (SCA) (0IAAI C2) and SCA interface lines | 4202410 |
| S3 | System channel |  |
| S5 | System VMC |  |

## 3203 RC Table

| Result <br> Code | Definition | $\mathrm{P} / \mathrm{N}$ |
| :--- | :--- | :--- |
| S6 | CPF or user operating system |  |
| S7 | Customer application program or data |  |
| SF | Stacker full (not an error) |  |
| Z1 | Goto system hard-copy MAP 0500, entry <br> point E |  |
| Z9 | Not valid error code. Inform your <br> programming support structure. |  |

## 4245 SRC Table $(\mathrm{OU}=40,41)$

Use position 5, 6, 7, 8 of the SRC to find the Result Code.
Use the Result Code and Probability to determine the action or parts required.

| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see C8 thru C10 Card 1 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 00xx | 0000 | Power on'olf failure | NK60 CS 10 PR 10 PB05 PD 05 CF05 CR04 S201 |
| 000x | 3203 | Channel errors | S399 |
| 00xx | 3205 | Command reject (IOC) | $\begin{aligned} & \text { S530 S325 S015 J110 } \\ & \text { A009 S605 E005 S201 } \end{aligned}$ |
| 00.x. | 4020 | Aciapter CSA parity error | J145 A040 CW10 E005 |
| 00xx | 4034 | Invalid disconnect | S340 S040 J 120 |
| $00 \times \mathrm{y}$ | 4038 | Adapter detect invalid OU | J160 S330 S010 |
| 000xx | 4040 | IOC internal parity error | J160 A030 CW 10 |
| 00xx | 4080 | IOC DBI parity error | A060 J140 |
| 00xx | 4101 | FOB time-out | $\begin{array}{\|l} \hline \mathbf{J} 125 \text { A020 S320 S5ı5 } \\ \text { NJ10 S109 S201 } \\ \hline \end{array}$ |
| 00xx | 4102 | Invalid adapter response | J130 S330 S025 A015 |
| 00xx | 4103 | Error on startup | $\begin{aligned} & \text { J145 A025 S320 CW05 } \\ & \text { S005 } \end{aligned}$ |
| 00xx | 4110 | Error on read sense | $\begin{aligned} & \text { CE30 WT20 NI } 15 \\ & \text { A010 NJ } 10 \text { CD05 J105 } \\ & \text { E005 } \\ & \hline \end{aligned}$ |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Detilition | For result code definitions, see C8 thulu C'10 C'ard I <br> Result Codes and I'robability \% |
| :---: | :---: | :---: | :---: |
| $00 \times 1$ | 4111 | Error on read 10 C sense, read line total, or read erom $\log \mathrm{FOB}$ | $\begin{aligned} & \mathrm{J} 1+5 \mathrm{~A} 025 \mathrm{~S} 320 \\ & \text { CW05 E005 } \end{aligned}$ |
| $000 \times x$ | 4450 | Invalid SCS | S785 A005 J105 S605 |
| 00 xx | 4452 | FOB busy (invalid chamel sequence) | J135 S035S330 |
| 00 xx | 5436 | Intervention requited - ribhon check | NR(9) |
| 000 xx | 5437 | Intersention required forms jam | NH99 |
| 00 xx | 7004 | Interface check | WT25 FC20 N115 <br> CEIs A010 NJon EOO <br> CDO3 RE01 Slol |
| 000 x | 7006 | Incorrect length | $\begin{aligned} & \mathrm{A} 030 \mathrm{WT} 20 \mathrm{IC}(0 \\ & \text { NHo J J } 10 \mathrm{CE} 05 \mathrm{CD} 03 \\ & \mathrm{EO} 02 \end{aligned}$ |
| 000 xx | 7010 | 1.6-ms timer Pailure | J150 S335 A015 |
| 000 x | 7014 | Command rejeet from 424.5 | $\begin{aligned} & \mathrm{A} 030 \text { WT20 FC20 } \\ & \text { NIı } \mathrm{CE} 10 \mathrm{~J} 105 \mathrm{CD} 03 \\ & \text { E002 } \end{aligned}$ |
| $00.8 x$ | 7030 | Intervention required-stacker full jam | SF98 NJ(0) |
| $00 \times \mathrm{x}$ | 7031 | Near end of Platen Protect Tape | TN99 |
| 00 xx | 7101 | Equipment check-miscellancous | NA70 NJ30 |
| 00 xx | 7102 | Equipment check - hammer fire or coil protect | NB70 NJ30 |
| 00xx | 7103 | Equipment cheek - controller check | NC60 NJ20 E020 |
| 00 xx | 7104 | Data cheek - unprintable character | S760 ND 25 NJ 10 S605 |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code delinitions, see C8 thru C10 Card 1 <br> Result Codes and P'robability \% |
| :---: | :---: | :---: | :---: |
| 00xx | 7106 | Data check - line position check | NE.70 NJ30 |
| 00xx | 7108 | Buffer parity check - PLB | NT50 FC30 N.J20 |
| 00xx | 7110 | End of platen protect tape | T'E99 |
| 00xx | 7111 | Equipment check - printer channel adapter | $\begin{array}{\|l} \hline \text { FC50 NL20 WTO5 } \\ \text { NI05 CE05 A006 J104 } \\ \text { E003 CD02 } \\ \hline \end{array}$ |
| 00xx | 7112 | Equipment check - wrong or failing print band | NP99 |
| 00xx | 7120 | Load check - load FCB command | $\begin{aligned} & \text { A030 WTR20 N120 } \\ & \text { FC10 J110 CE05 } \\ & \text { CD033 E002 } \end{aligned}$ |
| 00xx | 7122 | Bus out check | $\begin{aligned} & \text { WT } 30 \text { NI20 FC20 } \\ & \text { CE10 A010 J105 E003 } \\ & \text { CD02 } \end{aligned}$ |
| 00xx | 9000 | Operation program error | Z199 |
| 00xx | 9998 | Channel busy | Z199 |
| 00xx | All <br> other codes | Not a valid error code | Z999 |

## 4245 RC Table

| Result Code | Definition | P/N |
| :---: | :---: | :---: |
| A0 | Printer adapter card at location 01AAI P2 or 01AB2 M2 | 8328100 |
| CD | If the Printer adapter is at 01AAI P2, flateable at 01AA1 P2 X (Bus) or 01AA1 P2 Y (Tag) If the Printer adapter is at 01 AB 2 M 2 , flateable at 01 AB 2 M 2 X (Bus) or 01 AB 2 M 2 Y (Tag) | $\begin{array}{\|l\|l} 2549917 \\ 2549916 \end{array}$ |
| CE | External Bus or Tag eable | 5353920 |
| CF | Cable 01BAI F4 in system power controller |  |
| CR | If 4245 OU 40 is failing, power control cable from relay board to system tailgate 01DA3 A3 If 4245 OU41 is failing, power control cable from relay board to system tailgate 0IDA3 B3 |  |
| CS | Power control cable from 4245 to system tailgate at 01 DA 3 A 3 or 01 DA 3 B 3 | 5351178 |
| CW | Crossover at location 01AA1 P2 W or 01AB2 M2 W | 1794410 |
| E0 | Electrostatic discharge |  |
| FC | 4245 Frinter Channel Adapter Card 01AAI B2 in the printer is failing. |  |
| J1 | Printer attachment IOC card at location 01AAI Q2 or 01 AB2 L2 | 4361306 |
| NA | 4245 MIM MAP 1000 (misc equipment cherk) |  |
| NB | 4245 MIM MAP 1000 (hammer fire/coil check) |  |
| NC | 4245 MIM MAP 1000 (,mintroller check) |  |
| ND | 4245 MIM MAP 1000 (unprintable character check) |  |
| NE | 4245 MIM MAP 1000 (line position check) |  |
| NF | 4245 MIM MAP 1000 (PLB parity check) |  |

4245 RC Table

| Resill Code | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| NII | 4245 MlM MAP 1000 (forms jam) |  |
| NI | 424.5 MIM MAP 1000 (interface problem between system and printer) |  |
| N.J | 424.5 MIM MAP 1000 (mise 4245 problem) |  |
| NK | $4245 \mathrm{MIM} \mathrm{MAP1000} \mathrm{(4245} \mathrm{power} \mathrm{problem)}$ |  |
| NL. | 4245 MIM MAP $1000(42.45$ Printer Chanmel Adapter lature) |  |
| NP | 4245 MiM MAP 1000 ( 42.45 failing prime band) |  |
| NR | 4245 MIM NAAP 1000 ( 42.45 ribbon check) |  |
| P13 | Feature comtrol card 0! BAI 134 in syhtem power controller. | 4176357 |
| PI) | Base logic cand 013A1 D2 in system power controlior | 4176537 |
| PR | Feature control relay hoard in system power contruller | 5564869 |
| RE | Error calused by the other 32034245 Printer |  |
| S0 | Syetem problem |  |
| S1 | System 1.6. ${ }^{2}$-millisecond clock |  |
| S2 | System CO, adapter (SCA) (01AAI C2) and SCA interlace line. | 4202410 |
| S3 | System channel |  |
| S5 | Systemi VMC |  |
| S6 | CPF or user operating system |  |
| S7 | Customer application program or data |  |
| SF | Stacker full (not an error) |  |
| TE | The 4245 Platen Protection Tape has reached the end and must be replaced by a Scrvice Representitive. |  |
| TN | The 4245 Platen Protection Tape is near the end. Five hours of print time is left before an error occurs. Have the Platen Protection Tape replaced by a service representitive. |  |


| Result Code | Definition | $P / N$ |
| :---: | :---: | :---: |
| W/I | Run the system's selectable 3203/4245 cable wrap test for the failing printer. MAP 6122 |  |
| / 11 | Co to system hard-copy MAP 0500, enery point E |  |
| 79 | Not valid error coole. Inform your programming support structure. |  |

$\mathrm{Cl} 2^{1} \operatorname{Card} 1$

## 3430/3422 SRC Table

There alle two section for 3.431 and 3.122 Systern Relenence (inder




C13 Card

3430 SRC 'Table

## 3430 SRC Table

Une poxition 5, 6,7,8 af the SRC to lind the Result Code.
Use the Result Code to determine the action required.

| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Detinition | For result code delimitions, see Al2 Hiru Al5 C'ird 2 <br> Resull Code |
| :---: | :---: | :---: | :---: |
| 00.52 | (0)00) | Error oceurred during power on off. | N(0) |
| 00.52 | 1000 | Post event. | P140 P340 / 320 |
| 00.52 | 1200 | Unsuccesbiul pust erent. | R150 U450 |
| 0052 | 1300 | Invalid disconnect. | S $150 / .250$ |
| 00.52 | 2000) | FOB error. | S199 |
| 0052 | 2300 | Error code indicates sense reyuired. | S(\%90 P110 |
| 00.25 | 2500 | Tape unit busy. | S545P125 P325 S 105 |
| 0)0.52 | 3000) | Select in error. | N0.60 $\mathbf{Q}^{(030} \mathbf{Q 1 3 0}^{0}$ |
| 00.52 | 3200 | Disconnect in error. | Q299 |
| 0052 | 3300 | Interface contro! check. | P235S530 P120 P315 |
| 0052 | 3400 | Adapter bus in check. | R635R825 R925R710 |
| 0052 | 3500 | Adapter detectesi bus out check. | P140 P340 2,320 |
| 0052 | 3600 | Unexpected status. | P 160 S 530 P 310 |
| 0052 | 3700 | Expand data crror. | S370 P330 |
| 0052 | 3800 | HDC card failure. | P370 2330 |
| 0052 | 3900 | Interface error. | /2340 P130 P330 |
| 0052 | 3 A 00 | HDC card error. | P370 2330 |


| $\begin{gathered} \text { SRC } \\ 1234 \end{gathered}$ | 5678 | Definition | For result code definitions, see A12 thru A15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | $3 \mathrm{B00}$ | IOM error. DSTAT byte 4 is hex OA, OB, $0 \mathrm{C}, 0 \mathrm{D}$ ). <br> Hardware errer: DSTAT byte 4 is hex 20 . 21,22 , or hex 23. <br> Unexpected error: DSTAT byte 4 is hex 25 or 26. <br> Adapter error: DSTAİ byte 4 is hex 15 , 27 or 26 . | $\begin{aligned} & \text { S199 } \\ & \text { P399 } \\ & \text { P399 } \\ & \text { P199 } \end{aligned}$ |
| 0052 | $3 \mathrm{C00}$ | Block sipe not compatible. | P440 P130 P330 |
| 0052 | 3D00 | Interface error. | P140 P340 2320 |
| 0052 | 3E00 | IOM error. | S099 |
| 0052 | 4000 | Channel response register error. | P190 Z210 |
| 0052 | 4100 | Channel overrun. | P145 Z230 R015 P310 |
| 0052 | 5000 | Device problems. | S599 |
| 0052 | 5100 | Subsystem detected bus ent check. | R930 R130 R620 R820 |
| 0052 | 5120 | Tape control unit error before this command. | R199 |
| 0052 | 5130 | Temporary wad failure or tape unit failure. | R199 |
| 0052 | 5200 | Intervention required. | R199 |
| 0052 | 5300 | Command reject. | R199 |
| 0052 | 5400 | Subsysiem detected overrun. | R130 R930 R220 R420 |
| $0052$ | 5500 thru 5E00 | Device problems. | R175 P115 P310 |


| SRC |  |  | For result code definitions, <br> see A12 lhru A15 Card 2 <br> 1234 |
| :--- | :--- | :--- | :--- |
| 06678 | Definition | Result Code |  |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Detinition | For result code definitions, see A12 thru Al5 Card 2 Result Code |
| :---: | :---: | :---: | :---: |
| 00.2 | F01A | Any error line active without error data. | 132 133 A 4 Al |
| 0052 | F01C | Any error line active without error data. | A2/A3 A4 A1 B2 |
| 00.52 | F021 | Any error line active-microcode cannot reset it. | A4 134135 |
| 0052 | F022 | Any error line active-microcode cannot reset it. | B3 |
| $005 ?$ | F023 | Any error line active-microcode cannot reset it. | B5 B3 A2/A3 |
| 0052 | 1024 | Any error line active-microcode cannot rese it. | A! 132 |
| 0052 | F025 | Any error line active-microcode cannot reset it. | 135132 |
| 0052 | F026 | Any error line active-microcole cannot rese it. | A1 B2 B3 |
| 0052 | F(027 | Any error line active-microcode cannot resct it. | A4 A1 B2 |
| 0052 | F028 | Any error line active-microcode cannot reset it. | A4 A1 B3 |
| 0052 | F029 | Any error line active-microcode cannot resct it. | B4 |
| 0052 | F02A | Any crror line active-microcode cannot reset it. | B2 B3 |
| 0052 | F02C | Any error line active-microcode cannot reset it. | A2/A3 |
| 0052 | F030 | Normal operation code. | J2 |
| 0052 | F040 | No action is required. | J2 |
| 0052 | F051 | Tape unit interrupt active and cannot be resct. | B5 B4 |
| 0052 | F053 | Tape unit interrupt active and cannot be resct. | A2/A3 D2 C1 D6 |


| $\begin{aligned} & \text { SRC } \\ & 12.34 \end{aligned}$ | 5678 | Decrinition | For result code definitions, see A12 thru A15 Card 2 Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | F055 | Tape unit interrupt active and cannot be reset. | 135 |
| 0052 | F059 | Tape unit interrupt active and cannot be reset. | 34 |
| 0052 | 105C | Tape unit interrupt active and cannot be reset. | D4 |
| 0052 | F0SE | Tape unit intereupt active and cannot be reset. | D2 C1 |
| 0052 | F05 ${ }^{\text {a }}$ | Tape unit interrupt active and cannot be reset. | D2 |
| 0052 | F064 | Bus in/Bus out parity error on initial selection. | A1 B2 |
| 0052 | F065 | Bus in/Bus out parity error on initial selection. | A1 B2 |
| 0052 | F066 | Bus in/Bus out parity error on initial selection. | A1 B2 |
| 005? | F067 | Bus in/Bus out parity error on initial selection. | A1 12 |
| 0052 | F068 | Bus in/Bus out parity error on initial selection. | A1 B2 |
| 0052 | F06A | Bus in/Bus ou: parity error on initial selection. | B.) A1 |
| 0052 | F074 | Parity arror on read. | A. 1 B2 |
| 0052 | F075 | Parity error on read. | A1 B2 |
| 0052 | F076 | Parity error on read. | A1 B2 |
| 0052 | F077 | Parity crror on read. | A1 82 |
| 0052 | F078 | Parity error on read. | A1 |
| 0052 | F07A | Parity error on read. | B2. |


| Result Code | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| F0 | Forms or forms path | See Printer M1M parts catalog |
| Fl | Forms jam LED assembly or forms stacker | Sce Printer MIM parts catalog |
| H0 | Hammer coils or cables | Sec Printer MIM parts catalog |
| J0 | $\begin{aligned} & \text { Printer attachment } 10 C \text { card } \\ & \text { (01AA1 Q2) }(O U=18) \\ & \text { Printer attachment } 10 \mathrm{C} \text { c.ard } \\ & (01 \mathrm{AB} 2 \mathrm{~L} 2)(\mathrm{OU}=58) \end{aligned}$ | $\begin{aligned} & 4361306 \\ & 4361306 \end{aligned}$ |
| L8 | Intermittent or difficult problem MAP - thermal check 1 |  |
| L9 | Intermittent or difficult problem MAP - thermal check 2 |  |
| M0 | Intermittent or difficult problem MAP - carriage check I |  |
| M2 | Intermittent or difficult problem MAP - carriage check 2 |  |
| M3 | Intermittent or difficult problem MAP - carriage check 3 |  |
| M4 | Intermittent or difficult problem MAP - any hammer on |  |
| M5 | Intermittent or difficult problem MAP - haminer echo check |  |
| M6 | Intermittent or difficult problem MAP - data parity check |  |
| N7 | Intermittent or difficult problem MAP - power check |  |
| M8 | Intermittent or difficult problem MA, P - belt speed checi- |  |
| M9 | Intermittent MAP - carriage pedestal check |  |
| N0 | intermittert or difficult problem MAP - belt up to speed |  |

## 3262/5211 RC Table

| Result Code | Definition | $P / N$ |
| :---: | :---: | :---: |
| N1 | Intermittent or difficult problem MAP - ribbon check |  |
| N2 | Intermittent or difficult problem MiAP - forms jam check |  |
| N3 | Intermittent or difficult problem MAP - busy too long |  |
| N5 | Intermittent or difficult problem MAP - carriage check 4 |  |
| N6 | Intermittent or difficult problem MAP - fire tier sync check |  |
| N7 | Intermittent or difficult problem MAP - power check |  |
| N8 | Intermitteat or difficult problem MAP - contactor stuck closed check (in printer MLM) |  |
| P 0 | Printer power | See Printer MIM parts catalog |
| R0 | Riboon or ribbon drive or ribbon path | See Printer MIM parts catalog |
| S0 | System problem |  |
| S2 | System control adapter (SCA) (01AAIC2) | 4202410 |
| S3 | System channel |  |
| S4 | VMC or customer application program |  |
| S5 | SCA/console adapter card 2 (01AAI D2) | $\begin{aligned} & 4223707 \\ & 4223708 \text { (Katakana) } \end{aligned}$ |
| 70 | Preventive maintenance on printer |  |
| Z1 | Go to MAP 0500 entry point E |  |
| Z9 | Not valid error code - inform your programming support structure |  |

## 5424 SRC Table ( $\mathrm{OU}=19$ )

Use position 5, 6, 7, $\mathbf{8}$ of the SRC to find the Resuit Code.
Use the Result Code and Probability to determine the action or parts required.

| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see B4 Card 1 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 0019 | 0000 | Device power off failure | A150 B150 |
| 0019 | 1901 | Feed check 1 | B190 A205 A305 |
| 0019 | 1902 | Feed check 2 | B190 A305 A205 |
| 0019 | 1903 | Feed check 3 | B190 A305 A205 |
| 0019 | 1904 | Feed check 4 | B195 A305 |
| 0019 | 1905 | Feed check 5 | B190 A305 A205 |
| 0019 | 1906 | Feed check 6 | B199 |
| 0019 | 1907 | Feed check 7 | B199 |
| 0015 | 1908 | Feed check 8 | B195 A205 |
| 0019 | 1909 | Feed check 9 | B195 A205 |
| 0019 | 190A | Feed check 10 | B190 A205 A305 |
| 0019 | 1908 | Feed check 11 | B190 A205 A305 |
| 0019 | 190C | Feed check 12 | B190 A205 A305, |
| 0019 | 190D | Feed check 13 | B190 A205 A305 |
| 0019 | 190 E | Fee ${ }^{\text {d }}$ check 14 | B190 A205 A305 |
| 0019 | 190F | Feed check 15 | B190 A205 A305 |
| 0019 | 1910 | Feed check 16 | B190 A205 A305 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see B4 Card 1 <br> Result Codies and Probability \% |
| :---: | :---: | :---: | :---: |
| 0019 | 1911 | Feed check 17 | B190 A205 A305 |
| 0019 | 1912 | Feed check 18 | B190 A305 A205 |
| 0019 | 1913 | Feed check 19 | B190 A305 A205 |
| 0019 | 1914 | Feed check 20 | B199 |
| 0019 | 1915 | Cells defective | B195 A305 |
| 0019 | 1916 | Read check | B195 A305 |
| 0019 | 1917 | Punch invalid | A180 Z120 |
| 0019 | 1918 | Punch check | B190 A205 A305 |
| 0019 | 1919 | Device address out of sequence | A450 A125 Z125 |
| 0019 | 191A | Fire emitter check | B195 A305 |
| 0019 | 191B | Print sync check | B195 A305 |
| 0019 | 191C | Print home check | B195 A305 |
| 0019 | 191D | Print clutch check | B190 A305 A205 |
| 0019 | 191E | Power fault | B175 A125 |
| 0019 | 191F | Command reject | A450 A125 Z125 |
| 0019 | 1920 | IOC parity error from DBI | A125 A225 A325 A425 |
| 0019 | 1921 | IOC internal parity error | A125 A225 A325 A 425 |
| 0019 | 1922 | CSA parity error | A175 A425 |
| 0019 | 1923 | Adapter detected invalid OU | A450 A125 Z125 |
| 0019 | 1924 | Invalid disconnect | A450 A125 Z125 |
| 0019 | 1925 | Chantel error | A450 A125 Z125 |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see 13.4 Card <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 0019 | 1926 | FOB time-out | A450 A125 Z125 |
| 0019 | 1928 | Operation program error | A450 A125 2125 |
| 0019 | 1929 | Invalid BSTAT/DSTAT data | A450 A125 Z125 |
| 0019 | 192A | Read sense/read data failure | A450 A125 Z125 |
| 0019 | 192B | Start-up failure | A450 A125 Z125 |
| 0019 | 1980 | Hopper cheek | B190) A205 A305 |
| 0019 | 9998 | Channel busy | A450 A125 Z125 |

## 5424 RC Table

## 5424 RC Table

| Result <br> Code | Definition | $\mathbf{P} / \mathbb{N}$ |
| :--- | :--- | :--- |
| A1 | Adapter card 1 (01AB2 D2 Card) | $\mathbf{8 3 2 8 0 7 0}$ |
| A2 | Adapter card 2 (01AB2 C2 Card) | $\mathbf{2 5 4 9 0 8 0}$ |
| A3 | Adapter card 3 (01AB2 B2 Card) | $\mathbf{2 5 5 0 6 3 2}$ |
| A4 | IOC card (01AB2 E2 Card) | $\mathbf{4 3 6 1 3 0 6}$ |
| B1 | 5424 Box problem, on site service is rec,uired. |  |
| $\mathbf{Z 1}$ | Channel problem, on site service is required. |  |

## WSC/WSC-E SRC Table

There are two sections for WSC System Keference Codes.

- WSC starts at B6 Card I.
- WSC-E starts at BII Card 1 .
- Ask the eustomer which type of controller caused the SRC (WSC or WSC-E).

If the customer does not know the type, the following procedure will help determine the type.

- Display the control unit description (CUD) of the failing control unit. Use the DSPCUD command.
- Record the TYPE parameter (WSC or WSCE) for the CUD corresponding to the SRC and report it along with the SRC when making a call for IBM service.

To find this procedure the customer can see the PDP code description of the CPF error message (in both the Messages Guide and the Problem Determination Guide).

## WSC SRC Table $(\mathrm{OU}=30-33,70-73, \mathrm{~B} 0-\mathrm{B3}$, and F0-F3)

Use powition $\mathbf{5}, \mathbf{6}, \mathbf{7}, \mathbf{8}$ of the $\operatorname{SRC}$ to lind the Result Code.
Use the Result Code and Probability to determine the action or parts required.

| $\begin{array}{\|ll} \text { SRC } & \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see B9 and B10 Card I <br> Result Codes and J'robability \% |
| :---: | :---: | :---: |
| $00 \mathrm{xx}-00003$ | WSC internal error $\log$ ( vary off CD) | U299 |
| $00 \times x-0005$ | WSC internal error log (bulfer werthow) | D)399 |
| $00 \mathrm{xx}-0007$ | WSC internal error log (program reguested) | U299 |
| 00xx-00FF | Undetined error | U199 |
| $00 \mathrm{xx} \neg \mathbf{0} 120$ | halt or channel parity error on upposite OU | 2265C030 \305 |
| 00 xx 015x | 1.0 crorn (invalid BSTAT byte 1) | 5190 U210 |
| 00xx-020) | Command invalid or invalid for startup mode | C0:5C145S110 |
| 00xx-0202 | Command invalid or invalid for inbound queue | C045C145S110 |
| $00 \mathrm{xx}-0203$ | Command insatid or invalid for buthound queue | C045C145S110 |
| $00 \times x-0.04$ | Address error in read or wite data storage command | C045C145S110 |
| 00xx-0205 | Read data command length error | C0.5 C145S110 |
| $00 \times x-0206$ | Command invalid for post error mode | C045C145S110 |
| $00 \mathrm{x} \times-0207$ | Error in load poll list command | $\begin{aligned} & \mathrm{C} 135 \mathrm{C} 130 \mathrm{C} 210 \mathrm{C} 310 \\ & \text { C410 S105 } \end{aligned}$ |


| $\begin{array}{\|ll\|} \hline \text { SRC } & \\ 1234 & 5678 \\ \hline \end{array}$ | Definition | For result code definitions, see B9 and B10 Card I <br> Result Codes and Probability \% |
| :---: | :---: | :---: |
| 00xx -0208 | Write data FOB error | C045 C145S1 10 |
| 00xx $\neg 0209$ | Reset command rejected due to command pending | C045 C145S110 |
| $00 \mathrm{xx} \neg \mathbf{0 2 \mathrm { xx }}$ | Command reject-invalid BSTAT byte I | C045 C145S110 |
| 00xx -0301 | Start device time-out | $\begin{aligned} & \text { Z235 C025 C115 C415 } \\ & \text { A310 } \end{aligned}$ |
| 00xx -0302 | Channel error | Z26s C030 A305 |
| 00xx -0303 | Read sense command failed during vary off | C155 C045 |
| 00xx -0304 | Read serse command failed while processing an error | $\mathrm{Cl}_{55} \mathrm{C} 045$ |
| 00xx -0305 | Vary off data | U299 |
| 00xx -0306 | FOB time-cut | C050 C140 Z210 |
| 00xx $-\mathbf{0 4 0 1}$ | Selective reset failed during vary-on CD | S070'S230 |
| 00xx -0402 | Insufficient WSC extended data storage | S570 S030 |
| 00xx -0403 | MCR indicates this OU number is not configured | N050 S050 |
| 00xx $\neg \mathbf{0 4 0 4}$ | The translate table specified has not been installed | N170 S030 |
| 00xx - 0405 | LUD device specific area invalid | D065 S035 |
| 00xx-0500 | Operation program error | Z190 S 110 |
| 00xx -0612 | lOC check reset attention | $\begin{aligned} & \mathrm{C} 030 \text { C130 T220 S215 } \\ & \text { A205 } \\ & \hline \end{aligned}$ |
| 00xx $\neg 0618$ | Adanter transmit error | C160 C035 A203 A402 |

WSC SRC Table

| $\begin{array}{\|ll\|} \hline \text { SRC } & \\ 1234 & 5678 \\ \hline \end{array}$ | Definition | For result code definitions, see B9 and B10 Card I <br> Result Codes and Probability \% |
| :---: | :---: | :---: |
| 00xx -0620 | Conırol storage address parity error | C035 T035 C130 |
| $00 \mathrm{xx}-0640$ | IOC internal parity error | $\begin{aligned} & \mathrm{C} 030 \mathrm{C} 125 \mathrm{C} 215 \mathrm{C} 315 \\ & \mathrm{C} 415 \end{aligned}$ |
| 00xx $\neg \mathbf{0 6 8 0}$ | IOC DBI parity crror | $\begin{aligned} & \text { C020 C120 C220 C320 } \\ & \text { C419 A401 } \end{aligned}$ |
| 00xx -0688 | Cable address register parity error | $\begin{aligned} & \text { C430 C225 T125 T3ı5 } \\ & \text { C105 } \end{aligned}$ |
| 00xx-06xx | Invalid event data byte 3 | $\begin{aligned} & \mathrm{C} 030 \mathrm{C} 130 \mathrm{C} 214 \mathrm{C} 413 \\ & \mathrm{C} 513 \end{aligned}$ |
| 00xx -0801 | SNA: resource not available | S0ヶ9 |
| $00 \mathrm{xx}-8004$ | SNA: unrecognized DAF | S099 |
| 00xx -9998 | Channel busy | Z199 |
| $\begin{array}{\|l\|} \hline 00 \mathrm{xx}-\text { All } \\ \rightarrow \text { other } \\ - \text { codes } \end{array}$ | Not valid error code - inform your programming support structure | Z999 |

## WSC RC Table



| Result Corle | Definition |  | Location/Additional Service Aids | $P / N$ |
| :---: | :---: | :---: | :---: | :---: |
| S1 P | Program check (VMC) |  |  |  |
| S2 | SCA related failure (IOM or interface) |  |  |  |
| S5U <br> u <br> w | Verify number of logical units in the load poll list with features installed |  |  |  |
| TO | RW to SW (lop card connector) |  | See note | 1794410 |
| T1 | SX to TX (top card connector) |  | Sec note | 1794410 |
| $12 \left\lvert\, \begin{aligned} & \text { R } \\ & \mathrm{c}\end{aligned}\right.$ | RY to SY (top card connector) |  | See note | 1794410 |
| T3 | SZ to TZ (top card connector) |  | See note | 1794410 |
| U1 | Error detected by the WSC-E or IOM |  |  |  |
| U2l <br> f <br> cod | Informational log entry - no failure associated with tops code |  |  |  |
| $\underline{\chi 1}$ | OP program crror or channel busy |  | Co to MAP 0500 e point E |  |
| Z2 | Channel error |  | Sce channel ELA M |  |
| Z9 | Not valid error code. Inform your programming support. |  |  |  |
| Note: Board location depends on OU number shown in position 3 and 4 of the SRC. |  |  |  |  |
| OU <br> Number | WSC | Board Location |  |  |
| 30.31 | 1 | 01AAI |  |  |
| 70, 71 | 2 | 01AA2 |  |  |
| B0, B1 | 3 | 02AA2 |  |  |
| F0, F1 | 4 | 02AAI |  |  |

## WSC-E SRC Table ( $\mathrm{OU}=30-33,70-73$, $\mathrm{B} 0-\mathrm{B} 3$, and F(6-F3)

Use position $5,6,7,8$ of the SRC to find the Result Code.
Use the Result Code and Probablitiy to determine the action or parts required.

| $\begin{array}{ll} \text { SRC } & \\ 1234 & 5678 \end{array}$ | Destrition | For result code definitions, see B14 and 1315 Card I <br> Resull Codes and Probability \% |
| :---: | :---: | :---: |
| $00 \mathrm{xx} \neg 00003$ | W'SC-! internal error $\log$ (vary off CD) | U299) |
| $00 \times x \sim 00005$ | WSC-E internal error log (buffer overfow) | D)309 |
| $00 . \mathrm{xx} \neg 00007$ | WSC-E internal cror $\log$ (program requested) | U299 |
| $00 \mathrm{x} \times-00 \mathrm{FF}$ | Undelined error | U199 |
| 00xx - 0110 | Reset by opposite OU (1/O error) | C060 S I 40 |
| $00 . \mathrm{xx} \neg 0120$ | Halt or channel parity error on opposite OU | 2265 C030 A305 |
| $00 \mathrm{xx} \neg 01 \mathrm{xx}$ | 1,O error (invalid BSTAT byte 1) | S190 U210 |
| $00 \mathrm{xx} \neg 0201$ | Command invalid or invalid for startup mode | C045 C145 S 110 |
| 00xx--0202 | Command invalid or invalid for inbound queue | C045 C145S110 |
| $00 . \mathrm{xx} \neg \mathbf{0 2 0 3}$ | Command invalid or invalid for outbound queue | C045 Cl45 S 110 |
| $00 \mathrm{xx}-0204$ | Address error in read or write data storage command | C045C145S110 |
| $00 \mathrm{xx}-0205$ | Read data command length error | C045C145S110 |
| $00 x x-0206$ | Command invalid for post crror mode | C045 Cl45S110 |


| $\begin{array}{ll} \operatorname{SRC} \\ 1236 & 5678 \end{array}$ | Delinition | For result code definitions, see 13!f and B15 Card I <br> Result Codes and Probability $\%$ |
| :---: | :---: | :---: |
| (1)1, 0.0207 | Error in lad poll line command | C150 C0.65S109 |
| (00) 0.10208 | White data Fob error | C0.45C145S110 |
| (00) 5.0209 | Reser command rejeeted due to command pending | C04, C145 110 |
| (00) 02020.1 | Invalid OU in $\dot{\sim}$ OB byte 8 | C0.5 C.145S110 |
| (0) 02.0. | Command reject-invalid BSTAT bytel | C0tic 145 S 110 |
| (10) 5 0.301 | Start derice timerat | $\begin{aligned} & \mathrm{Z} 23 \mathrm{CO} \mathrm{CO} \mathrm{C} 115 \mathrm{~A} \mathrm{~B}_{0} \\ & \mathrm{~N} 205 \end{aligned}$ |
| (10) 0302 | Chamel ertor | $\underline{120.5 C 0.30) ~-~} 30.5$ |
| 0040303 | Read sembe commamd failed durimg SII? (1l | C15sC045 |
| (10) 0.304 | Read seme command lailed while processime an eror | C15.5CO.5 |
| (10) 0305 | Vaty off data | U299 |
| $000 \pm 3$ | FOB time-nut | C075C115 $/ 210$ |
| 00010401 | Selective reset lailed during varvorn CD | C040 Cl30 5220 S010 |
| (0) 2 a | Insufficiont WSC-E data sorage | S570 S(0)30 |
| 00.50403 | MCR indicates this OU number is not configured | N0.50 S0So |
| 00.8 Cl 4 | The translate table specified hats not been imstalled | N170S(3) |
| (0)0x - 0405 | LUD device specific area invalid | D)005 S0 3.5 |
| $00 \times x-0500$ | Operation program error | 2/190S 110 |
| 00xx-0610 | CSD parity crror | C095 N205 |


| $\begin{array}{ll} \text { SRC } & \\ 1234 & 5678 \end{array}$ | Definition | For resull code definitions. see B14 and B15 Card I <br> Result Colles and Probability \% |
| :---: | :---: | :---: |
| $00 \mathrm{xx}-0614$ | Adapter diagnostic failed on start up | C150 C040 T405 T505 |
| 00xx -0618 | Adapter transmit error | C160 C035 A203 A402 |
| 00xx -0634 | Invalid disconnect (post event) | 7.250 CO 0 |
| 00xx -063 C | Link register , erflow/underflow | C095 N 205 |
| 00xx $-\mathbf{0 6 4 0}$ | IOC internal parity error or DBI parity crror | C060 C130 T405 T50. |
| $00 \mathrm{xx}-0680$ | IOC ABO or DBO parity error | $\begin{aligned} & \mathrm{CO50} \mathrm{C} 140 \mathrm{~T} 407 \mathrm{~T} 502 \\ & \mathrm{~A} 401 \end{aligned}$ |
| 00xx -06 xx | Invalid event data byte 3 | C050 C150 |
| 00xx -0801 | SNA: resource noi available | S099 |
| $00 \mathrm{xx}-\mathbf{8 0 0 4}$ | SNA: unrecognized DAF | S080 C020 |
| 00xx-9998 | Channel busy | Z199 |
| 00xx $\neg$ D019 | Ideographic extension character-RAM address not valid | D660 S140 |
| 00xx - D020 | Ideographic extension character-RAM contents list missing in request $1 / \mathrm{O}$ save агеа | O399 |
| $00 \mathrm{xx} \neg \mathrm{D} 021$ | idcographic request I/O save area not valid | O399 |
| $\begin{gathered} 00 \mathrm{xx} \neg \text { All } \\ \neg \text { other } \\ \neg \text { codes } \\ \hline \end{gathered}$ | Not valid error code-inform your programming support structure | Z999 |

## WSC-E RC Table

## WSC-E RC Table



| Result Code | Definition |  | Location/Additional Service Aids |  | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| S2 | SCA related failure (IOM or interface) |  |  |  |  |
| S5 | Verify number of logical units in the load poli list with features installed |  |  |  |  |
| T4 | X Crossover |  | $\begin{aligned} & \text { TX to U } \\ & \text { 1-4, RX } \\ & \text { WSC-E } \end{aligned}$ | for WSC-E <br> SX for (Sce note) | 1794410 |
| T5 | Y Crossover |  | $\begin{aligned} & \text { TY to U } \\ & \text { 1-4, RY } \\ & \text { WSC-E } \end{aligned}$ | for WSC-E <br> SY for (See note) | 1794410 |
| U1 | Error detected by the WSC-E or IOM |  |  |  |  |
| U2 | Informational log entry - no failure associated with this code |  |  |  |  |
| 21 | OP program error or channel busy |  | Go to M point E | $0500 \text { entry }$ |  |
| Z2 | Channel error |  | Sce chan | ELA MAPS |  |
| 29 | Not valid error code. Inform your programming support. |  |  |  |  |
| Note: Board location depends on $O U$ number shown in position 3 and 4 of the SRC. |  |  |  |  |  |
| OU | WSC-E | Board Loc | OU | WSC-E |  |
| 30, 31 | 1 | 01 AA 1 | 32,33 | 50 | A1 |
| 70, 71 | 2 | 01AA2 | 72,73 | 60 | A2 |
| B0, B1 | 3 | 02AA2 | B2,B3 | 70 |  |
| F0, Fl | 4 | 02AA1 | F2,F3 | 8 0 | A1 |

## 3203/4245 SRC Table

There are wo sections for 3203 and 4245 System Reference Codms.

- 3203 starts at 1817 Card 1.
- 4245 starts at C5 Card 1.
- Ask the customer which type of printer caused the SRC (3203 or 424.5).

If the customer does not know the type, the following procedure :ill help determine the type.

- Display the device description (DEVD) of the failing Printer. Use the DSPDEVD command.
- Record the DEVTYPE parameter (3203 or 4245) for the DEVD corresponding to the SRC and report it along with the SRC when making a call for IBM service.


## 

## IBM Systena

## IBM Systencs

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Fifth Edition (June 1986)

This major revision makes obsolete S229-8165-6. Because the changes and additions are extensive, this publication should be reviewed in its entirety. Changes and additions were made to suppert the $9332 / 9335$ DASD and Attachment.

Changes are periodically made to the information herein; any such changes will be reported in subsequent revisions or Technical Newsletters.

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## System/38 Reference Code (SRC)

Use position 1 of the SRC to find the proper microfiche card and frame. Invalid codes are not shown.

## SRC Table

| SRC  <br> 1234 5678 | Row/Frame/Card | Device |
| :---: | :---: | :---: |
| Help with SRC | A2 Card 1 | How to use SRC |
| Oxxx xxxx | A4 Card 1 | I/O Devices |
| 1xxx xxxx | D6 Card 2 | SCA Detected |
| 2xxx xxxx | D13 Card 2 | CPU (MS, CS, Chan, Planar) |
| 3 xxx xxxx | E18 Card 2 | 62PC |
| 4xxx xxxx | A7 Card 3 | 3370 |
| 6xxx xxxx | B10 Card 3 | 1/O Power |
| 7xxx xxxx | C14 Card 3 | 9332/9335 Attachment |
| 8xxx xxxx | D3 Card 3 | 9335 |
| 9xxx xxxx | A17 Card 4 | 9332 |
| $\mathrm{F}_{\mathrm{xxx}} \mathrm{xxxx}$ | B12 Card 3 | Console Status |

## How to Use the System Reference Code (SRC) Table

- The customer is directed by the Problem Determination (PD) Guide to gather information, record, and report the 8 -character $S R C$ when placing a call for service. (See frame A3, then return here)
- If no SRC is reported, the CE she uld contact the customer and determine if information is available to create: SRC. See Frame A3 for the information sources.
- Look up the SRC on these microfiche cards to deiermine:
- Failing feature or device.
- Probable failing FRUs with \% of probability.
- FRU part numbers.
- Need for programming support.
- References to the maintenance package.
- Start at Frame Al and follow the instructions as they are presented to decode the SRC.
- This information will allow the CE to determine what service action is required and what FRUs should be taken on the service call.
- The customer gets the ? - . from one of the three sources shown depending on the type of problem.

Example 1
Machine Check With SRC on Screen


Valid Codes:
$\left.\begin{array}{r}\text { SCA-1 } X X X X X X X \\ \text { CPU- } 2 X X X X X X X \\ \text { 62PC- } 3 \times X X X X X X \\ 3370-4 X X X X X X X \\ \text { Power- } 6 X X X X X X X\end{array}\right\}$


Example 2
CPF Message on Screen

OUOL" ERRC $0019192 ?$


With
Machine
Check On



Example 3
Machirse Check
With Elank Screen

Vaid Codes:
FU00 XXXX Mach Ck-F001 XXXX SCA-F002 XXXX SCA/Mach Ck-F002 XXXX Thermal-F004 XXXX

With
Blank Screen

System Reference Code

## I/O Devices SRC Table

## I/O Devices SRC Table

Use positions $\mathbf{3}, \mathbf{4}$ (OU numbei) of the SRC to find the proper card and frame.

| $\begin{array}{\|ll\|} \hline \text { SFC } & \\ 1234 & 5678 \\ \hline \end{array}$ | Row/Frame/Card | Device |
| :---: | :---: | :---: |
| 0001 XXXX | On-site service is required | SCA |
| 0002 XXXX | A6 Card 1 | Console |
| 0003 XXXX | A. 6 Card 1 | Console |
| 0012 XXXX | On-site service is required | 72 MD |
| 0015 XXXX | A9 Card 1 | 3410/11 |
| 0018 KXXX | A12 Card 1 | 3262/5211-1 |
| 0019 XXXX | B1 Card 1 | 5424 |
| 0020 Xxxx | On-site service is required | Comm Line 1T |
| 0021 XXXX | On-site service is required | Comm Line 2T |
| 0022 XXXX | On-site service is required | Comm Line 3T |
| 0023 xxXX | On-site service is required | Comm Line 4T |
| 0028 XXXX | On-stie service is required | Comm Line 1R |
| 0029 XXXX | On-site service is required | Comm Line 2R |
| $002 \mathrm{~A} \times \mathrm{XXX}$ | On-site service is required | Comm Linc 3R |
| 002 BXXXX | On-site service is required | Comm Line 4R |
| 0030 XXXX | B5 Card 1 | WSC/WSC-E IT |
| 0031 XXIK | B5 Card 1 | WSC/WSC-E IR |
| 0032 XXXX | B5 Card 1 | WSC-E 5T |
| 0033 XXXX | B5 Card 1 | WSC-E 5R |
| 0040 XXXX | B16 Card 1 | First 3203/4245 |


| $\begin{array}{\|ll\|} \hline \text { SRC } & \\ 1234 & 5678 \end{array}$ | Row/Frame/Card | Device |
| :---: | :---: | :---: |
| 0041 XXXX | 916 Card I | Si:cond 3203/4245 |
| 0052 XXXX | Cll Card 1 | 3430/3422 |
| 0058 XXXX | A12 Card 1 | 3262/5211-2 |
| 005C 70xX | D10 Card 2 | X. 25 |
| 005E 70XX | D10 Card 2 | X. 25 |
| 0070 XXXX | B5 Card 1 | WSC/WSC-E 2T |
| 0071 XXXX | B5 Card 1 | WSC/WSC-E 2R |
| 0072 XXXX | B5 Card! | WSC-E 6T |
| 0073 XXXX | B5 Card 1 | WSC-E 6R |
| $00 \mathbf{B 0} \times \times X X$ | B5 Card 1 | WSC/WSC-E 3T |
| 00B1 XXXX | B5 Card 1 | WSC/WSC-E 3R |
| 00B2 XXXX | B5 Card 1 | WSC-E 7T |
| 00B3 XXXX | B5 Card 1 | WSC-E 7R |
| 00F0 XXXX | B5 Card 1 | WSC/W'SC-E 4T |
| 00 F 1 XXXX | B. 5 Card 1 | WSC/WSC-E 4R |
| 00F2 XXXX | B5 Card 1 | WSC-E 8T |
| 00 F 3 XXXX | B5 Card ! | WSC-E 8R |

Console SRC Table

## Console SRC Table ( $\mathrm{OU}=02,03$ )

Use position 5, 6, 7, 8 of the SRC to find the Resuit Code.
Use the Result Code and Probability to determine the action or parts required.

| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see A7 and A8 Card I <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 00xx | 0101 | Channel error | Z290 N110 |
| 00xx | 0104 | Not valid sereen format (1/O exception) | P099 |
| 00 xx | 0105 | Command reject | P199 |
| $00 . \times \mathrm{x}$ | 0201 | CRT cable out | M135 C030 D030 A105 |
| (0) xx | 0202 | Keyboard cable out | M1150 C140 A110 |
| 00 xx | 0203 | Screen bulfer parity error | A195 S005 |
| 00)x | 0204 | Screen bufler/DBI parity error | X040 A130 S025 B00. |
| $00 \times x$ | 0205 | Keyboard overrun | $\begin{aligned} & \text { N075 K010 K1: C204 } \\ & \text { A101 } \end{aligned}$ |
| 00x. ${ }^{\text {x }}$ | 0206 | Invalid keyboard scan code | $\begin{aligned} & \text { K045 K135 C110 \& } 105 \\ & \text { S005 } \end{aligned}$ |
| 00xx | 0301 | Post event (1OCl parity error) | N150 S020 A120 B110 |
| 00xx | 0302 | Post event (nct valid disconnect) | Z290 N110 |
| 00xx | $04_{4}^{4} 01$ | Not valid BSTAT/DSTAT data | U070 P220 A110 |
| 00xx | 0402 | FOB time-out | M255 N120 S020 Al05 |
| 00xx | 0403 | Operational program crror | Z199 |
| 00xy | 9998 | Channel busy | Z199 |

## Console RC Table

| Result Code | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| Al | SCA/console adapter card 2 (0\|AAI D2) | $\begin{aligned} & 422370) 7 \\ & (4223708 \text { lor Katakana) } \end{aligned}$ |
| 130 | $0\|A A\|$ board - check DBI/CS data bit P/8 input at D2 M1! for continuity and for a shorted net. |  |
| 131 | 01 AAI board - check IOC interface lines from 014 A 1 B 2 to C 2 or D 2 for continuity and for shorted nets. |  |
| C0 | Cable from 01AA1 D2 Z, to the CRT. | 2549410 |
| C1 | Cable from 01AA1 D2 Y to the keyboard. | 2550924 |
| C2 | Cable from 01AAI D2 Y to the keyboard. | 2550924 |
| D0 | CRT assembly - see Volume B (B12-130) For removal replacement procedure | 2549412 |
| K 0 | Keyboard cleaning procedure - B12-150 |  |
| K1 | Keyboard logic PC board. | 8626969 |
| M1 | Check console cable continuity --s see Volume A (A20-040) |  |
| M2 | Use the SCA ELA MAPs to identify related problems. |  |
| N0 | Operator pressed several keys at once. (This is not a failure condition.) |  |
| N1 | 10 Cl (01AA1 B2) | 4361306 |
| P0 | Program data-write command control not correct. Sec Volume B (B12-210 parts 2 and 3) for a description of when the I/O Exception Status bit is set to signa! incorrect screen format errors. |  |

## Console RC Table

| Result <br> Code | Definition | $\mathbf{P / N}$ |
| :--- | :--- | :--- |
| P1 | The console IOM sent a command that is not <br> valid for the console - inform your <br> programming support structure |  |
| P2 | BSTAT/DSTAT data not correct-inform <br> your programming support structure |  |
| S0 | SCA/console adapter card I (U1AA1 C2) | $\mathbf{4 2 0 2 4 1 0}$ |
| U0 | No DSTAT returned for an 1/O <br> error - possible causes of console 1/O crrors <br> are on B12-020 (Part 2) |  |
| $\mathbf{X 0}$ | X-crossover connector, (01AAI C2 X to D2 <br> X) | $\mathbf{1 7 9 4 4 1 0}$ |
| $\mathbf{Z 1}$ | Use MAP 0500, entry point E to determinc <br> service required. |  |
| $\mathbf{Z 2}$ | Use the channel ELA MAPs to determine <br> service required. |  |

## $3410 / 11$ SRC Table ( $\mathrm{OU}=15$ )

Use position 5, 6, 7, 8 of the SRC to finc the Result Code.
Use the Result Code and Probability to determine the action or parts required.

| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see All Card I <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 0015 | 1100 | Channel error | A540 B640 S1:5 A405 |
| 0015 | 1200 | Channel error disconnect | B590 A405 B605 |
| 0015 | 1400 | Channel error-sequence | A550 B650 |
| 0015 | 1500 | Channel error-overrun | B650 A425 A525 |
| 0015 | 2100 | IOC check | A450 A 550 |
| 0015 | 3200 | Command reject-sense required | A450 A550 |
| 0015 | 4100 | ABI/ABO parity error on status update | S165 A435 |
| 0015 | 4200 | ABI parity error | S180 A420 |
| 0015 | 4300 | ABO parity error | A499 |
| 0015 | 4400 | ABI parity attachment | A499 |
| 0015 | 5200 | ABO error | A450 S150 |
| 0015 | 5300 | Control tag | A460 S140 |
| 0015 | 5400 | Instruction tag | A460 S140 |
| 0015 | 6100 | CMD reject-stetus update interface arror | S165 A435 |
| $\begin{aligned} & 0015 \\ & 0015 \end{aligned}$ | $\begin{aligned} & 6200 \\ & \text { thru } \\ & 62 F F \end{aligned}$ | 3411 intel sace tag error | S150 A550 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Delinition | For result code definitions, see All Card I <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 0015 | 6300 | Initial error | S180 $\mathbf{A 4 2 0}^{\text {2 }}$ |
| 0015 | 8800 | Illegal command | S160 A440 |
| 0015 | 8A00 | Word count () | S160 A440 |
| 0015 | $8 \mathrm{C00}$ | Drive not attached | S195 A405 |
| 0015 | 9998 | Channel busy | <199 |
| 0015 | 13200 | Attachment 3411 overrun | S155 $\mathbf{1 4 2 5 ~} \mathbf{S 5 2 0}^{\text {2 }}$ |
| 0015 | 13300 | Bus out check | A450S150 |
| 0015 | 13900 | NRZI write crror | S160 A420 A520 |
| 0015 | 131000 | PE write cror | S160 A420 $\mathbf{S 5}^{2} 0$ |
| 001.5 | C200 | Attachment 3411 overrun | S155 A425 A520 |
| 0015 | L110 | Read sense fatud during error recovery | A440 A540 13615 S105 |
| $0015$ | $\begin{aligned} & \text { E140 } \\ & \text { thru } \\ & \text { E } 210 \end{aligned}$ | See B42-230 (Pari 10) for the error description | A440 A540 13615 S 105 |
| 0015 | E 290 | FOB time-out | S185 A405 A505 B605 |
| $0015$ | All <br> other codes | Tape subsystem problem | S199 |

## 3410/11 RC Table

| Result <br> Code | Definition | $\mathrm{P} / \mathrm{N}$ |
| :--- | :--- | :--- |
| A4 | Adapter card (01.AB2 N2) | $\mathbf{4 3 6 4 0 9 8}$ |
| A5 | IOC Card (01AB2 P2) | $\mathbf{4 3 6 1 3 0 6}$ |
| B5 | VMC problem |  |
| B6 | Channel problem |  |
| S1 | 3410/11 subsystem problem, on-site service is <br> required |  |
| Z1 | Use MAP 0500, entry point E to determine <br> service required |  |

## 3262/5211 SRC Table ( $\mathrm{OU}=18,58$ )

Use position 5, 6, 7, $\mathbf{8}$ of the SRC to find the Result Code.
Use the Result Code and Probability to determine the action or parts required.

| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, sec A16 thro A18 Card ! <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 00xx | 0000 | Power on/off failure | $\begin{aligned} & \text { P035 N735 A01.5 CX10 } \\ & \text { S202 S502 S001 } \end{aligned}$ |
| 00xx | 3203 | Channcl errors | S399 |
| 00xx | 3205 | Command reject (IOC) | $\begin{aligned} & \text { J030 S430 A010 S310 } \\ & \text { S010 S206 S504 } \end{aligned}$ |
| 00xx | 3215 | Wrong belt/image | S450 B035 J010 B105 |
| 00xx | 4008 | Carriage advance overrun | C160 E020 A015 CY05 |
| 00xx | 4020 | Adapter CSA parity error | E030 A030 J030 CW 10 |
| 00xx | 4034 | Invalio disconaect | S340 S040 J020 |
| 00xx | 4038 | Adapter detect invalid OU | J070 S030 |
| 00xx | 4040 | IOC internal parity error | J045 A045 CW05 S005 |
| 00xx | 4080 | IOC DBI parity error | A045 J045 S0:0 |
| 00xx | 4101 | FOB time-out | $\begin{array}{\|l\|} \hline \mathbf{J 0 4 0} \mathbf{A 0 4 0} \mathbf{~ S 3 1 0 ~ C W 0 5 ~} \\ \text { S005 } \\ \hline \end{array}$ |
| 00xx | 4102 | Invalid adapter response | J050 S040 A010 |
| 00xx | 4103 | Error on startup command | J030 A030 S420 S020 |
| 00xx | 4105 | Error recovery failure on read sense | J050 A050 |
| 00xx | 4106 | Error on read sense due to channel error | S330 J030 A020 S020 |


| $\begin{array}{\|c} \mathrm{SRC} \\ 12.34 \\ \hline \end{array}$ | 5678 | Definition | For result code definitions, see Al6 Ihru Als Card I <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 00xx | 4107 | Error recovery failure on read error log | $\pm 050 \mathrm{~A} 050$ |
| 00 xx | 4108 | Eiror ori read error log due to channel puror | S330.J030 A020 S020 |
| 00xx | 4447 | Invalid interrupt | J050 A050 |
| 00xx | 4450 | Invalid SCS command | S480 J010 A010 |
| 00xx | 4452 | Invalid channel sequence | J050 S330 5020 |
| 00xx | 5420 | Carriage cheek 1 | $\begin{aligned} & \text { C135 M035 F010 E010 } \\ & \text { A005 CY05 } \end{aligned}$ |
| 00xx | 5421 | Manual cartiage check | C185 F005 A005 CY05 |
| 00xx | 5422 | Cable interlock | $\begin{array}{\|l} \mathrm{CX} 30 \mathrm{CY} 30 \mathrm{CZ} 20 \\ \mathrm{~A} 020 \end{array}$ |
| 00xx | 5423 | Carriage check 2 | $\begin{aligned} & \text { M245 C140 F005 A005 } \\ & \text { CY05 } \end{aligned}$ |
| 00xx | 5424 | Carriage check 3 | $\begin{aligned} & \text { C135 M33: F010 E010 } \\ & \text { A005 CY05 } \end{aligned}$ |
| 00xx | 5425 | Any hammer on cr.eck | $\begin{aligned} & \text { H9 } 3_{5}^{5} \text { M435 A010 B205 } \\ & \text { CY05 CZ05 E005 } \end{aligned}$ |
| 00xx | 5426 | Hammer echo check | $\begin{aligned} & \text { H0:5 M535 A010 B205 } \\ & \text { CY05 CZ05 E005 } \end{aligned}$ |
| 00xx | 5427 | Data parity check | $\begin{aligned} & \text { M665 E020 A010 } \\ & \text { CZ05 } \end{aligned}$ |
| 00xx | 5430 | Belt sync check | $\begin{aligned} & \text { M745 B020 B120 A005 } \\ & \text { CY05 E005 } \end{aligned}$ |


| $\begin{aligned} & \mathrm{S} 18 \mathrm{C}^{\circ} \\ & 12.34 \end{aligned}$ | 5678 | Delinition | For result code delinitions, see Alo lirn Al8 Card I <br> Result Codes and Probahility " 10 |
| :---: | :---: | :---: | :---: |
| 00い: | 54.31 | Belt uneed chack | $\begin{aligned} & \text { M8.10 } 13330 \quad 13010 \quad \mathrm{~A} 010 \\ & \mathrm{~B} 105 \mathrm{CY} 05 \end{aligned}$ |
| 1001 | 54.32 | Cambinge pedertal chock | M950 Cl 3.5 A010 CY05 |
| 00011 | 54.33 | Belt up to ypeed cheat: | N040 13330 B301.5 (0) 0 CYO5 |
| 01001 | 54.34 | Ihermal check 1 | $1.885 \mathrm{N010} \mathrm{C}$. |
| (101) | 54.35 | Thermal chech? | $1.985 \triangle 010 \mathrm{CX} 05$ |
| 11011 | 54.36 | Rihhon chech | $\begin{aligned} & 120+5 \mathrm{~N} 135 \mathrm{~A} 010 \quad 13005 \\ & \text { CY05 } \end{aligned}$ |
| 0001 | 54.37 | I wrm, jam check | $\begin{aligned} & \text { F030 } \mathrm{F} 130 \mathrm{~N} 225 \text { A } 005 \\ & \mathrm{CY} 0 . \mathrm{E} 005 \end{aligned}$ |
| 1)0以 | 5440 | Printer busy tow long | $\begin{aligned} & \mathrm{R} 045 \mathrm{~N} 3.40 \mathrm{~B} 005 \mathrm{X} 005 \\ & \mathrm{CY} 05 \end{aligned}$ |
| (110) | 5441 | Primler husy loo often | $\begin{aligned} & \text { R040 N335 A010 CY10 } \\ & \text { B005 } \end{aligned}$ |
| 0001 | 5442 | Cambage check 4 | $\begin{aligned} & \text { C135 N535 } \mathbf{F}(010 \text { E010 } \\ & \text { A0 } 05 \mathrm{CY} 05 \end{aligned}$ |
| 000: | 5443 | lire dier syne check | $\begin{aligned} & \text { N6.5 B110 B210 } 13010 \\ & \text { A010 CZ05 } \end{aligned}$ |
| 000.8 | 5444 | Power laull | N770 P010 A010 S305 CX05 |
| 00) | 5451 | Contactor stuck chosed | $\begin{aligned} & \text { N875 A010 CY } 10 \\ & \text { CZ05 } \end{aligned}$ |


| $\begin{aligned} & \mathrm{HRC} \\ & 12.34 \end{aligned}$ | 56\%\% | D) Clinition | For result code definitions. see Alb thru N18 ('ard I <br> Result Codes and Probability "/n |
| :---: | :---: | :---: | :---: |
| (0)\! | 5500 | Bell up to upeed retries | \% (0)\% |
| 0001 | 9000 | Op, ration progatameron | \% 109 |
| 00.5 | 9998 | Chammel busy | 7.1ヶ9 |
| $0011$ | AII other codes | Nor a saliderme conde | \% 9\% |

## 3262/5211 RC Table

| Result Code | Definition | P/N |
| :---: | :---: | :---: |
| A) | Printer adapter card (01AAI P2) $(O U=18)$ <br> Printer adapter card (01AB2 M2) $(O U=58)$ | $\begin{array}{\|l} 2771576 \\ 2771576 \end{array}$ |
| 130 | Print bett | $\mathrm{P}, \mathrm{N}$ varies with eubtomer requirements |
| B1 | Printer subscans transducer or print beit emitter | See Printer MIM parts catillog |
| B2 | Impression control potentiometer or switch assembly | Sec Printer MIM parts caitilog |
| B3 | Print belt drive or belt LED assembly or belt path | See Printer MIIM parts catalog |
| C1 | Carriage drive or carrage motor fedback LED assembly | Sce Printer MIM parts catalog |
| CIN | ```Attachment W erossover (01AA1 P2-Q2) (OU \(=18\) ) Attachment W crossover (01AB2 \(\mathrm{N}^{\prime} 2-\mathrm{L} 2\) ) ( \(\mathrm{OU}=58\) )``` | $\begin{aligned} & 1794410 \\ & 1794410 \end{aligned}$ |
| CX | $X$ signal cable or interposer (01AA\| P2) (OU $=18$ ) X signal cable or interposer ( $01 \mathrm{AB2} 2 \mathrm{M} 2$ ) $(\mathrm{OU}=58)$ | $\begin{array}{\|l} 2548429: 5997533 \\ 25499145997533 \end{array}$ |
| CY | Y signal cable or interposer ( 01 AAI P2) $(\mathrm{OU}=18$ ) Y signal cable or interposer ( $11 \mathrm{AB2} \mathrm{M} 2$ ) $(\mathrm{CU}=58$ ) | $\begin{array}{ll} 2548429 & 5997533 \\ 25499145997533 \end{array}$ |
| CZ | Z signal cable or interposer (01AAi P2) (OU = 18) Z signal cable or interposer $(01 \mathrm{AB} 2 \mathrm{M} 2)(\mathrm{OU}=58)$ | $\begin{array}{\|l\|l\|} \hline 2548429 & 2548429 \\ 59975335997533 \end{array}$ |
| 1 If printer is $3262-\mathrm{Al}$ (bolt-on), do not use this $\mathrm{P} / \mathrm{N}$ for eables, but refor to printer MIM parts catiolog. |  |  |
| E0 | Electrostatic discharge | Sce Printer MIM parts catalog |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see E10 thru E17 Card 2 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 2X00 | $\mathrm{XX2F}$ | Centrol store failure | $\begin{aligned} & \text { B975 B120 B403 B501 } \\ & \text { B301 } \end{aligned}$ |
| 2X00 | XX30 | Internal microcode error | 13660 B130 13405 G903 B302 |
| 2X00 | XX31 | Control store failure | N175 B120 B403 B501 B301 |
| 2X00 | XX32 | Controt store failure | $\begin{aligned} & \text { B975 B120 B403 B501 } \\ & \text { B301 } \end{aligned}$ |
| 2X00 | XX33 | CPU control store parity check | N375 B120 B403 B50t B301 |
| 2×00 | XX35 | Control store failure | $\begin{aligned} & \text { C260 B130 B408 B501 } \\ & \text { B301 } \end{aligned}$ |
| 2X00 | XX36 | Control store failure | C360 B130 B408 B501 1330) |
| 2×00 | XX37 | Control store failure | $\begin{array}{\|l} \mathbf{C} 230 \\ \text { B301 } \\ \hline \end{array}$ |
| 2X00 | XX38 | Control store failure | C160-B130 B404 B301 |
| 2X00 | XX3D | Control store failure | C160 C230 B108 B404 B301 |
| 2X00 | XX3E | Control store failure | $\mid \text { C160 C330 IB108 B401 }$ B301 |
| 2X00 | XX3F | Control store failure | B234 B133 D432 B301 |
| 2X00 | XX40 | DHMC detected channel failure | B135 D135 A116 D305 F305 D201 B301 B401 ZY01 |

## CPU SRC Table

| $\begin{aligned} & \mathrm{SKC} \\ & 1234 \end{aligned}$ | 5678 | Detinition | For result code definitions, see Ei0 thre E17 Card 2 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 2 X 00 | XX41 | DIIMC detected channel farilure | B130 D130 A130 D306 D201 B301 B401 ZY01 |
| $2 \times 00$ | xX42 | DHAC detected channel lailure | D599 |
| $2 \times 00$ | $x \times 43$ | DHMC detected channel failure | Y299 |
| 2 X 00 | $\times \mathrm{X} 44$ | DIMMC detected channel failure | A 199 |
| 2 X 00 | $\times \mathrm{X} 45$ | DHASC detected channel failure | $\begin{aligned} & \text { B132 DI32 D532 B301 } \\ & \text { B401 D201 D301 } \end{aligned}$ |
| 28.10 | $\times \times 46$ | DHNS deteted channel failure | $\begin{aligned} & \text { B132 D132 Y232 B301 } \\ & \text { B401 D201 D301 } \end{aligned}$ |
| 2800 | $x \times 48$ | DHMC detected channel failure | B199 |
| 2 X 000 | $\times \times 50$ | DHAC detected main store Yailure | H175 3120 1340313302 |
| 2 X 00 | $\times \mathrm{X} 51$ | DHMC detected main store failure | H275 13120 13403 B302 |
| 2X00 | X $\times 52$ | DHAC detected main store failure | H375 B120 B403 B302 |
| 2X00 | $\times \times 53$ | DHMC detected main store failure | H475 B120 B403 B302 |
| 2X00 | $\times \times 54$ | DHMC detected main store failure | H575 B120 B403 B302 |
| 2 Xco | XX55 | DHMC detected main store failure | H675 B120 B403 E302 |
| $2 \times 00$ | XX56 | DHMC detected main store failure | H775 B120 B403 B302 |
| 2X00 | XX57 | DHMC detected main store failure | H875 B120 B403 B302 |
| 2.200 | XX58 | DHMC detected main store failure | $\begin{aligned} & \text { IBI65 H920 HA05 I3405 } \\ & \text { G903 B302 } \end{aligned}$ |
| 2X00 | $\times \times 59$ | LHMC detected main store fillure | $\begin{aligned} & \text { B160 G920 B615 B403 } \\ & \text { B302 } \end{aligned}$ |


| $\begin{array}{ll} \mathrm{SRC} & \\ 1234 & 5678 \\ \hline \end{array}$ | Dectinition | For result code definitions， see ElO thru E17 ：＂ard 2 <br> Resmin Codes and <br> Probability \％ |
| :---: | :---: | :---: |
| $2 \times(0) \times 160$ | Dunic deweral planar lature | CMIso B120 |
| $2 \times 00 \times \times 80$ | Channel buskey | Y0\％ |
| $2 \times 000 \times 881$ | （hammel bus tix | X199 |
| $2 \times 00 \times \times 82$ | Channel bus じい | X29） |
| $2 \times 000 \times 183$ | Chamnel buムビい | X 3 （ $)$ |
| $2 \times 000 \times 84$ | （hammel has cor | X 4 曲 |
| $2 \times 00 \times \times 85$ | Chammel bustex | 13199） |
| $2 \times 00 \times \times 86$ | Channel bustert | $131 \times 9$ |
| 2800 ．$\times 1.0$ | Erpor or read wite MS Stimu Wome | $\begin{aligned} & 1375 \text { A220 D)103 1)30i } \\ & \mathrm{D} 201 \end{aligned}$ |
| 2 XOO XXE 1 | Vill wart emor | 131751222.1403 |
| $2 \mathrm{X00} \times \mathrm{XEE} 2$ | HNAC int errme | 1317513220 $1.703 \mathrm{B3} 01$ |
| 2 Y 00 XXE 3 | lime out dusing channel text | （B140）D135 D320 D205 |
| $2 \times 00$ XXE4 | Val init failure | 13175 \％620 $17.703 \mathrm{B3} 01$ |
| $2 \mathrm{XOO} \times \mathrm{XES}$ | Error in SCA scanning CPU | $\begin{aligned} & \mathrm{B} 1.50 \mathrm{~A} 219 \mathrm{D} 115 \mathrm{D} 31.5 \\ & \mathrm{~A} 401 \end{aligned}$ |
| 2 K 00 KXE6 | 1．SSI）Mag Media error | C499 1401 |
| $2 \mathrm{X00} \times \mathrm{XF} 7$ | VMC detected error | 29\％） |
| $2 \mathrm{X00}$ XXF8 | Scan error F Z Chan Combenter card | D） 185 A 210.1405 |
| 2X00 XXF9 | Scan error E2 Chan Conserter card $\qquad$ | D）385 $\mathbf{2 1 0} 10405$ |
| $2 \mathrm{X00} \times \mathrm{XFA}$ | Second machine check and canno： rad CPU | 13610 13235 13125 |

## CPU SRC Table

| $\begin{array}{ll} \text { SRC } & \\ 1234 & 5678 \end{array}$ | Definition | For result code delinitions, see El0 thru E. 17 Card? <br> Result Codes and Probability \% |
| :---: | :---: | :---: |
| $2 \times 00 \times \mathbf{X F B}$ | Error in SCA scanning out CPU | 13140 A 235 A 425 |
| 2Xto XXFC | Error in SCA reading CPU storage | 13140 A235 A425 |
| $2 \times 00 \times 15$ | SCA deected error in hex conversion | A599 |
| $2 \times 00 \times \mathbf{X L E}$ | Error in SCA scanning to the CPU SI register | 13140 A235 A425 |
| $2 \times 00 \times \mathbf{X F F}$ | VAT detected machine check | $71507 / 250$ |
| $2 \mathrm{X} 50 \times 02 \mathrm{X}$ | IO data bit | 13199 |
| $2 \mathrm{X} 50 \mathrm{x03X}$ | 10 tag in or parity bit | 13199 |
| $2 \times 50 \times 060$ | Chan data bit 0 (0\|AA| B2 S02) | X 799 |
| $2 \times 50 \times 061$ | Chan data bit $1(91$ AAl 132 S03) | X 799 |
| $2 \times 50 \times 062$ | Chan data bit 2 (01AA1 122 S 04$)$ | X799 |
| $2 \times 50 \times 063$ | Chan data bit 3 (0\|AA1 B2 S05) | X 799 |
| $2 \times 50 \times 064$ | Chan data bit 4 (01AA1 B2 S07) | X799 |
| $2 \times 50 \times 065$ | Chan data bit 5 (01AAI 132 S 08$)$ | $\mathbf{X} 799$ |
| $2 \times 50 \times 066$ | Chan data bit 6 (01AA1 B2 S09) | X $/ 99$ |
| $2 \times 50 \times 067$ | Chan data bit $7(01 \mathrm{~A} 1 \mathrm{~B} 2 \mathrm{U} 10)$ | $\mathbf{X 7 9 9}$ |
| $2 \times 50 \times 068$ | Chan data bit 8 (0\|AA| K2 G02) | $\mathbf{X} 799$ |
| $2 \times 50 \times 069$ | Chan data bit 9 (01AA1 K2 G03) | X799 |
| $2 \times 50 \times 06 \mathrm{~A}$ | Chan data bit 10 (01AAI K.2 G()4) | X 799 |
| $2 \times 50 \times 0613$ | Chan data bit 11 (0iAAI K2 G05) | X799 |
| $2 \mathrm{X} 50 \times 06 \mathrm{C}$ | Chan data bit 12 (01AAI K2 G08) | X 799 |


| $\begin{array}{ll} \text { SRC } & \\ 12.34 & 56.78 \end{array}$ | Definition | For result code delinitions, see E10 thru E17 Card 2 <br> Result Codes and Probahility \% |
| :---: | :---: | :---: |
| $2 \times 50 \times 061)$ | Chan data bit 13 (01AA1 K2 G09) | $\mathbf{X} 799$ |
| $2 \times 50 \times 06 \mathrm{E}$ | Chan data bit 14 (01AA1 K2 G10) | X799 |
| $2 \times 50 \times 06{ }^{\circ}$ | Chan data bit 15 (01AA1 K2 G11) | $\mathbf{X} 799$ |
| $2 \times 50 \times 070$ | Chan data bit P0 (01AA! K 2 U11) | X799 |
| 2×50 $\times 071$ | Chan data bit Pl (01AAl K2 G.12) | X799 |
| 2x50 $\times 1078$ | Chan valid B (01AAI B2 UI2) | $\times 799$ |
| $2 \times 50 \times 079$ | Chan valiid H (01AAI K2 M02) | X 799 |
| $\times 50 \times 07 \mathrm{~A}$ | Chan EOC (01AAI B2 U13) | X 799 |
| $2 \times 50 \times 0713$ | Chan prefetch (01AAI F2 S10) | X799 |
| $2 \times 50 \times 07 \mathrm{C}$ | Chan error (01AA1 B2 Pl1) | X799 |
| $2 \times 50 \times 08 \mathrm{X}$ | Chan data bit | D1951310313301 [3C01 |
| $2 \times 50 \times 090$ | Chan data bit P0 | D199 |
| $2 \times 50 \times 101$ | Chan data bit Pl | D199 |
| 2×50 $\times 098$ | Chan valid B | D199 |
| 2×50 $\times 099$ | Chan valid 11 | D185 B105 B305 B1B05 |
| $2 \times 50 \times 09 \mathrm{~A}$ | Chan EOC | D18513105 B305 B1B05 |
| $2 \times 50 \times 09 \mathrm{~B}$ | Chan prelech | D185 3105 B305 BC05 |
| 2×50 $\times 109 \mathrm{C}$ | Chan crror | D185 B105 B305 BB05 |
| 2×50 80F4 | Enhanced channel diagnostics | D380 B310 B105 B1305 |
| $2 \mathrm{X} 60 \times \mathbf{0 2 X}$ | 10 data bit | B199 |

CIU SRC Table

| $\begin{aligned} & \text { SRC } \\ & 1234 \quad 5678 \end{aligned}$ | Definition | for resilt code definitions, see El0: in El7 (ard 2 <br> Result Codes and I'robabiiity \% |
| :---: | :---: | :---: |
| $2880 \times 8030$ | 10) data bit $P^{\prime}$ () | 13199 |
| $2 \times 60 \times 031$ | 10 data hit P1 | 13199 |
| $2 \times 60 \times 038$ | 10 valid B (01APL B4 D11) | X599 |
| $2 \times 60 \times 039$ | 10 valid 11 (01APL, B4 B12) | X599 |
| $2860 \times 1331$ | 10 EOC (01APL, B4 D07) | X599 |
| 2860 80313 | 10 profetch (01APL 351304 ) | X599 |
| 2860 $\times 0.3 \mathrm{C}$ | 10 Ermor (1)1AP1, 13.1 13) | X 5 (9) |
| $2 \times 60 \times 060$ | Chan data bit 0 (02AAI A5 D06) | K890 |
| $2 \times 00 \times 061$ | Chan data hit 1 (02AAI A5 D07) | X899 |
| $2 \times 60 \times 062$ | Chan data bit $2(02 \mathrm{AAI}$ A5 D09) | X899 |
| $2 \times 60 \times 063$ | Chan data bit 3 (02AA1 A5 D10) | X899 |
| $2 \times 60 \times(064$ | Chan data bit 4 (02AA1 B1 C11) | $\mathbf{X 8 9 9}$ |
| $2 \times 60 \times 065$ | Chan data bit 5 (02AA! BIDI1) | K899 |
| $2 \times 60 \times 066$ | Chom data bit 6 (02AAI Cl All) | $\mathbf{X 8 9 9}$ |
| $2 \times 60 \times 067$ | Chan data bit 7 (02AAI C1 B11) | X899 |
| $2 \times 60 \times 068$ | Chan data bit $8(02 \mathrm{AAI} \mathrm{Ml}$ D11) | X899 |
| $2 \times 60 \times 069$ | Chan data bit 9 (02AA1 N1 AII) | X899 |
| $2 \times 60 \times 06 \mathrm{~A}$ | Chan data bit 10 (02AA1 N1 B11) | $\mathbf{X 8} 99$ |
| $2 \times 60 \times 0613$ | Chan data bit $11(02 \mathrm{AAINI} \mathrm{Cl} 1)$ | X899 |
| $2 \times 60 \times 06 \mathrm{C}$ | Chan data bit 12 (02AA1 L1 D13) | X899 |
| $2 \times 60 \times 06 \mathrm{D}$ | $\begin{aligned} & \text { Chan lata bit } 13 \text { (02AAl MII } \\ & \text { A13) } \end{aligned}$ | X899 |

E06 Card 2

| $\begin{array}{\|ll} \mathrm{SIRC} & \\ 12.34 & 5678 \end{array}$ | Deflinition | For resulf code definitions, see E:10 thru E:17 Card 2 <br> Result Codes and Probability \% |
| :---: | :---: | :---: |
| $2 \mathrm{Kt}(0) \times 06 \mathrm{~L}$ | Chan dita bit 14 (02AA1 M1 C1.3) | X89) |
| $2 \mathrm{X60} \times 806 \mathrm{~F}$ | Cham data bit 15 (02AAI NI A13) | X899 |
| 28860 入070 | Chan diata bit P0(02AAI ( 11 Al 3 ) | X899) |
| $2 \times 60 \times 071$ | Chan data bit P! (O2AAI NH B11) | K809 |
| $2 \times 60 \times 078$ | Chan valid 13 (02sA1 \| 1 1.) | 1 ) | K899 |
| $2860 \times 079$ | Chan valid \\| (02AA | 11 I:11) | X809 |
| $2 \times 60 \times 07 \mathrm{~A}$ | Chan FOC (02AA1 (1) B13) | X89\% |
| 2x60 $\times 1073$ | Chan prelereh (02AAl V 51308 ) | X890 |
| $2860 \times 07 \mathrm{C}$ | Chan error (02AAI V 5 D05) | X890 |
| $2 \mathrm{~K} 00 \times 8 \mathrm{XX}$ | Chan data bit | D)3951310313301 13C01 |
| $2 \times 60 \times 090$ | Chan data hit P0 | 1)399 |
| $2 \times 60 \times 091$ | Chan data bit PI | 1)399 |
| 28r0 8098 | Chan valid B | D)38, 1310513305131305 |
| $2860 \times 099$ | Chan valid 11 | 1)399 |
| $2 \times .00 \times 09 \mathrm{~A}$ | Chan EOC | 1)399 |
| $2 \times 60 \times 0913$ | Chan prefetch | 1) 399 |
| $2 \times 60 \times 09 \mathrm{C}$ | Chan error | D)399 |
| $2 \times 60 \times 0{ }^{4} 4$ | Enhanced channel diagnostics | $\begin{aligned} & \mathrm{D} 3+5 \mathrm{D}) 1413105 \mathrm{~B} 305 \\ & \mathrm{BB} 305 \end{aligned}$ |
| $2 \times \mathrm{CO} \times 020$ | 10 data bit 0 (01AP1. B5 D11) | X 699 |

CPU SRC Table

| $\begin{array}{ll} \text { SRC } \\ 1234 & 5678 \end{array}$ | Delinition | For result code definitions, see E10 fhru EI7 Card 2 <br> Result Codes and Probability $\%$ |
| :---: | :---: | :---: |
| $2 X C 0 \times 021$ | 10 diata bit I (01APL, 355 BOS ) | K 699 |
| $2 \times 60 \times 022$ | 1 O datia bit 2 (01APL B5 BI2) | X 609 |
| $2 \times C 0 \times 023$ | 10 dital bit 3 (01APL B4 D09) | X699 |
| $2 \mathrm{KCO} \times 1024$ | 10 datal bit 4 (01APL B4 D05) | N699 |
| $2 \times C 0 \times 025$ | 10 datil hit 5 (01APL B.5 B02) | \690 |
| $2800 \times 026$ | 10 data bit of (0)APL B5 [03) | S'ıり |
| 2XC0 X027 | 10 data bit 7 (0)API, B4 DO6) | X 699 |
| $2 \mathrm{XCO} \times 028$ | IO data but 8 (01API, B5 B10) | X690 |
| $2 \mathrm{XCO} \times 1029$ | 10 data bit ${ }^{(0) 1}$ (0PL, B5 DIO) | X699 |
| $2 \mathrm{KCO} \times 102 \mathrm{C}$ | 10 data hit 10(01APL B5 DO9) | X 6.9 |
| $2 \mathrm{CCO} 80 \leq 13$ |  | X690 |
| $2 \mathrm{XCO} \times(12 \mathrm{C}$ | 10 data bit 12 (01APL $13+[308)$ | X699 |
| $2 \mathrm{XCO} \times \mathrm{\lambda} 02 \mathrm{D}$ | 10 data bit 13 (01API, B4 B15) | X699 |
| $2 \times \mathrm{CO} \times 02 \mathrm{E}$ | 10 data bit 14 (01API, B5 D02) | X699 |
| $2 \times \mathrm{CO} \times 02 \mathrm{~F}$ | 10 data bit 15 (01APL B4 B13) | X699 |
| $2 \times \mathrm{CO} \times 030$ | 10 data bit P() (01APL B +BlO ) | X699 |
| $2 \times \mathrm{CO} \times 031$ | 10 data bit PI (01APL B4 B06) | X699 |
| $2 \times \mathrm{C0} \times 032$ | 10 TA (01APL B5 D0\%) | X699 |
| $2 \times \mathrm{CO} \times 033$ | 10 TP (01APL B5 B06) | X699 |
| 2 $\times$ C0 X034 | $10 \mathrm{TD} \mathrm{(0\mid APL} \mathrm{B5} \mathrm{DI3)}$ | X699 |
| $2 \times \mathrm{CO} \times 035$ | $10 \mathrm{grant} \mathrm{(01APL} \mathrm{B5} \mathrm{B09)}$ | X699 |


| $\begin{array}{ll} \mathrm{SRCC} & \\ 1234 & 5678 \\ \hline \end{array}$ | Definition | For result code definitions, see E10 thru E17 Card 2 <br> Result Codes and Probability \% |
| :---: | :---: | :---: |
| $2 \mathrm{XC0} \times 036$ | 10 disconaect (01APL B5 D09) | X699 |
| $2 \times 60 \times 037$ | 10 halt (01APL 35 D05) | X699 |
| $2 \times \mathrm{C0} \times 04 \mathrm{X}$ | 10 data bit | B170 D110 B310 BA 10 |
| $2 \times \mathrm{C0} \times 050$ | 10 data $\mathrm{P0}$ | B170 D110 B310 BB 10 |
| $2 \times \mathrm{C0} \times 051$ | IO data P1 | B170 D110 B310 BB10 |
| $2 \times \mathrm{C} 0 \times 052$ | 10 TA | B170 D110 B310 BC10 |
| $2 \times \mathrm{C} 0 \times 053$ | 10 TP | B170 D110 B310 BC10 |
| $2 \times \mathrm{C0} \times 054$ | 10 TD | B170 D110 B310 BC10 |
| $2 \mathrm{XC0} \times 055$ | 10 grant | B170 D110 3310 BC10 |
| 2 C C0 X 056 | 10 disconnéct | B170 D1 10 B310 BC10 |
| $2 \times \mathrm{C} 0 \times 057$ | IO halt | B170 D1 10 B310 BC10 |
| $2 \times \mathrm{C} 0 \times 072$ | Chan TA (01AAI A4 B02) | X799 |
| $2 \times \mathrm{C} 0 \times 073$ | Chan TP (01AA1 A4 B04) | X 799 |
| $2 \times \mathrm{C0} \times 074$ | Chan TD (01AAI A4 B06) | X799 |
| $2 \mathrm{XC0} \times 075$ | Chan grant (01AA1 A3 B02) | X7p99 |
| $2 \times 60 \times 076$ | Chan disconnect (01AA1 A3 B04) | X799 |
| $2 \times \mathrm{C0} \times 077$ | Chan halt (01AAI A3 B06) | X799 |
| $2 \times \mathrm{C0} \times 992$ | Chan TA (01AA1 B5 D02) | D199 |
| $2 \times 60 \times 093$ | Chan TP (01AA1 B 5 D04) | D199 |
| $2 \times \mathrm{C} 0 \times 094$ | Chan TD (01AA1 B5 D05) | D199 |
| $2 \times \mathrm{C} 0 \times 095$ | Chan Grant (01AAI B5 D06) | D190 D310 |


| $\begin{array}{ll} 8 R C \\ 1234 & 5078 \end{array}$ | i) clinition | For result code delimidions, <br>  <br> Result Codes and I'robal:ility $1 / 2$ |
| :---: | :---: | :---: |
| $2 \lambda(0) \times 096$ | Chan disconnect (0) A A1 BS D07) | 1) 109 |
| $2 \mathrm{xc} 0 \times 8097$ | Chan hatt (01AAI 35.5009 | 1)199 |
| 2XC0 X (0I'1 |  | D)1801)31013305131)05 |
| $2 \times \mathrm{CO} \times \mathrm{OH} 2$ | Enhateed channel diagmostich | 1) 19\% |
| $2 \times 00 \times 02 \mathrm{x}$ | 10 data bit | 1)39513305 |
| $2 \lambda \mathrm{n} 0 \times 03 \mathrm{~S}$ | 10) lag aut or patity hit | 1)39513305 |
| $2 \times 100 \times 04 \times$ | 10 data bit | 13180 D320 |
| $2 \times 100 \times 050$ | 10) ditia bit P0 | $131 \times 010320$ |
| $2 \lambda 100 \times 051$ | 10) Nata init P1 | 131801)320 |
| $2 \times 100 \times 052$ | 1014 | 131801)320 |
| $2 \times 100 \times 053$ | 1019 | 131801)320 |
| $2 \times 100 \times 054$ | $1011)$ | 131801)320 |
| $2 \times 100 \times 055$ | 10 Gram | 131801)320 |
| $2 \times 100 \times 056$ | 10 disconnect | 131801)320 |
| $2 \times 100 \times 057$ | 10 halt | 131801)320 |
| $2 \times 100 \times 072$ | Chan TA (02AAI A 5 1302) | X898) |
| $2 \times 100 \times 073$ | Cham TP (0)AAI A5 B 04 ) | X890 |
| $2 \times 100 \times 074$ | Chan 11) (02AAI A5 B(0)) | X899 |
| $2 \times 100 \times 075$ | Chan Grant (02AAI A! D13) | X899 |
| $2 \times 100 \times 076$ | Chan disconneet (02AAI B1 Cl3) | X899 |
| 2XD0 X077 | Chan inalt (02AAI B! A13) | X899 |


| $\begin{array}{ll} \mathrm{SRC} & \\ 123.4 & 5678 \end{array}$ | Detinition | for resull code definitions, sue El0 Ihra El7 Card 2 <br> Result Codes and Prohahility " |
| :---: | :---: | :---: |
| $2 \times 100 \times 092$ | Chan TA (62AA! A5 B02) | I) $3 y 9$ |
| $2 \times 100 \times 093$ | Chan TP (02AA1A5 B 0 H) | D39\% |
| $2 \times 00 \times 094$ | Chan TD (0)AA: AS 306) | D) $39 \%$ |
| $2 \mathrm{XDO} \times 095$ | Chan Graut (02AAI A1)13) | I) 399 |
| $2 \times D 0 \times 096$ | Chan discommect (02AA) B1 C13) | D) $39 \%$ |
| $2 \mathrm{XDO} \times 097$ | Chan hala (02AAI B1 A13) | D)399 |
| $2 \mathrm{DDON0F} 1$ | Enhanced chamel diagnontics | D)390 13305131)05 |
| 2 KDOXOF 2 | Enhanced channel diagnostios | 1)39\% |
| $2 \mathrm{XDOX0F} 3$ | Enhanced channel dagnosic. | i) 309 |

Ell Card 2

CPU SRC Table

## CPU RC Table

| Result Code | Deffinition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| A 1 | SCA/console loc card (01AAI B2) | 4361306 |
| A2 | SCA/console card 1 (01AAI C2) | 4202410 |
| A.3 | SCA/console card 2 (01AAI D2) | $\begin{aligned} & 4223707 \\ & 4223708 \text { (Katakana) } \end{aligned}$ |
| A4 | SCA cable to planar (01AAIZ. to 01AB(W3) |  |
| A5 | SCA microcode SLVIA |  |
| B1 | Planar (01APL) | $\begin{aligned} & 8328300(\operatorname{Mod} 3-5) \\ & 4361076 \text { (Mod 1-2, 6-8) } \\ & 6370231(\operatorname{Mod} 9 . \end{aligned}$ |
| 132 | One or more control store cards (01ABI S2, T2, U2, V2, R2) | $\begin{aligned} & 5122494(\operatorname{Mod} 3) \\ & 6229410(\operatorname{Mod} 1-2,4-9, \text { A.F }) \\ & 8691997(\operatorname{Mod} A-E) \\ & \hline \end{aligned}$ |
| 133 | Array board (01ABI) | $\begin{aligned} & \mathbf{2 5 5 0 8 9 4}(\operatorname{Mod} 3-5) \\ & \mathbf{8 3 2 8 3 2 0}(\operatorname{Mod} 1-2,6: 8) \\ & \mathbf{6 2 0 3 3 8 0}(\operatorname{Mod} 9, F) \\ & \mathbf{2 4 4 5 8 5 4}(\operatorname{Mod} \text { A-E) } \\ & \hline \end{aligned}$ |
| 134 | 1. ierposers | $\begin{aligned} & 2549343 \text { (power) } \\ & 2549344 \text { (signal) } \\ & \hline \end{aligned}$ |
| 135 | Control sture terminator cards (01AB1 R2, R4) Not used on Mod A, C | $\begin{array}{\|l} 8328248(\operatorname{Mod} 1-2,6-8) \\ \mathbf{2 5 4 9 0 5 6}(\operatorname{Mod} 3-5,9, F) \\ \hline \end{array}$ |
| 136 | $\begin{aligned} & \text { One or more mainstore cards ( } 0\|\mathrm{AB}\| \mathrm{G} 2 \text {, } \\ & \mathrm{H} 2, \mathrm{~J} 2, \mathrm{~K} 2, \mathrm{~L} 2, \mathrm{M} 2, \mathrm{~N} 2, \mathrm{P} 2 \text { ) } \end{aligned}$ | $\begin{aligned} & 4599188(\operatorname{Mod} 3,4) \\ & 7378022(\operatorname{Mod} 5 \text { Type A) } \\ & 5658718(\operatorname{Mod} 5 \text { Type B) } \\ & 5120271(\operatorname{Mod} 7) \\ & 4772607(\operatorname{Mod} 1-2,6,8-9, \\ & \text { A-B,F) } \\ & \mathbf{2 4 4 4 9 6 2}(\operatorname{Mod} \text { C-D }) \\ & \mathbf{6 5 X 5 7 5 0}(\operatorname{Mod} \text { E) } \end{aligned}$ |

El2 Card 2

| ikesult Code | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| 137 | Control shme card (01ABI V2) | 6229416 (Mont1-2, +99, 1.) |
| B8 | Commonstore card (01 ABI U2) | 6,229+16 (Mos) 1-2, 4.9.1.) |
| 139 | Comtrol shore cand (0) ABt ? ? | $\begin{aligned} & 5122494(\text { (Mod 3) } \\ & 6229416 \text { (Mod 1-2. } 7.9 \text { ) } \end{aligned}$ |
| BA |  | 2549344 |
| 1313 | Interpmer (0) ABP B.A) | 2549344 |
| 13C | Interpuer (0) AB1 $\mathrm{Ba}^{5}$ ) | 2549344 |
| B1) | Imterpmer (0) ABI . 22 ) | 2549344 |
| Cl | Comerol vene card (0) A S 1 R2) |  |
| C2 | Control tere card (0) AB1 S2) | 8691997 (M10d A-1) |
| C3 | Control shere card ( 01 A 131112 ) | 8691997 ( 11004 A-t) |
| D1 | Chamme combere card (0)AB11-2) | $\begin{aligned} & 2549055 \text { (M) } 1-8 \text { ) } \\ & 2445900 \text { (Mod 9. A-1) } \end{aligned}$ |
| D) 2 | Channel terminater card | 2549040 |
| D) 3 | (hannel combeter card (0)AB) 123) | $\begin{aligned} & 4202240 \text { (Mod 1-8) } \\ & 24+5902 \text { (Mod). A. } \end{aligned}$ |
| D5 | (12PC adapler card (01AA) (12) | 2771438 |
| 13 | Any 10 adapter on-site service is respuited |  |
| C1 |  | $\begin{aligned} & 4599188 \text { (Mod 3, 4) } \\ & 7378022 \text { (Mod 5 Гype A) } \\ & 5658718 \text { (Mod } 5 \text { Type B) } \\ & 512,1271 \text { (Mod 7) } \\ & 4772607 \text { (Mod 1-2, 6, S-9. } \\ & \text { A-B, F) } \\ & 2444962 \text { (Mod C-D) } \\ & 65 \times 5750 \text { (Mod E) } \end{aligned}$ |

El3 Card 2

| Result C'ode | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| (:2 | Mam பlucc carl (1)1 \B1 N2) |  |
| C,3 | Nain い6mbald (0)AB1 M2) | ```4599188(Mod 3, 4) 7378022(Mod 5 Tspe A) 5658718 (Mod 5 Type B) 5120271 (Mod 7) $772607(Mlod 1-2, 6, 8-9. A-B, F) 2444962(Mod C-1)) 65\5750(Mod E)``` |
| Ci4 | Vain uture cilld (0)AB1 L.2) | $\begin{aligned} & 4599188(\operatorname{Mod} 3.1) \\ & 7378022(\operatorname{Mod} 5 \text { Type A) } \\ & 5658718(\operatorname{Mod} 5 \text { Type B) } \\ & 5120271(\operatorname{Mod} 7) \\ & 4772607(\operatorname{Mod} 1-2,6,8-9 \\ & \text { A-B, F }) \\ & 2444962(\operatorname{Mod} C-D) \\ & 65 \times 5750(\operatorname{Mod} E) \\ & \hline \end{aligned}$ |
| 65 | Nain ulore card (01 ABI K2) | $\begin{aligned} & 4599188(\operatorname{Mod} 3,4) \\ & 7378022(\operatorname{Mod} 5 \text { Type A) } \\ & 5658718(\operatorname{Mod} 5 \text { Type } B) \\ & 5120271(\operatorname{Mod} 7) \\ & 4772607(\operatorname{Mod} 1-2,6,8-9, \\ & \text { A-B,F) } \\ & 2444962(\operatorname{Mod} C-D) \\ & 65 X 5750(\operatorname{Mod} E) \end{aligned}$ |

El 4 Card 2

| Result Corle | Definition | I/N |
| :---: | :---: | :---: |
| C6 | Main store card (0\|AB! .12) | $\begin{aligned} & 4599188 \text { (Mod 3, 4) } \\ & 7378022 \text { (Mod } 5 \text { Type A) } \\ & 5658718 \text { (Mod } 5 \text { Type B) } \\ & 5120271 \text { (Mod 7) } \\ & 4772607 \text { (Mod 1.2, (1, ‥9, } \\ & \text { A-B, F) } \\ & 2444962 \text { (Mod C-1)) } \\ & 65 \times 5750 \text { (Mod L) } \end{aligned}$ |
| 6.7 | Main wore card (0)ABB\| - 2) | $\begin{aligned} & 4599188 \text { (Mod 4) } \\ & 7378022 \text { (Mod } 5 \text { Iype A) } \\ & 5658718 \text { (Mod } 5 \text { Type } B) \\ & 5120271 \text { (Mod } 7 \text { ) } \\ & 4772607 \text { (Mod } 1-2,0,8-9 . \\ & \text { A-B,F) } \\ & 2444962 \text { (Mod C-D) } \\ & 65 \times 5750 \text { (Mod E) } \end{aligned}$ |
| C.8 | Main tore card (01AB1 G2) | ```4599188 (Mod 4) 7378022 (Mod.5 Type A) 5658718 (Mod 5 TyF心 B) 5120271 (Mod 7) 4772607, Mod 1-2, 6,8-9. A-B,F) 2444962 (Mood C.D) 65X5750(Mod E)``` |
| G9 | Main store terminator cards (01BIQ2, Q4) | 5122482 |
| III | Two main store cards (01AB1 M2, P2) | $\begin{aligned} & 4772607(\operatorname{Mod} 9 . A-B, F) \\ & 2444962(\operatorname{Mod} C-D) \\ & 65 \times 5750(\operatorname{Mod} E) \end{aligned}$ |

E15 Card 2

CPURC Table

| Result Code | Definition | $1 \mathrm{l} / \mathrm{N}$ |
| :---: | :---: | :---: |
| 112 | Two main store cards (0\| $13112.2 P^{\text {a }}$ ) | $\begin{aligned} & 4772607 \text { (Mod 9, } A-B, 1) \\ & 2444962 \text { (Mod C.D) } \\ & 65 \times 5750 \text { (Mod E) } \end{aligned}$ |
| 113 | IW0 main vore cards (0) $\mathrm{ABI} \mathrm{M} 2, \mathrm{~N} 2)$ | $\begin{aligned} & 4772607 \text { (Mod } 9, A-B, 1) \\ & 2444962 \text { (Mod }(-1)) \\ & 6585750 \text { (Nod } B) \end{aligned}$ |
| 114 | Two main store cards (0) $\mathrm{AB} 311.2, \mathrm{~N} 2)$ | $\begin{aligned} & 4772607(\operatorname{Mod} 9, A-B, 1) \\ & 2444962(\operatorname{Mod} C-1)) \\ & 65 \times 5750(\operatorname{Mod} \mathrm{E}) \end{aligned}$ |
| 115 | Three main store carss (0)ABI $1.2, \mathrm{~N} 2$, 12) | $\begin{aligned} & 4772607 \text { (Mod 9, A-13, F) } \\ & 2444962 \text { (Mod C-1) } \\ & 65 \times 5750 \text { (Mod B) } \end{aligned}$ |
| 116 | Three main store cards (0) / M $31 \mathrm{M} 2, \mathrm{~N} 2$, P2) | $\begin{aligned} & 4772607 \text { (Mod 9. A-B.1) } \\ & 2444962 \text { (Mod C-D) } \\ & 65 \times 5750 \text { (Mod E) } \end{aligned}$ |
| 117 | Three main store cards ( $0: \mathrm{ABI} 1.2, \mathrm{M} 2$, P2) | $\begin{aligned} & 4772607(\operatorname{Mod}), A-3, F) \\ & 2444962(\operatorname{Mod} C-1)) \\ & 65 \times 5750(\text { Moci }: 1) \end{aligned}$ |
| 118 | Three main store cards ( 01 AB ) L2, M2, N2) | $\begin{aligned} & 4772607(\operatorname{Mod} \cdot A-B, F) \\ & 2444962(\operatorname{Mod} C-D) \\ & 65 \times 5750(\operatorname{Mod} \mathrm{E}) \end{aligned}$ |
| 119 | One failure main store card is causing all main store cards to appear to be failing (O)AB1P2, N2, M2, L2, K2, J2, H2, G2) | $\begin{aligned} & 4772,607 \text { (Mod 9. A-B, F) } \\ & 2444962 \text { (Mod C-D) } \\ & \mathbf{6 5 X 5 7 5 0} \text { (Mod E) } \end{aligned}$ |
| IIA | All main store cards are failure (0\|AB1P2, N2, M2, L2, K2, J2, H2, G2) | $\begin{aligned} & 4772607(\operatorname{Mod} 9, A-B, F) \\ & 2444962(\operatorname{Mod} C-D) \\ & 65 \times 5750(\operatorname{Mod} E) \end{aligned}$ |


| Result Code | Deffinition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| N1 | Control store card (01AB1 S2) | $\begin{aligned} & 5122494(\operatorname{Mod} 3) \\ & 8691997(\operatorname{Mod} A-1) \end{aligned}$ |
| N3 | Control store card (0\|AB1 T2) | $\begin{aligned} & 5122494(\operatorname{Mod} 3) \\ & 6229416(\operatorname{Mod} 1-2,7.9,1 \cdot) \end{aligned}$ |
| X 0 | On-site service required. Go to MAP 1983, entry point G | $\begin{aligned} & \text { B135 A125D110 D310 } \\ & \text { ZV06 ZT06 } \mathbf{Z X} 06 \boldsymbol{Z 8} 02 \end{aligned}$ |
| X1 | On-site service required. Go to MAP 1983, entry point H | $\begin{aligned} & \mathrm{B} 135 \mathrm{~A} 125 \mathrm{D} 110 \mathrm{D} 310 \\ & \mathrm{ZV} 96 \mathrm{ZT} 06 \mathrm{ZX} 06 \mathrm{Z8} 03 \end{aligned}$ |
| X 2 | On-site service required. Go to MAP 1983, entry point I | $\begin{aligned} & \mathrm{B} 135 \mathrm{~A} 125 \mathrm{D} 110 \mathrm{D} 310 \\ & \text { ZV06 ZT06 ZX06 Z.802 } \end{aligned}$ |
| X3 | On-site service required. Go to MAP 1983, entry point $G$ | $\begin{aligned} & \text { I 3 } 3+5 \text { Z825 ZZ08 ZU0is } \\ & \text { ZX08 B102 A102 D102 } \\ & \hline \end{aligned}$ |
| X4 | On-site service required. Go to MAP 1983, entry point H | $\begin{aligned} & \text { D345 Z825 ZZ08 ZU08 } \\ & \text { ZX08 B102 A102 D102 } \end{aligned}$ |
| X5 | On-site service required. Record the failing pin, then go to MAP 1920, entry point A | $\begin{aligned} & \text { B130 D130 D330 I3305 } \\ & \text { BA } 05 \end{aligned}$ |
| X 6 | On-site service required. Record the failing pin, then go to MAP 1920, entry point B | $\begin{aligned} & \text { B130 D130 D330 } 13305 \\ & \text { BA } 05 \end{aligned}$ |
| X7 | On-site service required. Record the failing pin, then go to MAP 1920, entry point D | $\begin{aligned} & \text { F350 D135 ZT05 ZV05 } \\ & \text { ZX05 } \end{aligned}$ |
| X8 | On-site service required. Record the failing pin, then go to MAP 1920, entry point E | $\begin{aligned} & \text { F350 D335 ZU05 ZZ05 } \\ & \text { ZX05 } \end{aligned}$ |
| Y2 | 72MD adapter card (01AAI H2) | 4360810 |
| Z1 | CPU machine check occurred however no data in machine check log buffer to allow a FRU callout. On-site service is required. |  |

CPU kC Table

| $\begin{aligned} & \text { Result } \\ & \text { Code } \end{aligned}$ | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| Z2 | If SRC is $22 \times X$ XXFF the HMC or SCA diagnosites detect a failure, but no dita was available for a FRU call out. On-site service is required. |  |
| 7.3 | SCA could not read or write MS status wond. Use Mag Media diagnostucs to isolate failure. |  |
| 24 | SCA could not read LSSD data because of wrong SLVIR or Mag Nedia failure. |  |
| 25 | Solimare in HMC could also caluse this error. |  |
| 26 | Software in 'VMC or HMC could cause error. |  |
| 27 | SLVIA ALT IMPL, HMC, or VMC microcode. |  |
| 28 | An $1 / \mathrm{O}$ adapter could be holding a channel a tit active. |  |
| 29 | Perform main storage stand-alone dump and referer.se AA070 for further action. Use the MCCHECK LED codes. |  |
| ZM | The planar board level in the MCR and the planar board level of the installed planar ars not equal. |  |
| ZTI | Channel terminator card on first channel. See MAP Ref 019-010 through 019-040 for locations by configuration. | 2549040 |
| ZU | Channel terminator card on second channel. See MAP Ref 019-070 through 019-090 for locations by configuration. | 2549040 |
| ZV | Channel cable on first channel. Sec MAP Ref 019-010 through 019-040 for locations by configuration. |  |
| ZX | Board protem |  |

## El8 Card 2

## 3422 RC Table

| Result <br> Code | Delinition | MiM Keference | $\mathbf{P} / \mathrm{N}$ |
| :---: | :---: | :---: | :---: |
| A0) | A 1 comberion card tracks $0,1,4$, and 6 (A01) | LOC 050 |  |
| A i | A D comberion card tracks $2,3,5$, 7. and P (AO2) | LOC 050 |  |
| A2 | Data sepatator card (A06) | LOC 050 |  |
| 13 | Deskewing and decoding card tracks $0,1,2$, and 3 (A09) | LOC 050 |  |
| 14 | Deskewing and decoding card tacks 4, 5, 6, and 7 (A10) | LOC 050 |  |
| 15 | Read path control, deskewing and decoding track P card (AII) | LOC 050 |  |
| A6 | Error cortection, PE-GCR card (A12) | LOC 050 |  |
| 17 | Data Bufler Control, CRC Regintar, and Error correction card (.113) | LOC 050 |  |
| A8 | formatter data buller, CRC register card (A\|4) | LOC 050 |  |
| \9 | Write path control write triggers tracks P, 0, 1, 2, and 3 card $6: 15$ ) | LCC 050 |  |
| 130 | Write path contod write triggers tracks 4, 5, 6, and 7 card (A16) | LOC 050 |  |
| 131 | Clock: gencration and distribution, Block Format detection card (A17) | LOC. 050 |  |
| B2 | TU control GCR, BSBCC, and VCC counters card (A18) | LOC 050 |  |
| 133 | Microprogram sequencer card (A\|9) | LOC 050 |  |
| 134 | System adapter inierface control card (A20) | LOC 050 |  |


| Result Code | Definition | MIM Reference | $P / N$ |
| :---: | :---: | :---: | :---: |
| 135 | Formattor diagnostics card (A22) | LOC 050 |  |
| 136 | Data streaming buffer card (A23) | LOC 050 |  |
| 137 | Bufler, bormatter interface control card (A24) | $1.0 C 050$ |  |
| 138 | System adapter microprogram card (A25) | L.OC 050 |  |
| 139 | Channel interlace control card (A26) | LOC 050 |  |
| C0 | Control memory card (A27) | LOC 050 |  |
| C1 | System formatter interlace cand (B21) | LOC 050 |  |
| C2 | Two channel switch card (B27) | 1.0 OC 0.50 |  |
| C3 | Communicator, read signals card (C01) | LOC 0.50 |  |
| C4 | TU7 read signal card ( C 02 ) | LOC 0.50 |  |
| C5 | I U6 read signal card ( C 03 ) | LOC 050 |  |
| C6 | TUS rcad signal card (C04) | LOC 050 |  |
| C7 | TU4 read signal card (C05) | $1 . \mathrm{OC} 050$ |  |
| C8 | TU3 read signal card (C06) | 1.OC 050 |  |
| C9 | TU2 read signal card ( C 07 ) | LOC 050 |  |
| D) | TU1 read signal card (C08) | LOC 050 |  |
| D)1 | TU0 read signal card (C09) | LOC 050 |  |
| D2 | TU address control card ( Cl 0 ) | LOC 050 |  |
| D3 | Communicator, write control signals card (CII) | LOC 050 |  |
| D4 | Communicator, write control signals card (Cl2) | LOC 050 |  |


| Result Code | Definition | MHM Reference | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: | :---: |
| D5 | TU7 write/control card (C13) | LOC 050 |  |
| D6 | TU6 write/control card (C14) | LOC. 050 |  |
| D7 | TU5 write/control card (C15) | LOC 050 |  |
| D8 | TU\& write control card (C16) | LOC 050 |  |
| D9 | TU3 writejcontrol card (C17) | LOC 050 |  |
| E0 | TU2 write/control card (C18) | LOC 050 |  |
| E1 | TU1 write/control card (C19) | LOC 050 |  |
| E2 | TU0 writc/control card (C20) | LOC 050) |  |
| E3 | Nonreportable error card (C23) | LOC 050 |  |
| E4 | Diskette control, CU service panel interface card (C24) | LOC 050 |  |
| E5 | Diagnostic monitor card (C25) | LOC 050 |  |
| E6 | Interface card, channel A (C26) | LOC 050 |  |
| E7 | Interface card, channel B (C27) | LOC 050 |  |
| E8 | Diskette |  |  |
| E9 | Disk drive |  |  |
| F0 | Cartridge opener assembly | CARR 062 |  |
| F1 | Tape unit logic board | CARR 470 |  |
| F2 | S104 transfer valve microswitch | CARR 520 |  |
| F3 | Transfer valve motor | CARR 530 |  |
| F4 | Front check valve | LOC 140 |  |
| F5 | Sl09 lcft tape loop warning switch | LOC 100 |  |
| F6 | Sl08 right tape loop warning switch | LOC 100 |  |


| Result <br> Code | Definution | MIM Reference | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: | :---: |
| 57 | Write enable ring sensor | CARR 640 |  |
| 18 | Tape unit write card | CARR 490 |  |
| 19 | Read write head assembly | CARR 360 |  |
| C) | Capstan motor | CARR On0 |  |
| Cil | S113 lile reel hub switch | CARR 160 |  |
| C.2 | Capstan driver board | CARR 470 |  |
| C:3 | Capstan reel control board | CARR 470 |  |
| 6.4 | File reel hub actuator | CARR 150 |  |
| C5 | Solenoid valve assembly | CARR 410 |  |
| C.6 | S116 high pressure sensor switch | LOC 100 |  |
| C.7 | BOT EOT sensor block | CARR 040 |  |
| C.8 | Sl07 tape lifter switch | CARR 435 |  |
| C.9 | Sl01 cartridge present switch | CARR 061 |  |
| H0) | S102 tape lifter switch | CARR 064 |  |
| HII | File reel rotation rate sensor | CARR 180 |  |
| 112 | File reel driver board | CARR 470 |  |
| 113 | Capstan | CARR 050 |  |
| 114 | Slof thread guide with reels loaded switch | CARR 500 |  |
| 115 | Machine reel rotation rate sensor | CARR 260 |  |
| 116 | Machine reel driver board | CARR 470 |  |
| 117 | S117 low pressure sensor switch | LOC 100 |  |
| I18 | Vacuum pump | CARR 580 |  |
| H9 | Tape unit DC power supply board | CARR 450 |  |


| Resulit <br> Code | Detinition | MIM Reference | I $/ \mathrm{N}$ |
| :---: | :---: | :---: | :---: |
| I) | Pressure pump | CARR 310 |  |
| 11 | Pump drise motor | CARR 340 |  |
| 12 | S11.5 S119 dwor interlock swith ascembly | CARR 200 |  |
| 13 | S114 window interlock switch | CARR 620 |  |
| 14 | S103 cantridge closed uwitch | CARR 64 |  |
| 15 | Window air cylinder | CARR 610 |  |
| 16 | Tape unit read card | CARR 480 |  |
| 17 | Tape unit lan assembly | CARR 460 |  |
| 18 | SR pranel | CARR 120 |  |
| 19 | T manster valse assembly | CARR 540 |  |
| J) | Tape lifter atsembly | CARR 430 |  |
| J 1 | Sl05 tranter salse microswitch | CARR 520 |  |
| J2 | No Action No FRU's |  |  |
| J3 | Go wo the pancl fall isolation guide | REF 1 |  |
| J4 | Go '0 "i"uwer Entry MAP" | REF I |  |
| J5 | Test complete, no errors found |  |  |
| . 16 | Tape unit addressed during load unload |  |  |
| J7 | Inspect top card comnectors |  |  |
| J8 | A manual operation was attempted while tape unit was online and uperational. |  |  |
| J9 | Defective media;dirty tape path |  |  |
| K0 | Rear check valve | LOC 140 |  |


| Result Code | Definition | MIM Reference | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: | :---: |
| K 1 | Cheek write enable ring and sensor |  |  |
| K2 | Check for tape slip/BOT detection |  |  |
| K3 | Writeicontrol card for TU experiencing failure ( $\mathrm{C} 20-\mathrm{C} 13$ ) | LOC 050 |  |
| N0 | $\begin{aligned} & \text { Power control failure - run MAP } \\ & 6503 \end{aligned}$ |  |  |
| P1 | Tape adapter card (01AB2-Q2) |  | 2271192 |
| P2 | Channel interface cards - tape unit |  |  |
| P3 | HDC feature card (01AB2-R4) |  | 2445462 |
| P4 | Block size on tape not compatible with the HDC feature |  |  |
| Q0 | Online offline switch |  |  |
| Q1 | Power on'off switch |  |  |
| R0) | Fxpected dats |  |  |
| R1 | Use device FSC generated and go to 3422 M1M Start section |  |  |
| R2 | System $/ 38$ ribbon cable <br> (01AB2-Q2W2) |  |  |
| R3 | Tailgate connectors (01DA2-bottom) |  |  |
| R4 | Exteraal tag cable |  |  |
| R5 | FSC for device |  |  |
| R6 | System/38 ribbon cable (01AB2-Q2X2) |  |  |
| R7 | Tailgate conncetors (01DA2-top) |  |  |
| R8 | External cable (bus) |  |  |
| R9 | Channel inierface cards in tape control unit |  |  |


| Result Code | Deflimition | Hhn Reference | $1 / \mathrm{N}$ |
| :---: | :---: | :---: | :---: |
| S0 | VNIC Pasture (ION1) |  |  |
| S1 | VNC lature |  |  |
| S2 | SCA related fature (IOM OI interliace |  |  |
| 53 | Decomprese data crob |  |  |
| S4 | Failed during vary on |  |  |
| S5 | Go to the $3422 \mathrm{M} / \mathrm{Mi}$-tart section |  |  |
| S7 | Command rejoct |  |  |
| S8 | rost crent |  |  |
| S9 | FOB time out |  |  |
| U4 | Experted tenult of error canderd by <br>  |  |  |
| U5 | Unexpected result for llb C feature |  |  |
| / $/ 1$ |  |  |  |
| / $/ 2$ | Go to channel crror log analy in MAP |  |  |
| 73 | Interpmer (0) AB2 Q2Y and R+W. Q27. and R4X) |  |  |
| 27 | Volume SDR counter were reset |  |  |

## SCA Detected SRC Table

Lhe position 2, 3, 4, 6, 7, 8 of the SRC lo tind the Result Code.
We the Resulf Code and Probability to determine the action or parts reyuited.

```
Pusiton: Mmat Jype
() (immot!)etermine
| Nontel lo(0
2. Nowdel 200
{ Nowel?
4 Monlel4
S Mowels
B Noxdelo
4) Naxclis
```

7 Noxdel 7 () Nllwher
$\therefore$ Nowds 1 Iype 13 (Mod 5 only)

| $\begin{array}{ll} \operatorname{SRC} \\ 1234 & 5678 \end{array}$ | Definition | For resull code dedinitions, (see 1)8 and 1)9 ( and 2 <br> Result Codes and Probability \% |
| :---: | :---: | :---: |
| $1100 \times 101$ | SI error during SCA MCLB analysis ROS access. | 131 (1) \230 A110 |
| $1100 \times 102$ | Read CPU storage error during ROS analysis | 1313513630 13225 1110 |
| $1100 \times 103$ | Error detected by HMC during MSSL. | 1/19913201 |
| $1100 \times 1$ | Error detected by HMC diring FOSt). | 719913201 |
| $1100 \times \mathrm{N05}$ | Error starting FOSI) (CPU'scan error). | 1/199 1201 |
| $1311 \times \mathrm{X} 01$ | SCA is not able to communicate with 62PC-1. | $\begin{aligned} & \text { C140 C22513110 D210 } \\ & \text { D105 N105 D302 C401 } \\ & \text { A301 L101 } \end{aligned}$ |


| $\begin{array}{ll} \operatorname{SRC} \\ 12.34 & 5678 \end{array}$ | Delinition | Jor result eote delinitions, sere 1$) 8$ and I)9 (ard 2 <br> Recoult Codes and Probability "/o |
| :---: | :---: | :---: |
| $1311 \times 102$ | SCA is mot able to communicate wh si2pC-I. | C360 C140 |
| $1311 \times 103$ | SCA is not able to communicate wh 62PC-I. | C335C130 C2251)105 <br> D) 305 |
| $1512 \times 101$ | SCA is not able to communicate with 72MD. | E160 C240 |
| $1512 \times 102$ | SCA is not able to commonicate with 72M1). | 1.235 1:330 li125 C210 |
| $1512 \times 103$ | SCA is not able to communicate with 72.110 . | 123501630 C220 |

SCA Detected RC Table

## SCA Detected RC Table

| Resull <br> Code | 1)efinition | $P / N$ |
| :---: | :---: | :---: |
| AI | SCA comsote 1OC card (0) AAI B2) | 4361306 |
| A2 | SCA combule card I (0)AAIC2) | 4202410 |
| 13 | SCA console card ? (01AAI $) 2$ ) | $\begin{aligned} & \mathbf{4 2 3 7 0 7} \\ & \mathbf{4 2 2 3 7 0 8} \text { (katakima) } \end{aligned}$ |
| 131 | Plandr (0)AP1.) | $\begin{aligned} & 8328300(\operatorname{Mod} 3-5) \\ & 4361076(\operatorname{Mol} 1-2,(1-8) \\ & 6370231 \text { (Mod 9. A-F) } \end{aligned}$ |
| 132 | ()nc ar more contmature card (0) ABI S2. 12. (…V2, R2) | $\begin{aligned} & 5122494 \text { (Non 3) } \\ & 6229416 \text { (Nod 1-2. } 4-9 \text {. } \\ & \text { A-F) } \\ & 8691997 \text { (Mod } A-1 .) \end{aligned}$ |
| 136 |  12, K2, 1, 2, M2, N2, P2) | $\begin{aligned} & 4599188 \text { (Mod 3, 4) } \\ & 7378022 \text { (Mod } 5 \text { Type A) } \\ & 5658718 \text { (Mod } 5 \text { Type B) } \\ & 5120271 \text { (Mod 7) } \\ & 4772607 \text { (Mod 1, 2, 6.8. } \\ & 9, \text { A.B.F) } \\ & 2444962 \text { (Mod C. D) } \\ & 65 \times 5750 \text { (Mod E) } \\ & \hline \end{aligned}$ |
| C1 | 62PC adapter (A33-(340) (01AA1 (i2) | 2771438 |
| C2 | Chock card (0) A, 11 12, N2) | $\begin{aligned} & 27710642550550 \text { (okd } \\ & \text { wiylc) } \end{aligned}$ |
| C3 | 62PC device. on-site service is required |  |
| C4 | One or more 10C cands | 4361306 |
| D)1 | Channel converter card (01AB1 F2) | $\begin{aligned} & 2549055 \text { (Mod 1-8) } \\ & 2145900(\mathrm{Mod} 9 . \mathrm{A}-\mathrm{F}) \end{aligned}$ |


| Result Code | Definition | $1 \mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| D2 | Channel terminator card | 2549040) |
| D3 | Channel converter card (01ABI E2) | $\begin{aligned} & 4202240(\operatorname{Mod~1-8}) \\ & \mathbf{2 4 4 5 9 0 2}(\mathrm{Mod} \mathrm{9}, \mathrm{~A}-\mathrm{F}) \end{aligned}$ |
| E1 | 72 MD adapter card (01AAI H2) | 4360810 |
| E2 | Damaged or wrong SLVIA |  |
| C. 3 | 72 MD device problem, on-site service is required |  |
| L. 1 | $33 \% 0$ adapter card (01AAI K 2 or M2) | 2444872 |
| N1 | Any 1/O adapter on-site service is required |  |
| 7.1 | A error occurred while the system was trying to Force Operator Shutdown or a main store load. IMPL the system, with no retry, to see if the diagnostics will find the problem. |  |

## X .25 SRC Table $(\mathrm{OU}=5 \mathrm{C}, 5 \mathrm{E})$

Use powtion $56,7,8$ of the $\operatorname{SRC}$ on tind the Rewate Code.
Use the Result Code and Probability to determine the action or parm reyured.

| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For resull code detinitions, see 1)12 Card 2 <br> Result Codes and Probahility "㕷 |
| :---: | :---: | :---: | :---: |
| 00 xx | 7000 | Hardware failure | A09\% |
| 00 xx | 7001 | Hardware failure | A09, 13101 |
| $00 \times \mathrm{x}$ | 7002 | Hardware fature | A095 1104 13101 |
| 000 x | 7003 | Hardware failure | \050 M150 |
| $000 \times x$ | 7004 | VAIC or hardware failure | A09: 13101 V101 |
| $00 . \mathrm{xx}$ | 7005 | Harchare faiture | \098 \101 B10: |
| 00 xx | 7006 | Hardware failure | \075 \124 13101 |
| 00 xx | 7007 | Hardware failure | $\begin{aligned} & \text { M179 A010 M110 } \\ & \text { B101 } \end{aligned}$ |
| 00 xx | 7008 | Hardware failure | A090 \109 1101 |
| 00 xx | 7020 | VMC or IOP Microcode is railing | V190 M110 |
| 00 xx | 7021 | IOP Microcode is failing | M199 |
| $00 \times 8$. | 7022 | Missing X. 25 Module | On-bite service reyured |
| 00xx | 7030 | Hardware lailure | $\begin{aligned} & \mathrm{A} 140 \mathrm{M} 240 \mathrm{C} 115 \\ & \mathrm{CO} 05 \end{aligned}$ |
| 00xx | 7031 | VMC or hardware failure | V150 A (040 M110 |
| 00 xx | 7032 | Hardware failure | C150CO30 A120 |
| 00xx | 7050 | Harduare failure | A150 A049 13101 |


| $\begin{aligned} & \text { SRC } \\ & 12.34 \end{aligned}$ | 5678 | Defimition | For result code definitions, see I)12 ('ard 2 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| (0) 0.1. | 7051 | Hardware labiure | A175 A024 13i0! |
| 00 xx | 7052 | Hardware lailure | A175 A024 M201 |
| $0)^{(0) X, x}$ | 7053 | Ilareware failure | A190 A $)_{10}$ |
| 000 x | 7054 | Hatielware fature | $\begin{aligned} & \text { M270 C115 A110 } \\ & \text { C005 } \end{aligned}$ |
| $000 \times \mathrm{x}$ | 70.5 | Hardware lailure | A199 |
| $000 \times \mathrm{x}$ | 9XXX | Sce hardeopy MAP 5947 |  |
| $00 \times 1$ | AXXX | Sce haricopy MAP 59.47 |  |
| $000 \times x$ | BXXX | Sce hardeopy MAP 59.47 |  |
| $000 \times 8$ | CXXX | See hardcopy MAP 5947 |  |
| O)00\% | DXXX | Sec hardeopy MAP 59.47 |  |
| 000 xx | EXXX | Sec hardcopy MAP 5947 |  |

X. 25 irC Table

## X. 25 RC Table

| Result Code | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| A0 | The IOP card at location 01AB2 G2 | 2771814 |
| A 1 | The 1OA card at location 01AB2 F2 | 6370136 |
| B1 | The B2 board ot location 01AB2 | 6370078 |
| C0 | Internal catide | See table 1 |
| Cl | External cable | See table 1 |
| M11 | IOP Microcode error |  |
| M 2 | The moden is failing |  |
| T1 | 1OP Top card connector cable 01AB2 G2 | 2445673 |
| V1 | VMC Error |  |

TABLE I

|  | V.24 | V.35 | X. 21 |  |
| :--- | :--- | :--- | :--- | :--- |
| Port \# 1 | $\mathbf{2 4 4 5 9 0 5}$ | $\mathbf{2 4 4 5 9 0 7}$ | $\mathbf{2 4 4 5 9 0 9}$ | Internal cable |
| Port \# 2 | 2445906 | 2445908 | $\mathbf{2 4 4 5 9 1 0}$ | Internal cable |
| Port \# 1 or 2 | 2453129 | $\mathbf{2 4 4 5 8 7 0}$ | $\mathbf{2 4 4 5 8 6 9}$ | 20 ft external cable |
| Port \# 1 or 2 | $\mathbf{2 4 4 5 8 1 0}$ | $\mathbf{2 4 4 5 8 1 1}$ | $\mathbf{2 4 4 5 8 0 9}$ | 40 ft external cable |

D14 Card 2

## CPU SRC Table

Use position 6, 7, 8 of the SRC to find the Result Code.
lae the Result Code and Probability to determine the action or parts required.
Powition 2 - Machine Clieck Type
$0=$ First machine check
$1=$ Machine check occurted during analysis of lirst machine cheek
2 = Machine check occurred during IMPI/AIMPL diagnostics

Position 5 - Mociel Type
0 = Cannot determine
1 - Model 100
$2=$ Model 200
$3=$ Model 3
$4=$ Model 4
$5=$ Model 5
$6=$ Model 6
$7=$ Model 7
$8=$ Model 8
$9=$ Model 18
$\mathrm{A}=$ Model 20
$B=$ Model 400
$C=$ Mocicl 40
$\mathrm{D}=$ Model 600
$E=$ Model 700
$F=$ Model 300

## CPU SRC Table

| $\begin{array}{ll} \text { SRC } & \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see E.10) thm E:17 ('ard 2 <br> Result Codes and Probability \% |
| :---: | :---: | :---: |
| $2 \mathrm{XOO} \times \mathrm{X} 02$ | MDI Detected had ending statu; | A250 A330 \220 |
| $2 \mathrm{X} 000 \times \mathrm{X} 03$ | Planar Pailure | $\begin{aligned} & B 160136.5[3403 \quad 6901 \\ & 13301 \end{aligned}$ |
| $2 \mathrm{X00} \times \mathrm{X} 04$ | CPU controt store read data parity check | $\begin{aligned} & 13275131201340313501 \\ & 13301 \end{aligned}$ |
| 2800 AX05 | Invalid CPU control storage array | 13275 1312.4 7501 |
| $2 \times 00 \times \times 06$ | VAT detected machine check | 131997501 |
| $2 \mathrm{X00} \times \mathrm{XXO}$ | VAT detected machine check | $\begin{aligned} & \text { C16.5 } 1313013403 \quad \mathbf{G 9} 01 \\ & B 301 \end{aligned}$ |
| 2 X 00 XX08 | VAT detected machine check | $\begin{aligned} & \mathrm{C} 26.51313013403(3901 \\ & 13301 \end{aligned}$ |
| $2 \mathrm{X} 000 \times \mathrm{X} 09$ | VAT detected machine check | $\begin{aligned} & \text { G3651313013403 G901 } \\ & 13301 \end{aligned}$ |
| $2 \mathrm{XOO} \times \mathrm{XO}$ | VAT detected machine check | $\begin{aligned} & \text { G40.51313013403 G901 } \\ & 13301 \end{aligned}$ |
| $2 \mathrm{X} 00 \times \mathrm{XXOB}$ | VAT detected machine check | $\begin{aligned} & \text { G5651313013403 G901 } \\ & \text { B301 } \end{aligned}$ |
| $2 \mathrm{X00} \times \mathrm{XOC}$ | VAT detected machine check | $\begin{aligned} & \text { G665 13130 B403 G901 } \\ & 13301 \end{aligned}$ |
| $2 \times 00 \times \mathbf{X O D}$ | VAT detected machine check | $\begin{aligned} & \text { G765 1313013403 G901 } \\ & 13301 \end{aligned}$ |
| $2 \mathrm{X} 00 \times \mathrm{XXOE}$ | VAT detected machine cheek | $\begin{aligned} & \text { C865 B130 B4403 G901 } \\ & \text { B301 } \end{aligned}$ |


| $\begin{aligned} & \text { SRC } \\ & 1234 \\ & \hline \end{aligned}$ | 5678 | Definition | For result code detimitions, see ElO than E.I7 Caral 2 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 2X00 | XXOF | Hardware machine chock white in wait state | 13685 13110 B205 |
| 2X00 | XX10 | HMC machine check followed by hardware detected machine check |  |
| $2 \times 00$ | XX 11 | HMC tinceout occurred | $\begin{aligned} & 13170132201330513404 \\ & 2701 \end{aligned}$ |
| 2×00 | XX12 | Machinc check occurred and MCLR address is wrong | $\begin{aligned} & 13658 \quad 13130132082601 \\ & \mathrm{Z8} 011330113401 \end{aligned}$ |
| 2X00 | XX13 | Hardware detected machine check | $\begin{aligned} & 13685 \mathrm{~B} 11013403 \mathrm{C9} 01 \\ & \mathrm{~B} 301 \end{aligned}$ |
| $2 \times 00$ | $\times \times 14$ | Hardware detected machine check | B275 13120 B403 1350), B301 |
| $2 \times 00$ | XX15 | VAT machine check with card swap active | $\begin{aligned} & \mathrm{G} 26513130 \mathrm{B4} 03 \mathrm{G} 901 \\ & \mathrm{B3} 01 \end{aligned}$ |
| 2X00 | $\times \mathrm{X} 16$ | VAT machine chcck with card swap active | $\begin{aligned} & \text { C165 B130 13403 C901 } \\ & \text { B301 } \end{aligned}$ |
| $2 \times 00$ | XX17 | VAT detected machine check | G175131201340313302 |
| 2X00 | XX18 | VAT detected machine check | G275 B120 B403 B302 |
| $2 \times 00$ | XX19 | VAT detected machine check | C375 B120 B403 B302 |
| $2 \times 00$ | $\times \mathrm{X1A}$ | VAT detected machinc check | G475 B120 B403 13302 |
| $2 \times 00$ | XX1B | VAT detected machine check | G575 B120 B403 B302 |
| $2 \times 00$ | $\times \mathrm{X1C}$ | VAT detected machinc check | G675 B120 B403 B302 |

## CPUSRC Table

| $\begin{array}{ll} \text { SRC } \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see E:10 thru E:17 Card 2 <br> Result Codes and Proba!itity \% |
| :---: | :---: | :---: |
| 2 X 00 XXID | VAl detected machine check | C775 13120 1340313302 |
| $2 \mathrm{X} 000 \times \mathrm{X} 1 \mathrm{E}$ | VAT detected machine check | C875 13120 134031330? |
| 2 X 00 XXIF | VAT machine check with card swap active | C27513120 13403, 13302 |
| $2 \times 00 \times 20$ | VAT machine check with card swap active | C,175 13120 IB403 13302 |
| $2 \mathrm{X} 000 \times 21$ | VAT detected machine check | 13175 C120 B403 13302 |
| $2 \times 00 \times 22$ | VAT detected machine check | 13175 C:220 1340313302 |
| $2 \times 00 \times 23$ | VAT detected machine check | 13175 (3320 13403 13302 |
| $2 \times 00 \times 24$ | VAT detected machine check | 13175 G420 13403 B302 |
| $2 \mathrm{X} 000 \times 25$ | VAT detected machine check | 13175 C520 13403 13302 |
| $2 \mathrm{X} 00 \times \times 26$ | VAT detected machine check | B175 G6201340313302 |
| $2 \times 00 \times 27$ | VAT detected machine check | 13175 C720 13403 13302 |
| $2 \times 00 \times 28$ | VAT detected machine check | 13175 C820 13403【3302 |
| $2 \times 00 \times 129$ | VAT machine check with card -Wap active | 13175 G220 13403 13302 |
| $2 \mathrm{X} 00 \times \mathrm{X} 2 \mathrm{~A}$ | VAT machine check with card swap active | 13175 G120 13403 13302 |
| $2 \mathrm{~K} 00 \times \times 2 \mathrm{~B}$ | HMC check sum linied | B1:0 A 235 A324 $\mathbf{2 7 0 1}$ |
| $2 \mathrm{X} 00 \times \times 2 \mathrm{C}$ | Main store overlay failure | $\begin{aligned} & \text { B660 } \\ & \text { B13 } 13+13403 \quad 13301 \\ & \text { G901 } \end{aligned}$ |
| $2 \mathrm{X} 00 \times \mathbf{X 2 1}$ | Control store failure | $\begin{aligned} & \text { B775 B120 B403 B501 } \\ & \text { B301 } \end{aligned}$ |
| 2X00 XX2E | Control store failure | $\begin{aligned} & \text { B875 B120 B403 B501 } \\ & 13301 \end{aligned}$ |

## D18 Card 2

| $\begin{aligned} & \text { SRC } \\ & 123.4 \end{aligned}$ | 5678 | Definition | For resull code delinitions, see C17 thru D5 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | IP334 | Formatter system adapter failure. | D)8 132 D2 131 |
| 0052 | 1835 | Formatter/system adapter failure. | I)7 [32 D2 [31 |
| 0052 | 1336 | Formatter/syctem adapter failure. | D6 132 D2 131 |
| 00.52 | F1337 | Formatter system adapter failure. | D) [32 D2 I3I |
| 0052 | F1338 | Formatter/system adapter failure. | D2 D4 D3 132 |
| 0052 | 1-339 | Formatter system adapter failure. | D2 D4 D3 132 |
| 0052 | FB3A | Formatur system adapier failure. | D2 D4 D3 132 |
| 00.52 | F133B | Formater system adapter failure. | D2 D4 D3 132 |
| 0052 | FB3C | Formatersystem adapter failure. | D2 D4 D3 [32 |
| 0052 | FB3D | Formatter syctem adapter failure. | D)2 D4 D3 [32 |
| 0052 | FB3E | Formattersystem adapter failure. | D2 D4 D3 132 |
| 0052 | FB3F | Formatter system adapter failure. | D2 D4 D3 132 |
| 0052 | FB40 | Formatter/system adapter failure. | B6 137 C1 B4 |
| 0052 | FB+1 | Formatter/system adapter failure. | B6 137 C1 B4 |
| 0052 | FB42 | Formatter/system adapier failure. | B6 B7 C1 134 |
| 0052 | FB43 | Formatter/system adapter failure. | 136 B7 C1 134 |
| 0052 | FB44 | Formatter/system adapter failure. | B6 B7 C1 B4 |
| 0052 | FB45 | Formatter/system adapter falure. | B6 B7 C1 B4 |
| 0052 | FB46 | Formatter/system adapter failure. | 136 B 7 Cl 134 |
| 0055 | FB47 | Formatter system adapter failure. | B6 B7 C1 B4 |
| 0052 | FB48 | Formatter/system adapter failure. | E2 122 134 D4 |
| 0052 | FB49 | Formater/system adapter failure. | E1 D2 B4 D4 |


| $\begin{aligned} & \text { SRC } \\ & 123.4 \end{aligned}$ | 5678 | Definition | For resull code definitions, see ( 17 thru D5 (ard 2 <br> Ressult Code |
| :---: | :---: | :---: | :---: |
| 00.52 | $\mathrm{FB}+\mathrm{A}$ | Formatter sytem adapter failure. | 100 D2 134 D4 |
| 0052 | $1 \cdot 13413$ | Formatter system adapter failure. | D9 D2 134 D4 |
| 0052 | FB4C | Formater system adapter failure. | D8 D2 234 D 4 |
| 00.52 | FB4D | Formattersystem adapter failure. | D7 I) 2 BH D 4 |
| 00.52 | FB4E | Formatter/system adapter lailure. | D6 D2 134 I) 4 |
| 00.52 | FB4F | Formatter system adapter failure. | D5 D2 34 D4 |
| 00.22 | (1350 | Formattersystem adapter falure. | $136137 \mathrm{Cl\mid} \mathrm{15}$ |
| 0.52 | FB51 | Formatter uytem adapter faiure. | 136137 Cl 155 |
| 00.52 | $\mathrm{FB52}$ | Formatter ¢ytem adapter failure. | 136137 C1 155 |
| 0052 | 11353 | Formatter ¢istem adapter failure. | 136137 C1 15: |
| 0052 | 1 P 54 | Formatter system adapter, falure. | 136137 C1 15.5 |
| 00.52 | FB55 | Formatter system adapter failure. | 136137 C 1 ES |
| 0052 | F1356 | Formatter system adapter failure. | 136137 C 115 |
| 0052 | F1357 | Formater/system adapter failure. | 136137 C1 15 |
| 00.52 | F1358 | Formatter/system adapter failure. | C0 114 136137 |
| 00.52 | FB59 | Formatter system adapter failure. | C0 1) $4136 \mathrm{B7}$ |
| 00.52 | FB5A | Formatter/system adapter failure. | C0 1) +136137 |
| 000.52 | FB5B | Formater/system adapter failure. | C0 D) $4136 \quad 137$ |
| 0052 | FB5C | Formatter;system adapter failure. | C0 D) 4136137 |
| 00152 | Fi35D | Formatter/system adapter failure. | C0 D 4 B6 137 |
| 0052 | FB5E | Formatter/system adapter failure. | C0 D) $+136 \mathrm{B7}$ |
| 0052 | FB5F | Formatterisystem adapter failure. | C0 D4 136 137 |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code delinitions, see ( 17 thon 15 (ard 2. <br> Result Code |
| :---: | :---: | :---: | :---: |
| 00.52 | F1360 | Formatter/system adapter lailure. | 122 A9 I88 135 |
| 0052 | FB61 | Formatter/system adapter failure. | E1 19 I88 135 |
| 00.52 | F1362 | Formatterisystem adapter lailure. | 10 A9 188 35 |
| 00.52 | FB63 | Formatter/system adapter failure. | D9 A9 F8 135 |
| 0052 | FB64 | Formatter/system adapter failure. | D8 19188135 |
| 0052 | 1-1365 | Formatter/system adipter failure. | 1)7 19 F8 135 |
| 00.52 | FB66 | Formatter/system adapter failure. | D6 A9 188 135 |
| 0052 | F1367 | Formatter/system adapter failure. | i) 5 A9 F8 $\mathrm{B}_{5}$ |
| 00.52 | F1368 | Formatter eystem adapter failure. | $12.19181) 4$ |
| 0052 | 11369 | Formattersystem adapter failure. | L1 A918 1)4 |
| 0052 | FB6A | Formatter system adapter failure. | E0 1918 D4 |
| $00: 2$ | FB6B | Formatter/systemi adapter failure. | D9 A9 F8 D4 |
| 00.52 | [136C | Formater/system ddapter failure. | D8 A9 188 14 |
| 0052 | FB60) | Formatter/system adapter failure. | D) 79 F8 D 4 |
| 0052 | FB6E | Formatter/system adapter failure. | D6 $19 \mathrm{F8} \mathrm{D} 4$ |
| 0052 | FB6F | Formatter/system adapter failure. | D5 $19 \mathrm{F8} \mathrm{D}_{+}$ |
| 0052 | FB70 | Formatter/system adapter failure. | $\mathrm{L} 219 \mathrm{D}+\mathrm{f8}$ |
| 0052 | FB71 | Formatter/system adapter failure. | L1 A9 1) 4 I 8 |
| 0052 | F1372 | Formatter/system adapter failure. | 10 A9 D + 188 |
| 0052 | FB73 | Formatter/system adapter failure. | D9 19 D 4 F 8 |
| 0052 | FB74 | Formatter/system adapter failure. | D8 A9 D4 48 |
| 0052 | FB75 | Formatter/system adapter failure. | D) $79 \mathrm{D} 4 \mathrm{F8}$ |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Detimition | For resull code definitions, see (17 thru D5 Card 2 Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | F1376 | Formatter sytem adapter failure. | D6 A9 D4188 |
| 0052 | 11377 | Formatter sytem adapter falure. | D5 A9 D4 F88 |
| 0052 | F1378 | Formatler ssatem adapter failure. | $12 \mathrm{~A} 9 \mathrm{F8} 135$ |
| 0052 | F1379 | Formatter cystem adapter falure. | E1 A9 F8 135 |
| 0052 | FB7A | Formatler wstem adapter failare. | L0 A9 F8 135 |
| 0052 | FB7B | Formatuer ystem adapter failure. | D9 A9 188 135 |
| 0052 | FB7C | Formatter wism mapter failure. | D8 A9 188 35 |
| 00.52 | F1371) | Pormatler sytem atapter failure. | D) 79158 BJ |
| 00.52 | 1337 | Formater sylem atapter tailure. | D6 19 188 135 |
| 0052 | FB7F | Formatter system adapter failure. | D5 A9 I88 35 |
| 0052 | FB80 | Formatter system adapter failure. | E 2 B 2 F 1 |
| 0052 | FB81 | Formather system adipter failure. | E1 132 Fl |
| 00552 | F1382 | Formater system adapter failure. | E) 132 F 1 |
| 0052 | FB83 | Formather sybtem adapter failure. | D9 132 F1 |
| 0052 | FB84 | Formatter system adapter failure. | D8 132 FI |
| 0052 | F1385 | Formatter system adapter failure. | D) 132 F 1 |
| 0052 | F386 | Formattersystem adapter failure. | D6 62 Fl |
| 0052 | FB87 | Formater system adapter failure. | D5 132 F1 |
| 0052 | FB88 | Formatter system adapter failure. | E2 132 D3 A6 |
| 0052 | F1389 | Formatter/system adapter failure. | E1 B2 D3 A6 |
| 0052 | FB8A | Formatter/system adapter failure. | E0 B2 D3 A6 |
| 0052 | FB813 | Formatter'system adapter failure. | D9 132 D3 A6 |


| $\begin{array}{\|l\|l} \text { SRC } \\ 1234 \end{array}$ | 5678 | Definition | For resulf code definitions, see C.17 thru D5 Card? <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | FB8C | Formatter/system adapter failure. | D8 132 D3 A6 |
| 0052 | FB8D | Formatter/system adapter failure. | D) 7 [32 D3 A6 |
| 0052 | FB8E | Formatter/system adapter failure. | D6 132 D3 A6 |
| 0052 | FB8F | Formatter/system adapter failure. | D5 B2 D3 A6 |
| 0052 | FB90 | Formatter/system adapter failure. | E2 B2 D3 A6 |
| 0052 | FB91 | Formatter'system adapter failure. | E1 B2 D3 A6 |
| 0052 | FB92 | Formatter/system adapter failure. | E0 B2 D3 A6 |
| 0052 | FB93 | Formatter/system adapter failure. | D9 P 2 D3 A6 |
| 0052 | FB94 | Formatter/system adapter failure. | D8 132 D3 A6 |
| 0052 | FB95 | Formatter/system adapter failure. | D7 B2 D3 A6 |
| 0052 | FB96 | Formatter/system adapter failure. | D6 B2 D3 A6 |
| 0052 | FB97 | Formatter/system adapter failure. | D5 B2 D3 A6 |
| 0052 | F1398 | Formatter/system adapter failure. | E2 B2 F1 |
| 0052 | FB99 | Formatter/system adapter failure. | E1 B2F1 |
| 0052 | F39A | Formatter/system adapter failure. | E0 B2 Fl |
| 0052 | FB9B | Formatter/system adapter failure. | D9 B2 F1 |
| 0052 | FB9C | Formatter/system adapter failure. | D3 B2 F1 |
| 0052 | FB9D | Formatter/system adapter failure. | D7 B2 F1 |
| 0052 | FB9E | Formatter/system adapter failure. | D6 B2 F1 |
| 0052 | FB9F | Formatter/system adapter failure. | D5 B2 F1 |
| 0052 | FBA0 | Formatter/system adapter failure. | $16 \mathrm{F9}$ D1 E2 |
| 0052 | FBAl | Formatter/system adapter failure. | $16 \mathrm{F9}$ D0 El |


| $\begin{array}{\|ll\|} \hline \text { SRC } & \\ 1234 & 5678 \end{array}$ | Detinition | For result code definitions, see C17 thru D5 Card 2 <br> Result Code |
| :---: | :---: | :---: |
| 0052 IPBA2 | Formatter'system adapter failure. | 16 F 9 C 9 E 0 |
| 00.52 FBA3 | Formatter system adapter failure. | $16 \mathrm{F9}$ C8 D9 |
| 0052 FibA4 | Formatter system adapter failure. | 16 F 9 C 7 D 8 |
| 0052 FBA 5 | Formatter'system adapter failure. | 16 F 9 C6 D7 |
| 0052 FBA 6 | Formatter system adapter failure. | 16 F 9 C 5 D 6 |
| 0052 F13A7 | Formattersystem adapter failure. | I6 F9 C4 D5 |
| 00.52 「13A8 | Formatter system adapter failure. | I6 F9 D1 E2 |
| 0052 FBL 2.9 | Formatter system adapter failure. | I6 F9 D0 E1 |
| 0052 FBAA | Formatter system adapter failure. | I6 F9 C9 E0 |
| 0052 FBAB | Formatter system adapter failure. | 16 F 9 C 8 D 9 |
| 0052 FBAC | Formatter system adapter failure. | 16 F 9 C 7 D 8 |
| 0052 FBAD | Formatter system adapter failure. | 16 F 9 C 6 D 7 |
| 0052 FBAE | Formatter system adapter failure. | $16 \mathrm{F9}$ C5 D6 |
| 0052 FBAF | Formatter system adapter failure. | I6 F9 C4 D5 |
| 0052 FBB30 | Formatter/system adapter failure. | D1 E2 A0 A1 |
| 0052 FB3B1 | Formatter, system adapter failure. | D0 E1 A0 A1 |
| 0052 FBBB 2 | Formatter system adapter failure. | C9 E0 A0 A1 |
| 0052 FBB33 | Formatter, system adapter failure. | C8 D9 A0 A1 |
| 0052 FBBB4 | Formatter, system adapter failure. | C7 D8 A0 A1 |
| 0052 FBB35 | Formatter/system adapter failure. | C6.D7 A0 A1 |
| 0052 FBBB6 | Formatter/system adapter failure. | C5 D6 A0 A1 |
| 0052 FBBB 7 | Formatterisystem adapter failure. | C4 D5 A0 Al |


| $\begin{aligned} & \mathrm{SRC} \\ & 123.4 \end{aligned}$ | 5678 | Detinition | For result code definitions, see (17 lhm 1)5 Card 2 <br> Resull Code |
| :---: | :---: | :---: | :---: |
| 00.52 | 1'3138 | Fomather esacm adaper fatuse. | C3 D) 1 E2 A0 |
| 0052 | FBB9 | Formatke ゝ̧tem adapter failure. | C3 D0 E. 1 入 |
| 0052 | FBBA | Fommater sycm atapter failure. | C3C91:0 A0 |
| 001.52 | FBBBI | Formater sylem adapter tailure. | C3 C8 D9 10 |
| (0)0.52 | 1 1313 C | Formather syemem adapter lature. | C3 (7) 8 A0 |
| (0152 | F13131) | Promater sytem adapter failure. | C3 C6 D) ${ }^{\text {d }}$ |
| 00.52 | FBBEE | Formatcer sylem adapter failure. | C3 C5 D6 10 |
| 0 | FBBEF | Formater syctur adapter failure. | (3 C4 D) ${ }^{\text {d }}$ (1) |
| 00.2 | ICOO | Formater \stem adapmer failure. | 134137133131 |
| 0053 | HCO | Fommatter sytem adapter failute. | 131137 13+138 |
| 0053 | $\mathrm{HCO}^{\mathrm{C}}$ | Fommater -ysum atapter lialute. | A8 130 149 B4 |
| (10)52 | HCO | Formatter exacm atapter failuce. | $133 \mathrm{~B}+131$ A8 |
| 00.52 | HCO 4 | Formater sytem adapter failure. | 137181134 |
| 00.52 | IC08 | Formatter yshem adapter failure. | C1 L5 |
| 00.52 | 1 C 10 | Formatter ystem atapter failure. | C. 136 E 5135 |
| 0052 | $\mathrm{FCl1}$ | Formater system adapter failure. | A9 130 A 8 B1 |
| 0052 | $\mathrm{FCl}_{2}$ | Formatter ustem adapter bailure. | C1 130 136 A8 |
| 0052 | $\mathrm{HCl}^{1}$ | Formatter sybtem adapter falure. | 15 A8 A3 16 |
| 0053 | 1 C 15 | Formatter uyten adapter failure. | 134 133 131 38 |
| 00.52 | $\mathrm{HCl}^{6}$ | Formatter sytem adapter falure. | 134 133 131 138 |
| 0052 | FC17 | Formattersystem adapter talure. |  |
| 0052 | FC19 | Formater system adapter failure. | 134 137 A5 [31 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see C17 thru D5 Card: <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | FC 1 A | Formatter/system adapter lailure. | A9 130 A8 131 |
| 0052 | FC1B | Formatter/system adapter failure. | 13018 A 9 Cl |
| 00.52 | FC20 | Formatter/system adapter failure. | 134 A3 137 A5 |
| 0052 | FC21 | Formatter/system adapter failure. | 138 132 13134 |
| 00.52 | HC22 | Formatter/system adapter failure. | A5 134138137 |
| 0052 | FC23 | Formatter/system adapter failure. | A4 A7134 A5 |
| 0052 | HC24 | Formatter/system adapter failure. | 134138 133 A4 |
| 00.52 | FC25 | Formatter/system adapter failure. | 132131133134 |
| 0052 | FC26 | Formatter system adapter failure. | 15 138 134133 |
| 0052 | HC27 | Formatter/system adapter faiture. | 134137131 133 |
| 0052 | FC28 | Formatter/system adapter failure. | 134 17 131 19 |
| 0052 | FC29 | Formatter/system adapter failure. | A8 17 A5 A9 |
| 0052 | FC2A | Formatter/system adapter failure. | A8 A5 A9 138 |
| 0052 | FC2B | Formatter/system adapter failure. | 134 13137 A5 |
| 0052 | FC2C | Formatter/system adapter failure. | 134 A3137 15 |
| 0052 | FC2D | Formatter/system adapter failure. | 134 A4137 $\mathbf{1 5}^{5}$ |
| 0052 | FC2E | Formatter/sysiem adapter failure. | 132 133134 131 |
| 0052 | FC2F | Formatter/system adapter failure. | $135138 \quad$ A5 134 |
| 0052 | FC30 | Formatter/system adapter failure. | 138134133 |
| 0052 | FC31 | Formatter/system adapter failure. | 134138 133 |
| 0052 | FC40 | Formatter/system adapter failure. | A8 17 Cl 134 |
| 0052 | FC41 | Formatter/system adapter failure. | A8 C1 136 L5 |

$\left.\begin{array}{|ll|l|l|l|}\hline & & & \begin{array}{l}\text { For result code definitions, } \\ \text { SRC } \\ 1234\end{array} & 5678\end{array}\right)$

| $\begin{aligned} & \mathrm{sRC} \\ & 12.34 \end{aligned}$ | 5678 | Delimition | For result code definitions, see ( 17 Ihru D)5 Card 2 <br> Kesult Code |
| :---: | :---: | :---: | :---: |
| 0052 | HC5A | Formater sybem adapter Cailure. | B1 132 134 133 |
| 0052 | HCSB | Formatter system adapter failure. | B1 B2 D3 A6 |
| 00052 | HCSC | Formatter system adapter failure. | 131 132 134 133 |
| 0052 | FCSD | Formatter system adapter fature. | 134 B3 132 B1 |
| 00052 | HC61 | Formatter system adapter failure. | 131 132 133 |
| 0052 | $\mathrm{HCO}_{2}$ | Formatter system adapter failure. | 132.133 131 135 |
| 00.52 | IC63 | Formatter system adapter failure. | $135138131 \quad 132$ |
| 00152 | $\mathrm{I}^{\mathrm{C}} \mathrm{C} 4$ | Formatter sytem adapter failure. | 132 133 B1 134 |
| 0055 | HC65 | Formater system adapter failure. | 133 132 134 A3 |
| 10153 | I'C66 | Formater system adapter failure. | 132 134 133 A3 |
| 00.52 | $\mathrm{I}^{\prime} \mathrm{C} 67$ | Formater sytem adapter failure. | 132133134 A 3 |
| 00.52 | I'C69 | Formatter systens adapter failure. | A3 |
| 00.52 | ICOA | Formatter system adapter failure. | 14 |
| 00.52 | FCOC | Formatter system adapter faiture. | 134133 137 138 |
| 10052 | FC 70 | Formateresystem adapter failure. | A8 A9 135 |
| 0055 | 1 C 71 | Formatter ustem adapter failure. | $19 \mathrm{B5}$ |
| 00552 | 1 C 74 | Formatuer system adapter failure. | 130 A9 135 |
| 0052 | 1 C 78 | Formatter system adapter failure. | A9 134 135 |
| 0055 | FC79 | Formatter/system adapter failure. | K3 D3 132 B5 |
| 0055 | FC 713 | Formatter/system adapter failure. | C1 B2 135 K3 |
| 0052 | FC 7 C | Formater/system adapter failure. | K3 132 B5 |
| 00552 | FC 7 D | Formatter/system adapter failure. | 134 132 B5 K3 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Detinition | For result code detimitions, see ('17 thon D5 ('ard 2 Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | 1. C 7 F | Fommater sytem adapter finlure. | 132133 (1 D) 3 |
| 00.52 | $1 \cdot \mathrm{C80}$ | Formatter ustem adapter birlure. | D) 3 1) 4135 |
| 00.52 | 1 C 81 | Formatters shtem adapter failure. | 1)3 D2 1) 4135 |
| 0005 | HCO 4 | Formatter sybtem adapter failure. | B3 A3 A 4138 |
| 0052 | HCAD | Formatier syutem adapter failure. | 137136 C11 134 |
| 00.52 | $\mathrm{HCL}^{5}$ | SR prancl fature in SR mode only. | 18 |
| 0052 | PCl6 | SR prancl malure in SR mode only. | B5 138 18 184 |
| 00.52 | FCl7 | SR patme fature in SR mode only. | 18 lf |
| 00.52 | l-Cl8 | SR pathel failure. | 136132 |
| 00.5 | fCF9 | SR pancl failure. | 137 B1 K3 1)2 |
| 0052 | FCFC | SR panel failure. | $137 \mathrm{i31} \mathrm{~K} 3$ |
| 0052 | FCFI | SR panel failure. | 1) 2 B +k 3 |
| 0052 | FCFE | SR nel failure. | K3131 |
| 0052 | FD02 | Catw. opener check. | $11014 \mathrm{F0} \mathrm{~F} 1$ |
| 0052 | FD04 | Cartridge failed to open. | 110) F0 F1 J |
| 0052 | F1)06 | Cartridge faited to close. | $14 \mathrm{FOF1} \mathrm{~J}_{4}$ |
| 0052 | FI)08 | Transfer valse didn't close. | F2 J1 F1 193 |
| 0052 | FD0A | Transfer valve check. | 12 J 1 Fi |
| 0052 | FD0C | Left tape-loop scasor switch check. | F5 F1 |
| 0052 | FDOE | Transfer valve dicin't open. | J1 F1 19319 |
| 0052 | FDIO | Right tape - loop sensor switch. | F6 F1 |
| 0052 | FI) 14 | Write enable ring sensor check. | 17 Fl |


| $\begin{array}{\|l\|l\|} \hline \text { SRC } \\ 1234 \\ \hline \end{array}$ | 5678 | Delinition | For resull code definitions, see ( 17 limo D) (aril 2 <br> Resull Code |
| :---: | :---: | :---: | :---: |
| (0)5, | Fiol6 | Write enable ring semsor check. | 17 F 1 |
| 0052 | FD)18 | Erane current check. | 1819 |
| 00052 | FDIC | Write current check. | 18811199 |
| 0052 | FI)20 | Capstan check. | C,3 F1 C0 |
| 0052 | $1 \mathrm{D}) 22$ | File reel automatic hut swith shorted. | Cil 11 J4 |
| 0052 | FD 24 | Capstan creeps. | C2 C3 C0 |
| 0052 | FI)28 | File reed automatic hed active but $\$ 11.3$ dise not transfer. | C,1 C.4 Fil J4 |
| 0052 | FD2A | No capstan tachometer puken detected on tape unit lagic board. | 111 C3 C0 |
| 0052 | FD2C | The reels waded switch is hore circuited. | 114 11 |
| 005.2 | F1)2E | Sollenoid check. | 11165 |
| 00.52 | FD30 | Normal end FSC, routines 1 through 3. | J5 |
| 0052 | FIO32 | Pressure switeh is active when blower is off. | C6 FI |
| 0052 | FI)34 | Tape unit control, capstan - control set to differemt speed. | G3 F1 |
| 0052 | FD)36 | Pressure solenoid check - blower is on. | G6F1 G5 |
| 0052 | FI)38 | Pressure switch eheek - blower is off. | G6 F1 F4 J4 |
| 0052 | FID3A | Threading was not succersful without cartridge or diagnostic procedure error-BOT/EOT sensor not covered after I second. | G7 G3 F1 |
| 0052 | FD3C | Tape unit microprogram check. | 11 |
| 0052 | FD40 | Tape lifter switch is not active. | G8 F1 J0 K0 |
| 0052 | FD42 | Tape lifter switch active with blower off. | G8 F1 J0 |

## Cl2 Card 2

| $\begin{aligned} & \text { SRC } \\ & 123.4 \end{aligned}$ | 5678 | Detinition | For resulf code definitions, see C17 thru D5 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | 1D)4 | Threading is mot suceesflal with cartridge tape did not pass BOT/EOT block. | G7 G9 G3 Fi |
| 0052 | 11)46 | Tape lifier solenoid switch active with blower on-solenoid failure. | G8 G5 F1 |
| 0052 | (1) 48 | File red rotation rate sensor check with caltridge. | G9 111 112 G3 |
| 0052 | ID 4 A | File reel rotation rate sensor check without cartridge. | G3 H2 H1 Fl |
| 0052 | FD 4 C | Tape not taken up by machine recl. | 114 G3 F1 J4 |
| 00.52 | FDS0 | BOI mather too close to physical BOI. | G7 |
| 00.52 | [1)54 | BOI marker too far from physical 1301. | G7 |
| 0052 | FD58 | Machine reel rotation rate sensor check. | 115 |
| 0052 | HES | Tape is not drawn into the left vacuum column. | F5 H6 F1 |
| 0052 | FD60 | A second BOT marker has been detected. | G7 |
| 0052 | H1)62 | Low pressure swith active with blower off. | H7 F1 |
| 0052 | FO64 | BOT/EOT crror. | G7 F1 |
| 0052 | FD68 | Vacuum check. | H7 F1 H8 J4 |
| 0052 | HD6C | Tape is not drawn into the right vacuum column. | F6 H2 F1 |
| 0052 | FD70 | Under voltage. | H9 F1 F8 |
| 0052 | FD74 | Write current relay check. | H9 Fl F8 |
| 0052 | FD7A | Cartridge present switch shorted. | G9 F1 |


| $\begin{aligned} & 512(1 \\ & 123.6 \end{aligned}$ | 5678 | Inclinition | For result code delimitions, see ('17 Hrul)5 ('ard 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | FI) 7 C | High pressure chact. | 10 II J 4 |
| 0052 | 1'1)80 | Reset pushbuthen presed! whate anline. | . 18 |
| 0052 | 11)86 |  While unlane. | . 18 |
| 00.52 | F1)88 | Mathane ted rotation tor lims alter - perd decteras. | 11511 |
| 00.52 | F1)8( | Write enable ring sernor chach. | 1.71 .1 |
| 0052 | F1)90 | 13()I chech. | 6,7 11 |
| (0)52 | l1)94 | F()\| chech. | (17 1:1 |
| 0052 | FI)98 | Tape anit loṣic boad fatuse of thermal. | $\mathrm{F} \cdot \mathrm{I}$ |
| ()052 | HDOC | B() 1 EOI chack. | C.7 F1 |
| 00.52 | HD) $\mathrm{HO}^{\text {a }}$ | lape unit deor or window intertock is upen. | 12131511 |
| 0052 | 1) ${ }^{\text {a }} 2$ | Windaw done interfock circuit is actise at the wrong time. | I3 I2 I'1 |
| 0052 | 1]) 44 | Tape unit logic board memory erons. | II |
| ()052 | F1) 18 | Fife red hath window plosure chack with blower on. | IJ C5. 15 |
| 0052 | FI) 1 C | Emergency brate failure, lile ree and capstan moved with brake actise. | 0.3 Fl |
| 0052 | 1 1 ) 130 | Machine reel fails to mote with drive current active. | 1151160311 |
| 0052 | F1)132 | File reel capstan dees not move after drise current is actise. | 11963 |
| 0052 | F1)134 | Emergency brake lailure, file reel moved with: brake active. | 112 G 3 |
| 0052 | F1)138 | Emergency brake falure, machine red moved with brake active. | H6C3 F1 |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see (17 thru D5 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | FDBC | Emergency brake failure, capstan moved with brake active. | C2 G3 |
| 0052 | FIDC0 | Capstan servo check/control error. | 03 |
| 0052 | FidC4 | Capstan power amplifier eneck. | G2 G3 119 G0 |
| 0052 | FDC8 | Capstan motor/tachometer check. | G3 C2 G0) |
| $005:$ | FDCC | Internal communication error. | G3 F1 |
| 0052 | FDD0 | File reel moving without drive ctrrent active. | H2 G3 11 |
| 0052 | FDD4 | Machine reel moving without drive current active. | H6 G3 İ1 |
| 0052 | FDD8 | There is a tape loop warning on the right side while the tape unit is at normal speed. | F6 H12 F1 G3 |
| 0052 | FDDC | There is a tape loop varning on the right side during a higil speed rewind | F6 H12 F1 C3 |
| 0052 | FDE0 | Capstan current control failure. | G2 G3 |
| 0052 | FDE4 | Erase/write current check. | F8 F7 H9 F1 |
| 0052 | FDE8 | Reels loaded switch check on mid-tape load. | 114 |
| 0052 | FDEC | Tape unit logic board to capstan;reel control board communication check. | G3F1 J4 |
| 0052 | FDF0 | There is a tape loop warning on the left side while the tape unit is at inomal speed. | F5 116 F1 G3 |
| 0052 | FDF4 | Cooling fan check. | 17 G2 F1 G3 |
| 0052 | FDF8 | There is a tape loop warming on the left side during a high speed rewind. | F5 H6 F1 G3 |
| 0052 | FE00 | Parameter error. | I8 E4 |


| SRC <br> 1234 | 5678 |  | For resuit code definitions, <br> see C17 than D5 Card 2 |
| :--- | :--- | :--- | :--- |
| 0052 | FE20 | Buffer crror on. | Result Code |


| $\begin{aligned} & \mathrm{SRC} \\ & 12.34 \end{aligned}$ | 5678 | Delimition | For resull code definitions, see ( 17 Ithru D) 5 Card 2 <br> Resull Code |
| :---: | :---: | :---: | :---: |
| 00.52 | HE 52 | ROM parity. | 133 |
| 0052 | 1-553 | Missing track stop). | $136 \quad 139156 / 1: 7 \quad 137$ |
| 0052 | 1-154 | Spare word detected. | 133 |
| 0052 | F1555 | File protected on write. | K1 |
| 0052 | FE56 | Tape unit status not erase. | 132 K 3 |
| 0052 | 1-E57 | Command not valid. | 134138 133 |
| 00.52 | FE58 | Tape unit not in read status. | 132 131 K3 |
| 0052 | FE59 | Defective 1D burs. | F8 D) 11 A0 |
| 0052 | FL5A | Write inhibit filure. | 132 BI K 3 |
| $005 ?$ | FL5] | Tape unit ofllane. | 132 l 1 K 3 |
| 0052 | FE5C | Tape unit not in write status. | 132 B 1 K 3 |
| 0052 | FE5D | ARA or $1 D$ burst position error. | K2 |
| 0052 | FE5E | Tape unit not in backward status. | 132 131 K3 |
| 0052 | FE5F | Data detected during erase. | J9 |
| 0052 | FL60 | Tape unit not ready. | 131 K 3 |
| 0052 | FE61 | ID track written in erro:. | A9 130 |
| 0052 | FE6? | Backward CMD at BOT. | 132137 |
| 0052 | FE63 | GCR write ID failure. | J9 |
| 0052 | FE64 | IBG more than 25 feet. | 132 133134 |
| 0052 | FE65 | Ready on during rewind. | 32 K 3 |
| 0052 | FE66 | No tach pulses. | 13I K3 133 |
| 0052 | FE67 | No I BG after write. | A0 A1 |


| $\begin{aligned} & .8120 \\ & 12.34 \end{aligned}$ | 5678 | Delinition | ror result code definitions, ree C17 thru ID5 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | 116.68 | No IBG allter 11) burst. | A0 A1 |
| 0052 | 15.69 | Norecord found after backspace. | A0 Al |
| (0)52 | HE6A | GCR BOT marker bad. | J9 |
| 00.52 | IV1.613 | No data on read back check. | 134131 130 A9 |
| 0052 | IV6C | Write interrupt, read back check. | 136 137 131 132 |
| (0052 | lilal) | Tape unit release failure. | B1 134 |
| 00.52 | H166L | Tape unit busy to controller. | $\begin{array}{llllll}\text { K3 1)2 } & 131137\end{array}$ |
| ()0152 | F1960 | Tipe unit not available. | K3 D2 134 |
| 00.52 | 1「1i70 | Status crror. | 132 K 3 |
| ()0こ2 | 1671 | Tape unit sense command response error. | B1 132 K3 |
| 0052 | F1:72 | Error in sense sequence. | 134137 |
| 0)0.52 | $11: 73$ | Tape unit busy load/unloading | J6 |
| (0)52 | HENL | Error manager called - no bit set. | A8 B7 B4 138 |
| ()0.52 | F ETFT | Wrong test number. | 18 E 4 |
| 0052 | Flj C | HDC feature card (0\|AB2-R4) in the System/38. | P3 |
| 00.52 | F1]0 | Tape alapter card (0) $\mathrm{AB} 2-\mathrm{Q} 2$ ) in the System/38. | P3 |
| (0).52 | Fleo | Tape adapter card (01AB2-Q2) or HDC card (01AB2-R4), or the top card connectors in $\mathrm{S} / 38$. | P1 P3 J7 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see C17 thru D5 Card 2 Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | 3C00 | Block size not cornpatible. | P440 P130 P330 |
| 0052 | $3 \mathrm{D00}$ | linterlace error. | P140 P330 Z320 |
| 0052 | 3 E 00 | IOM error. | S099 |
| 0052 | 4000 | System adapter response register error. | P190 Z210 |
| 0052 | 4100 | System adapter overrun. | P145 Z230 R015 P310 |
| 0052 | 5000 | Device problems. | S5\%9 |
| 0052 | 5100 | System detected bus out check. | R930 R130 R620 P310 |
| 0052 | 5120 | Tape control unit error before this command. | R199 |
| 0052 | 5130 | Temporary wad failure or tape unit failure. | R199 |
| 0052 | 5200 | Intervention required. | R199 |
| 0052 | 5400 | Subsystem detected overrun. | R130 R930 R220 R420 |
| 0052 |  | Device problems. | R175 P115 P310 |
| 0052 | 5F00 | Subsystem detected word count zero. | R130 R930 R220 R420 |
| 0052 | 6000 | I/O error unit check. | R199 |
| 0052 | 6100 | I/O error unit check. | R199 |
| $0052$ | $\begin{aligned} & 7000 \\ & \text { thru } \\ & 7700 \end{aligned}$ | IOM detected error. | P130 P330 Z330 S010 |
| 0052 | 7800 | IOM error. | S099 |


| $\begin{array}{\|l\|} \hline \text { SRC } \\ 1234 \end{array}$ | 5678 | Definition | For result code definitions, see C17 thru D5 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | EE00 | Volume SDR counters were reset. | Z799 |
| 0052 | 9700 | System adapter error. | Z260 P120 P320 |
| 0052 | 9800 | Program problem-VMC. | S199 |
| 0052 | 9998 | System adapter busy. | Z199 |
| 0052 | F030 | Normal operation code. | J2 |
| 0052 | F032 | System adapter failure. | B8 |
| 0052 | F040 | System adapter failure. | B8 B9 |
| 0052 | F042 | System adapter failure. | C0 B8 |
| 0052 | F04C | Siystem adapter failure. | B7 |
| 0052 | F100 | Wrong or defective diskette. | E8 E4 |
| 0052 | F101 | Not ready, inoperable during command execution. | E8 E9 E4 B8 |
| 0052 | F102 | Not ready, inoperable during command execution. | E8 E9 E4 B8 |
| 0052 | F104 | Address mark error. | E4 E9 E8 |
| 0052 | F106 | CRC in ID or data ficld. | E4 E9 E8 |
| 0052 | F107 | CRC in ID or data field. | E4 E9 E8 |
| 0052 | F108 | Data overrun. | B8 E4 |
| 0052 | F109 | Seek error. | E4 E9 |
| 0052 | F10A | Record not found. | E4 E9 |
| 0052 | $\begin{aligned} & \text { F10B } \\ & \text { thru } \\ & \text { F10F } \end{aligned}$ | Wrong sector number, no directory, format wrong, file name or boundary error. | E8 |
| 0052 | F130 | CC interrupt. | B8 B3 B4 C0 |


| $\begin{array}{\|l\|l\|} \hline \text { SRC } \\ 12.34 \end{array}$ | 5678 | Definition | For result code delinitions, see C17 thru D5 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | F131 | Systemiadapter error. | 138 E6/E7 E3 139 |
| 0052 | F132 | Short interrupt. | B8 139 |
| 0052 | F133 | Tag sequence error. | E3 E6/E7 137 139 |
| 0052 | F134 | Memory external erroi. | c0 E3 38 |
| 0052 | F135 | Memory internal error. | B8 C0 E5 139 |
| 0052 | F136 | Channel adapter error. | E6/E7 E3 |
| 0052 | $\begin{aligned} & \text { F140 } \\ & \text { thru } \\ & \text { F144 } \end{aligned}$ | System adapter error. | B9 E3 B8 B7 |
| 0052 | F200 | Device not ready. | 137 D2 398136 |
| 0052 | F207 | System adapter error. | B4 |
| 0052 | F217 | System adapter error. | B2 K3 |
| 0052 | F230 | System adapter error. | B7 B1 C1 D2 |
| 0052 | F232 | System adapter error. | D2 134 D4 137 |
| 0052 | F236 | System adapter error. | B9 B7 B2 K3 |
| 0052 | F240 | System adapter crror. | B4 D2 B7 B1 |
| 0052 | F241 | System adapter error. | C1 B6 B4 137 |
| 0052 | F242 | System adapter error. | B7 B8 B9 B3 |
| 0052 | F243 | System adapter error. | B7 B4 B3 |
| 0052 | F244 | System adapter error. | B7 B6 E6/E7 C1 |
| 0052 | F245 | System adapter error. | B7 B6 |
| 0052 | F246 | System adapter error. | B7 E6/E7 |
| 0052 | F247 | System adapter error. | C1 B6 B7 A8 |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see C17 thru D5 Card. 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 00.52 | 1248 | System adapter error. | B6 B7 C1 |
| $0005 ?$ | 124 A | System adapter error. | B7 B4 E6/E7 B3 |
| 00.52 | 1-24B | System adapter error. | B6 B4 B3 B7 |
| (0)52 | F24C | System adapter crror. | B5 E3 B6 B7 |
| 0052 | 1241) | System adapter error. | B7 B6 |
| 00.52 | F24E | System iddapter error. | E3 B5 B7 136 |
| 0052 | 124F | System adapter error. | B5 B6 A7 C1 |
| 00.52 | F250 | System adapter error. | B3 B1 B7 B4 |
| 0052 | 1251 | System adapter error. | B4 B3 B8 B7 |
| 00532 | 1252 | System adapter error. | B4 B3 B8 B7 |
| 0052 | 1 F 254 | System adapter error. | B7 B9 E6/E7 138 |
| 0052 | F255 | System adapter error. | B7 B1 B3 |
| 0052 | F256 | System adapter error. | B7 B8 B3 134 |
| 00.52 | 1-257 | System adapter error. | B2 B7 |
| 0052 | F258 | System adapter error. | $16 \mathrm{~B} 7 \mathrm{B6}$ B2 |
| 0053 | 1260 | System adapter error. | B1 B7 B2 K3 |
| 0052 | F261 | System adapter error. | E6/E7 B9 B8 |
| 0052 | 1262 | System adapter error. | B2 B7 D2 D3 |
| 0052 | F264 | System adapter error. | B8 |
| 0052 | F266 | System adapter error. | E6/E7 B9 E3 |
| 0052 | F267 | System adapter error. | B6 B7 B9 |
| 0052 | F268 | System adapter error. | B2 B1 B3 B0 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Deflinition | For result code definitions, see C17 thru D5 Card 2 Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | F271 | System adapter crror. | E6/E7 139 |
| 0052 | F274 | System adapter error. | 133 134 138 132 |
| 0052 | F275 | System adapter error. | A9 A8 130 A7 |
| 0052 | F2A0 | System adapter error. | B8 B9 E3 |
| 00.52 | F2F0 | System adapter error. | C0 138 139 E3 |
| 005.2 | F 2 F 1 | System adapter error. | B8 B7 |
| 0052 | $\mathrm{F}_{2} \mathrm{~F} 4$ | System andapter error. | 138 137 139 |
| 0052 | 12F5 | System adapter error. | 138 C0 |
| (1)0.52 | 12F6 | System adapter error. | 138 137 E6/E7 |
| 0052 | F 2 F 7 | System adapter error. | 138 E.4 |
| 0052 | 12 F 8 | System adapter error. | 137 E 3 |
| 0052 | F 2 FF | System adapter error. | B7 139 E6/E7 C2 |
| 0052 | 1:300 | GCR formatter had a one track correction failure. | A8 A7 A6 |
| 0052 | F308 | GCR formatter had a one track correction failure. | A8 A7 A5 A6 |
| (3052 | F301) | GCR formatter had a one track correction failure. | B4 A3 137 A5 |
| 0052 | F310 | GCR formatter had a one track correction failure. | A6 A7 B1 |
| 0052 | F320 | GCR formatter had a one track correction failure. | A6 A7 A3 |
| 0052 | 1 324 | GCR formatter had a one track correction failure. | A6 A7 A4 |
| 0052 | I 328 | GCR formatter had a one track correction failure. | A6 A7 A5 |


| $\begin{aligned} & \mathrm{SRC} \\ & 123 \mathrm{t} \end{aligned}$ | 5678 | Definition | Fior result code definitions, see C17 thru D5 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | 1360 | GCR formatter had a one track correction failure. | A6 A7 |
| 0052 | F378 | GCR formatter had a one track correction failure. | A8 A7 $\mathrm{A}_{6} \mathbf{4 5}$ |
| 0052 | F400 | PE track error detection failure. | A3 A9 A7 A8 |
| 0052 | F404 | PE track crror detection failure. | A4 B0 A7 A8 |
| 0052 | $\mathrm{F} 40^{7}$ | PE track crror detection failure. | A4 130 A7 A8 |
| 0052 | F408 | PE track crror detection failure. | A9 A7 A5 A8 |
| 0052 | F409 | PE track crror detection failure. | 132 134 130 A7 |
| 0052 | F40A | PE track error detection failure. | A7 A6 A5 A4 |
| 00.52 | F40B | PE track crror detection failure. | A8 A7 132 131 |
| 0052 | F40C | PE track error detection failure. | B1 132 A7 A8 |
| 0052 | F401) | PE track error detection failure. | A8 A9 B0 A7 |
| 0052 | F40E | PE track crror detection failure. | A8 138 A9 B0 |
| 0052 | F41A | PE track error detection failure. | A6 A8 A5 A7 |
| 0052 | F413 | PE track error detection failure. | B4 A3 B7 A5 |
| 0052 | F41C | PE track crror detection failurc. | B7 B6 C1 134 |
| 0052 | F420 | PE track crror detection failure. | A3 A1 A8 |
| 0052 | F422 | PE track error detection failure. | A3 A0 A8 |
| 0052 | F424 | PE track error detection failurc. | A4 A1 A8 |
| 0052 | F425 | PE track error detection failure. | A4 A0 A8 |
| 0052 | F428 | PE track crror detection failurc. | A5 A0 A8 |
| 0052 | F42A | PE track crror detection failure. | A5 A3 A4 A7 |

\(\left.$$
\begin{array}{|ll|l|l|}\hline & & & \begin{array}{l}\text { For result code definitions, } \\
\text { sec C17 thru D5 Card 2 } \\
\text { SRC } \\
1234\end{array}
$$ <br>

\hline 0052 \& F42B \& Definition \& PE track error detection failure.\end{array}\right]\)| Aesult Code |
| :--- |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Delinition | Fior result code definitions, see C17 thru D5 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | 1503 | Formatter failure. | A6 A4 A7 A5 |
| 00.52 | 1504 | Formatter failure. | A 16 A 5 A |
| 0052 | 「505 | Formatter failure. | A8 A7 A6 A5 |
| 00052 | F508 | Formatter fature. | A7 A8 132 A5 |
| 0052 | 1509 | Formatter failure. | А8 16 А7 15 |
| 0052 | 150 A | Formatter failure. | 134 A3 137 A5 |
| 0052 | 1510 | Formatter failure. | 130 A9 A7 A5 |
| 0052 | F 511 | Formatter failure. | A3 A9 A7 A5 |
| 0052 | F 512 | Formatier failure. | A4130 A7 A5 |
| 0652 | F513 | Formatter failure. | А9 A8 130 $\mathbf{1 7}^{7}$ |
| 00.52 | F517 | Formatter lailure. | A5 B1 A ${ }^{\text {A }} 8$ |
| 0052 | 5520 | Formatter failure. | A8 38 |
| 0052 | H521 | Formatter failure. | А 5 A2 A0 132 |
| 0052 | F522 | Formatter failure. | A5 A2 A4 A6 |
| 0052 | F523 | Formatter failure. | A5 A4 A7 A3 |
| 0052 | F524 | Formatter failure. | A1 A2 B2 B1 |
| 0052 | H 525 | Formatter failure. | A0 A2 132 131 |
| 0052 | F526 | Formatter failure. | A3 A5 B1 A2 |
| 0052 | F527 | Formatter failure. | A4 A5 131 A2 |
| 0052 | F529 | Formatter failure. | A2 A7 Bi |
| 0052 | T52A | Formatter failure. | A2 B2 B1 A5 |
| 0052 | F52B | Formatter failure. | A5 B1 |


| $\begin{aligned} & \mathrm{SRC} \\ & 12.34 \end{aligned}$ | 5678 | Definition | For resuit code definitions, see C17 thru D5 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | F52C | Formatter failure. | R1 A5 |
| 0052 | F521) | Formatter failure. | A3 A4 A2 Al |
| 0052 | F 52 E | Formatter failure. | A3 |
| 0052 | F531 | Formater failure. | A1 A2 B2 A5 |
| 0052 | F 532 | Formatter failure. | A0 A2 132 A5 |
| 0052 | $1 / 533$ | Formatter failure. | A) 11 A2 A5 |
| 00.52 | F534 | Formatter failure. | A3 A2 B2 A5 |
| 00.52 | F535 | Formatter failure. | A4 A2 A1 A6 |
| 0052 | F536 | Formatter failure. | A4 A2 A0 A6 |
| 00552 | 1537 | Formatter failure. | A4 B2 A5 A0 |
| 0052 | F538 | Formatter failure. | A2 A4 32 A5 |
| 0052 | F539 | Formatter failure. | A3 A1 A2 A6 |
| 0052 | F53A | Formatter failure. | A3 A2 B1 A0 |
| $005 ?$ | F5313 | Formatter failure. | B1 A3 A5 A0 |
| 0052 | F53C | Formatter failure. | A2 A3 A4 A5 |
| 0052 | F53D | Formatter failure. | A1 A2 A3 A4 |
| 0052 | F53E | Formatter failure. | A0 A2 A3 A4 |
| 0052 | F53F | Formatter failure. | A5 B1 A6 A3 |
| 0052 | F541 | Formatter failure. | A0 A1 A2 A3 |
| 0052 | F542 | Formatter failure. | A5 A6 A7 A2 |
| 0052 | F543 | Formatter failure. | A0 A1 A2 B2 |
| 0052 | F544 | Formatter failure. | A1 B2 B1 A6 |


| $\begin{array}{\|l} \mathrm{SRC} \\ 12.34 \end{array}$ | 5678 | Delinition | For result code definitions, see C17 thru D5 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | 1 F 45 | Formatter failure. | A0 B2 B1 A6 |
| 0052 | 1546 | Formatter failure. | A3 132 B1 A6 |
| 0052 | F547 | Formatter failure. | A4 132 B1 A6 |
| 0052 | F549 | Formatter failure. | A5 A6 A2 A 7 |
| 0052 | F555 | Formatter failure. | A4 A6 A2 A8 |
| 0052 | F556 | Formatter failure. | A6 A4 A2 A8 |
| 0052 | F557 | Formatter failure. | A4 A0 A1 A3 |
| 0052 | F559 | Formatter failure. | A3 A6 A2 A8 |
| 005.2 | H55A | Formatter failure. | A6 A3 A2 A8 |
| 0052 | F5513 | Formatter failure. | A3 A0 A1 A2 |
| 0052 | F55F | Formatter failure. | A0 A1 A2 A3 |
| 0052 | F560 | Formatter failure. | A1 A2 A 3 A6 |
| 00.52 | F562 | Formatter failure. | A 0 A 23 A 6 |
| 0052 | F564 | Formatter failure. | A1 A2. A4 A6 |
| 00.52 | F565 | Formatter failure. | A0 A2 A4 A6 |
| 0052 | F569 | Formatter failure. | A6 A7 A2 A5 |
| 0052 | F56A | Formatter failure. | A2 A0 A1 A5 |
| 0052 | F56B | Formatter failure. | A0 A1 A2 A6 |
| 0052 | F571 | Formatter failure. | A1 A2 A7 |
| 0052 | F572 | Formatter failure. | A0 A2 A7 |
| 0052 | F573 | Formatter failure. | A0 A1 A2 A7 |
| 0052 | F574 | Formatter failure. | A4 A5 A2 A7 |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Delinition | For resill code definitions, see (17 Hiru D) 5 ('ard 2 <br> Result (code |
| :---: | :---: | :---: | :---: |
| 0052 | F575 | Formatter failure. | 11 $12 \times 415$ |
| 0052 | F576 | Formatter lialure. | Aí $12 \times 4.45$ |
| 005? | F.577 | Fommatter failure. | 10 11 12 14 |
| 00.52 | F578 | Formatter fiture. | 13 15 12 17 |
| 0052 | F579 | Formatter failure. | A1 12 13 A5 |
| 0052 | 15571 | Formatter fature. | 10 12 13 15 |
| 0052 | 1573 | Formatter failure. | 10 11 12 13 |
| 0052 | 157 C | Formatter failure. | A3 14, 12 A5 |
| 0052 | 1557) | Furmalter failure. | 11 13 14.12 |
| 0052 | 1537i | Formatter failure. | 10.13.14 12 |
| 0052 | F57\% | Formatter failure. | 10111213 |
| 0052 | F 581 | Formatter failure. | A0 11 A2 16 |
| 0052 | F 582 | Formatter failure. | A3 131 10 A1 |
| 0052 | F 583 | Formatter failure. | A0 11 \2 13 |
| 00.52 | 1584 | Formater failure. | A4 12.10 A1 |
| 0052 | F585 | Formatter failure. | 10 A1 A2 At |
| 0052 | 1 F 586 | Formater failure. | A5 A7 A3 A4 |
| 0052 | F587 | Formatter failure. | A0 A1 A2 A3 |
| 0052 | F588 | Formatter failure. | A5 A2 A0 |
| 0052 | F 589 | Formatter failure. | A0 A1 A2 A5 |
| 0052 | F58A | Formatter failure. | A3 A5 A2 A0 |
| 0052 | F58B | Formatter failure. | A0 A1 A2 A3 |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code delinitions, see ('17 thru 1)5 (ard 2 <br> Resull Code |
| :---: | :---: | :---: | :---: |
| 100.52 | $1: 58 \mathrm{C}$ | Formather farlure. | A4 $12 \mathrm{A5} 10$ |
| 0015 | 15881) | Formatter fialure. | A0) 11 \2 14 |
| 0105 | 1:58F | Formatter finlure. | A0 A1 12 A3 |
| 0052 | F590 | Fonmatter failure. | $13+1313715$ |
| 0052 | 15.11 | Finmatter failure. | A5 13 A 4131 |
| 01052 | $15 / 2$ | Formatter failure. | 17 131 18 16 |
| 01052 | 1313 | Fommatter failure. | 15.1713116 |
| (1)15 | 1586 | Formatter failure. | A3 A5 131132 |
| 00152 | $15: 17$ | 1-mmatter failure. | $1+13145132$ |
| 0052 | 15134 | Fommatter failure. | 12.13132 15 |
| 10052 | 15315 | Formatter failure. | $1+181611$ |
| 0005 | F5B6 | Formatter fature. | $\therefore 8$ A6 31 A4 |
| 10052 | 15137 | Formatter failure. | A+ $10 \times 1 / 2$ |
| 00052 | 15839 | Formatter failure. | A8 17 16 11 |
| 00.52 | 1533.1 | Formatter failure. | A2 A0 16 18 |
| 002 S | 1 FBF | Formatter failure. | A5 131 |
| 005.2 | $1: 700$ | Formatter failure. | A3 |
| 0052 | 17704 | Formatter falure. | A4 |
| 0052 | 17708 | Formatter failure. | A5 |
| 0052 | 1701) | Formatter failure. | 134 A3137 A5 |
| 00052 | 17710 | Formatter failure. | A3 A2 15 A1 |
| 0052 | 17711 | Formatter failure. | A3 A2 A1 131 |


| $\begin{array}{ll} 51 R C \\ 123.4 & 5078 \end{array}$ | Dectimition | For resmalt code delinitions, <br>  <br> Result Code |
| :---: | :---: | :---: |
| $010521: 712$ | Finmatter Pivilure. | A3 A2 10 B1 |
| $0052 \mathrm{~F} / 714$ | L mmater Pailume. | 14 12 \1131 |
| (0052 1.715 | Finmatter lailure. | A2 1311410 |
| 00521776 | Finmatuer Pailure. | A4 12 A 131 |
| 005217717 | Fumatuer Pailure. | A4 A2 B1 A0 |
| 005218718 | Fommather failure. | A5 12 \0 131 |
| (0)52 17719 | Finmater Pailure. | 17 A5 3116 |
| 0105218111 | Fimmatuer bailure. | 15 17 16 18 |
| (0052 1771F | Formater lature. | A7 |
| (0) 5218720 | Formatier failure. | 15 17 13131 |
| 005.17721 | Formatuer falure. | 13 A5 A2 11 |
| 0052 F 722 | Formater falure. | 13 A5 10 12 |
| 010521723 | Formatter failure. | $12 \wedge 3 \wedge 0 \wedge 6$ |
| $00.521: 724$ | Formatuer lature. | A4 131 A5 A2 |
| 0052 F 725 | Formater failure. | A4 12 A 0 A6 |
| 005217726 | Formatior fature. | A4 12 Al A6 |
|  | Formanter falure. | A4 A5 A2 A0 |
| 005217728 | Formatter failure. | A5 A4 13 A2 |
| 0005217729 | Formanter failure. | $18 \mathrm{A6}$ A7 $\mathrm{A}_{5}$ |
| 0052 F 72.1 | Formatter failure. | A5 B1 A2 A3 |
| 005217730 | Formatter failure. | 131 A 0 A1 134 |
| 1055217731 | Formatter failure. | 132134 A 0 Al |


| $\begin{aligned} & \mathrm{SR} 8 \\ & 12.34 \end{aligned}$ | 5678 | Delinition | For resull code detinitions, see ('17 thou D5 Card 2 <br> Resinle Code |
| :---: | :---: | :---: | :---: |
| 0052 | 1.732 | Formatler failure. | A0 \1 B1 134 |
| (0)ここ | 1.733 | Fonmaller lisilus. | A0 A1 131 134 |
| 0052 | 173.4 | Formater firiluse. | 131 A ${ }^{\text {A }}$ A1 |
| …, , 2 | 17733 | Fommater finluc. | \1131 \0 134 |
| 0052 | 1736 | Fomatme falare. | A0 131 132 K3 |
| 00.52 | 1737 | Fumatuer liture. | 15 17 A9 $\mathrm{AO}_{0}$ |
| 01025 | 17338 | Fommatter fature. | 13410.5 A1 |
| (00.i2 | 1741 | Fommather fialure. | 18 19 131 A1 |
| (1052 | 1742 | Formatur fatures. | $131 \wedge 0 \wedge 1 \backslash 8$ |
| 10052 | $1: 744$ | Formather lalure. | A8 130 131 \1 |
| 0052 | 1745 | Fommatter failure. | A8 130 131 A0 |
| (0052 | 1750 | Fommater fature. | A) 18 19 131 |
| 00052 | 1758 | Formatter fature. | 13411 A0 18 |
| 00.32 | 17760 | Formatter failure. | A1 131 A0 18 |
| 00.52 | 1761 | Formatter fature. | 131 A1 A8 A9 |
| (0)52 | 17762 | Formatter failure. | 131 A0 A1 132 |
| 00.52 | $1: 763$ | Formatior lature. | $131 \wedge 0 \wedge 8 \wedge 9$ |
| 00.52 | F 764 | Formater tailure. | A1 131 10 A8 |
| 00052 | 1765 | Formatter failure. | A0 131 A8 130 |
| 0052 | 1768 | Formatter failure. | 131132 A 0 A8 |
| 0052 | 1770 | Formatter failure. | I) 3 K3 132 A1 |
| 00.52 | 1771 | Formatter failure. | A1 131 A8 A9 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see C17 limo D5 Card 2 <br> Resuit Code |
| :---: | :---: | :---: | :---: |
| 0052 | F772 | Formatter failure. | A) B1 K3 A8 |
| 0052 | F773 | Formatter failure. | K3 B1 A0 A8 |
| 0052 | F774 | Formatter failure. | A1 B1 A8 30 |
| 0052 | F775 | Formatter tisiure. | B1 D3 K3 A0 |
| 0052 | F778 | Formatter failure. | A0 A 131 B 2 |
| 0052 | F795 | Formatter failure. | E2 B1 A0 A8 |
| 0052 | F798 | Formatter failure. | A0 A1 31 |
| 0052 | F7A0 | Formatter failure. | A1 A0 B1 A8 |
| 0052 | F7A3 | Formatter failure. | A1 A0 B1 A8 |
| 0052 | F7A5 | Formatter failure. | A1 A0 B1 A8 |
| 0052 | F7B0 | Formatter failure. | D3 138 A1 B1 |
| 0052 | F7B8 | Formatter failure. | B2 A0 B1 A8 |
| 0052 | F800 | Formaiter failure. | A2 A0 A1 A3 |
| 0052 | F802 | Formatter failure. | A2 A0 A3 A5 |
| 0052 | F804 | Formatter failure. | A2 A0 A1 A4 |
| 0052 | F805 | Formatter failure. | A2 A0 A4 A5 |
| 0052 | F809 | Formatter failure. | A2 A1 A3 A9 |
| 0052 | F80A | Formatter failure. | A2 A0 A1 A3 |
| 0052 | F80C | Formatter failure. | A2 A1 A3 A4 |
| 0052 | F80D | Formatter failure. | A2 A0 A1 A3 |
| 0052 | F80E | Formatter failure. | A2 A1 A3 A4 |
| 0052 | F814 | Formatter failure. | A2 A1 A3 A9 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For resmit code delinitions, see (17 thru D)5 Cind 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | F81D | Formatter failure. | A2 A0 A3 A4 |
| 0052 | F821) | Formatter failure. | A2 A0 A1 A4 |
| 0052 | F82E | Formatter failure. | A2 A1 A4 B0 |
| 0052 | F82F | Formatter failure. | A2 A0 A1 A4 |
| 0052 | F837 | Formatter failure. | A2 A0 A4 B0) |
| 0052 | 1840 | Formatter failure. | A5 A7 |
| 0052 | F849 | Formatter failure. | A7 |
| 0052 | F900 | Formatter failure. | A8 $17 \times 5$ 16 |
| 00.52 | 1901 | Formatter failure. | A8 17 A5 A6 |
| 0052 | F909 | Formatter failure. | A8 17 A6 |
| 0052 | F917 | Formatter failure. | A8 A7 16 |
| 0052 | F9C0 | Formatter failure. | A7 134139 |
| 0052 | FA00 | Formatter failure. | A8 [37 |
| 0052 | FA40 | Formatter failure. | B4 A3 137 A5 |
| 0052 | FB00 | Formatter/system adapter failure. | E2 B2 B1 D3 |
| 0052 | FB01 | Formatter/system adapter failure. | E1 B2 B1 D3 |
| 0052 | FB02 | Formatter/system adapter failure. | E0 B2 B1 D3 |
| 0052 | FB03 | Formatter/system adapter failure. | D9 B2 B1 ${ }^{\text {- }}$ |
| 0052 | FB04 | Formatter/system adapter failure. | D8 B2 B1 D3 |
| 0052 | FB05 | Formatter/systenı adapter failure. | D7 B2 B1 D3 |
| 0052 | FB06 | Formatter/system adapter failure. | D6 B2 B1 D3 |
| 0052 | FB07 | Formatter/system adapter failure. | D5 B2 B1 D3 |


| $\begin{gathered} \mathrm{SRC} \\ 1234 \end{gathered}$ | 5678 | Definition | For result code definitions, see C17 hru D5 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 00.52 | FB08 | Formatter/system adapter failure. | E2 132 131 F1 |
| 0052 | FB09 | Formatter'system adapter failure. | E1 B2 B1 F1 |
| 0052 | FB0A | Formatter/system adapter failure. | E0 B2 B1 F1 |
| 0052 | FB0B | Formatter/system adapter failure. | D9 32 B1 F1 |
| 0052 | FB0C | Formatter/system adapter failure. | D8 B2 B1 F1 |
| 0052 | FB0D | Formatter/system adapter failure. | D7 B2 B1 F1 |
| 0052 | FB0E | Formatter/system adapter failure. | D6 B2 B1 F1 |
| 00.52 | Fib0F | Formatter/system adapter failure. | D5 132 B1 F1 |
| 0052 | FB10 | Formatter/system adapter failure. | E2 B2 Bi F1 |
| 0052 | FB11 | Formatter/system adapter failure. | E1 B2 B1 F1 |
| 0052 | FB12 | Formatter/system adapter failure. | E0 B2 B1 F1 |
| 0052 | FB13 | Forinatter/system adapter failure. | D9 B2 B1 F1 |
| 0052 | FB14 | Formatter/system adapter failure. | D8 B2 B1 F1 |
| 0052 | FB15 | Formatter/systern atapter failure. | D7 B2 B1 F1 |
| 0052 | FB16 | Formatter/system adapter failure. | D6 B2 B1 F1 |
| 0052 | FB17 | Formatter/system adapter failure. | D5 B2 B1 H1 |
| 0052 | FB18 | Formatter/system adapter failure. | D3 B1 E2 B2 |
| 0052 | FB19 | Formatter/system adapter failurc. | D3 B1 E1 B2 |
| 0052 | FB1A | Formatter/system adapter failure. | D3 B1 E0 B2 |
| 0052 | FB1B | Formatter/system adapter failure. | D3 B1 D9 B2 |
| 0052 | FB1C | Formatter/system adapter failure. | D3 B1 D8 B2 |
| 0052 | FB1D | Formatter/system adapter failure. | n B1 D7 B2 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result corte definitions, see Cl 7 thru 15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | FB1E | Formatter/system adapter failure. | 3 B1 D6 B? |
| 0052 | FBIF | Formatter/system adapter failure. | D3 B1 D5 B2 |
| 0052 | FB20 | Forniatter/system adapter failure. | B2 E2 D2 B1 |
| 0052 | F1321 | Formatter/system adapter failure. | B2 E1 D2 B1 |
| 0052 | FB22 | Formatter/systern adapter failure. | B2 E0 D2 B1 |
| 0052 | FB23 | Formatter/system adapter failure. | B2 D9 D2 B1 |
| 0052 | FB24 | Formatter/system adapter failure. | B2 D8 D2 B1 |
| 0052 | Fi325 | Formatter/system adapter failure. | B2 D7 D2 B1 |
| 0052 | F1326 | Formatter, system adapter failure. | B2 D6 D2 B1 |
| 0052 | F1327 | Formatter/system adapter failure. | B2 D5 D2 B1 |
| 0052 | FB28 | Formatter/system adapter, failure. | E2 B2 D2 B1 |
| 0052 | F1329 | Formatter/system adapter failure. | E1 B2 D2 B1 |
| 0052 | FB2A | Formatter/system adapter failure. | E0 B2 D2 B1 |
| 0052 | FB2B | Formatter/system adapter failure. | D9 B2 D2 B1 |
| 0052 | FB2C | Formatter/system adapter failure. | D8 B2 D2 B1 |
| 0052 | FB2D | Formatter/system adapter failure. | D7 B2 D2 B1 |
| 0052 | FB2E | Formatter/system adapter failure. | D6 B2 D2 B1 |
| 0052 | FB2F | Formatter/system adapter failure. | D5 B2 D2 B1 |
| 0052 | FB30 | Formatter/system adapter failure. | E2 B2 D2 B1 |
| 0052 | FB31 | Formatter/system adapter failure. | E1 B2 D2 B1 |
| 0052 | FB32 | Formatter/system adapter failure. | E0 B2 D2 B1 |
| 0052 | FB33 | Formater/system adapter failure. | D9 B2 D2 B1 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see A12 thru A 15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | F9F9 | No IBG was detected after a write, read, or space. | B1 B4 |
| 0052 | F9FA | No IBG was detected after a write, read, or space. | B3 B1 |
| 0052 | F9FC | No 13 G was detected after a write, read, or space. | C1 A2/A3 D4 |
| 0052 | F9FE | No IBG was detected after a write, read, or space. | D2 C1 F6 H1 |
| 0052 | F9FF | No IBG was detected after a write, read, $0^{\prime \prime}$ pace. | D2 H1 |
| 00.52 | FA00 | No IBG detected after read or after write tape mark. | H1 |
| 0052 | FA01 | No IBG detected after read or after write tape mark. | B4 |
| 0052 | FA02 | No IBG detected after read or after write tape mark. | B3 |
| 0052 | FA03 | No IBG detected after read or after write tape mark. | A2/A3 B4 |
| 0052 | FA04 | No IBG detected after read or after write tape mark. | A5 B1 A1 |
| 0052 | FA05 | No IBG detected after read or after write tape mark. | A5 B1 |
| 0052 | FA06 | No IBG detected after read or after write tape mark. | B3 A1 |
| 0052 | FA07 | No IBG detected after read or after write tape mark. | B1 A1 |
| 0052 | FA08 | No IBG detected after read or after write tape mark. | B3 H1 |
| $0 C 52$ | FA09 | No IBG detected after read or after write tape mark. | B1 B4 |
| 0052 | FA0A | No IBG detected after read or after write tape mark. | I33 B1 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see Al2 thru A 15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | FA0C | No IBG detected after read or after writé tape mark. | C1 A2/A3 D4 |
| 0052 | FA0E | No $1 B G$ detected after read or after write tape mark. | D2 Ci F6 H1 |
| 0052 | FA0F | No IBG detected after read or after write tape mark. | D2 H1 |
| 0052 | FA11 | Command out, data end, or IBC did not occur within 1.2 seconds of detecting beginning of block. | B4 |
| 0052 | FA12 | Command out, data end, or IBG did not occur within 1.2 seconds of detecting beginning of block. | 133 |
| 0052 | FA13 | Command out, data end, or IBG did not occur within 1.2 seconds of detecting beginning of block. | B3 |
| 0052 | FA14 | Command out, dâta cr.d, or IBG did not occur within 1.2 seconds of detecting beginning of block. | B2 B1A1 A5 |
| 0052 | FA15 | Command out, data end, or IBG did not occur within 1.2 seconds of detecting beginning of block. | 131 A5 132 Al |
| 0052 | FA16 | Command out, data end, or iBG did not occlis within 1.2 seconds of detecting beginning of block. | B2 A1 B3 |
| 0052 | FA17 | Command out, data end, or IBG did not 0 . sur within $: .2$ seconds of detecting beginning of block. | B2 A1 B1 A5 |
| 0052 | FA18 | Command out, dat: end, or IBG did not occur within 1.2 seconds of deiecting beginning of block. | A1 133 |
| 0052 | FA19 | Command out, data end, or IBG did not ocrur within 1.2 seconds of detecting beginning of block. | B1 134 |


| $\begin{array}{\|l\|l} \hline \text { SRC } \\ 1234 \end{array}$ | 5678 | Definition | For result code definitions, see A12 thru A15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | FA1A | Command oui, data end, or IBG did not occur within 1.2 seconds of detecting beginning of block. | B2 B1 B3 |
| 0052 | FA20 | IL burst was written on the wrong track. | H1 |
| 0052 | FA21 | ID burst was written on the wrong track. | B4 |
| 0052 | FA22 | ID burst was written on the wrong track. | B3 |
| 0052 | FA23 | ID burst was written on the wrong track. | A2/A3 B4 |
| 0052 | FA24 | ID burst was written on the wrong track. | A5 B1 |
| 0052 | FA25 | ID burst was written on the wrong track. | A5 B1 |
| 0052 | FA26 | ID burst was written on the wrong track. | B3 |
| 0052 | FA27 | ID burst was written on the wrong track. | B1 |
| 0052 | FA28 | ID burst was written on the wrong track. | B3 H1 D2 Cl |
| 0052 | FA29 | ID burst was written on the wrong. track. | B1 34 |
| 0052 | FA2A | ID burst was written on the wrong track. | B3 B1 |
| 0052 | FA2C | ID burst was written on the wrong track. | C1 A2/A3 D4 H1 |
| 0052 | FA2E | ID burst was written on the wrong track. | D2 C1 F6 H1 |
| 0052 | FA2F | ID burst was written on the wrong traci. | D2 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see A12 thru A15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | FA30 | Time out occurred while searching for beginning of block. | H1 D2 C1 D6 |
| 0052 | FA31 | Time out occurred " ile searching for beginning of block. | B4 |
| 0052 | FA33 | Time out occurred while searching for beginning of block. | A2/A3 |
| 0052 | FA34 | Time out occurred while searching for beginning of block. | A5 B1 A1 |
| 0052 | FA35 | Time out occurred while searching for beginning of block. | A5 31 |
| 0052 | FA36 | Time out occurred while searching for beginning of block. | D2 A1 |
| 0052 | FA37 | Time out occurred while searching for beginning of block. | B1 A1 |
| 0052 | FA38 | Time out occurred while searching for beginning of block. | A1 |
| 0052 | FA39 | Time out occurred while searching for beginning of block. | B1 34 |
| 0052 | FA3A | Time out occurred while searching for beginning of block. | B1 |
| 0052 | FA3C | Time out occurred while searching for beginning of block. | C1 A2/A3 D4 |
| 0052 | FA3E | Time out occurred while searching for beginning of block. | D2 C1 F6 H1 |
| 0052 | FA3F | Time out occurred while scarching for beginning of block. | D2 |
| 0052 | FA41 | Command out occurred without data end or IBG within 1.2 seconds of bcginning of block. | B4 |
| 0052 | FA42 | Command out occurred without data end or 1BG within 1.2 seconds of beginning of block. | B3 |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code delinitions, see A12 thru A15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | FA43 | Command out occurred without data end or IBG within 1.2 seconds of beginning of block. | B3 |
| 0052 | FA44 | Command out occurred without data end or IBG within 1.2 seconds of beginning of block. | B2 B1 A5 |
| 0052 | FA45 | Command out gecurred without data end or IBG within 1.2 seconds of beginning of block. | B2 B1 A5 |
| 0052 | FA46 | Command out occurred without data end or 1BG within 1.2 scconds of beginning of block. | B2 B4 |
| 0052 | FA47 | Command out occurred without data end or IBG within 1.2 seconds of beginning of block. | B2 |
| 0052 | FA48 | Command out occurred without uata end or IBG within 1.2 seconds of beginning of block. | B3 |
| 0052 | FA49 | Corimand out occurred without data end or 1BG within 1.2 seconds of beginning of block. | B1 B4 |
| 0052 | FA4A | Command out occurred without data end or 1BG within 1.2 seconds of beginning of block. | B2 B1 B3 |
| 0052 | FA51 | A crease found during a write command, but command out detected to correctly end the record. | A4 B4 B5 |
| 0052 | FA52 | A crease found during a write command, but command out detected to correctly end the record. | B3 |
| 0052 | FA53 | A crease found during a write command, but command out detected to correctly end the record. | B3 B5 |

## 3430 SRC Table

| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see A12 thru A15 Card 2 Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | FA54 | A crease found during a write command, but command out detected to correctly end the record. | A1 |
| 0052 | FA55 | A crease found during a write command, but command out detected to correctly end the record. | 135 |
| 0052 | FA56 | A crease found during a write command, but command out detected to correctly end the record. | 133 |
| 0052 | FA57 | A crease found during a write command, but command out detected to correctly end the record. | A1 A4 |
| 0052 | FA58 | A crease found during a write command, but command out detected to correctiy end the record. | A1 A4 133 |
| 0052 | HA59 | A crease found during a write command, but command but detected to correctly end the record. | B4 |
| 0052 | FA5A | A crease found during a write cor.mand, but command out detected to correctly end the record. | [33 |
| 0052 | FB00 | No defined operation-operator error. | J2 |
| 0052 | FB10 |  | A2/A3 B4 135 A4 |
| 0052 | F1320 | Microdiagnostic hardware failure. | A4 A1 133 |
| 002 | FB30 | Beginning of tape bit not active. | C1 E2 |
| 0052 | Fs40 | File protect bit active. | C1 A4 D3 D4 |
| 0052 | FB50 | Expected ending status has occurred. | B1 B2 B4 B5 |
| 0052 | FB60 | Unexpected unit exception. | C1 E2 |
| 0052 | FB70 | Data miscompare. Unit check was not set. | A5 B1 B2 A1 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see Al2 thru A 15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | FB80 | Byte count miscompare. | B2 B1 A5 A1 |
| 0052 | FBAO | Operator error - invalid tape unit. | A4 B5 F2 |
| 0052 | FBB0 | Invalid track in crror byte. | H1 D2 D6 B2 |
| 0052 | FBC0 | Rewind failed-timed out. | C 5 C 4 C 1 |
| 0052 | FBD0 | No nonreportable error when expected. | A1 B4 A4 |
| 0052 | FBE0 | IBG is not within specification. | C 4 C 5 Cl |
| 0052 | FBF0 | A rewind unload operation failed. | C1 A2/A3 |
| 0052 | FC00 | Microdiagnostics determined that forced crrors are not detected. | 111 A5 131 132 |
| 0052 | FC10 | No nonreportable error when expected. | J2 |
| 0052 | FC20 | No nonreportable error when expected. | J2 |
| 0052 | FC30 | Microdiagnostics deterrnined that forced ARA errors are not detected. | C1 |
| 0052 | FC40 | Clock and detection card did not function within acceptable phase change range. | A5 B2 BI |
| 0052 | FC81 | Tape unit logic failure. | B4 B5 |
| 0052 | FC82 | Tape unit logic failure. | B3 |
| 0052 | FC83 | Tape unit logic failure. | A2/A3 133 135 |
| 0052 | FC85 | Tape unit logic failure. | B5 |
| 0052 | FC86 | Tape unit logic failure. | B3 |
| 0052 | FC88 | Tape unit iogic failure. | B3 |
| 0052 | FC89 | Tape unit logic failurc. | B4 |
| 0052 | FC8A | Tape unit logic failure. | 133 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Delinnition | For result code definitions, see A12 thru A 15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | FC8C | Tape unit logic failure. | C1 A2/A3 D4 |
| 0052 | FC8E | Tape unit logic failure. | Cl |
| 0052 | FC91 | Tape unit check and ready reset are not set but the sense bits that set theni are sct. | B4 B5 |
| 0052 | FC92 | Tape unit check and ready reset are not set but the sense bits that set them are sct. | B3 |
| 0052 | FC93 | Tape unit check and ready reset are not set but the sense bits that set them are set. | A2/A3 B3 B5 |
| 0052 | FC95 | Tape unit cheek and ready reset are not set but the sense bits that set them are set. | 135 |
| 0052 | FC96 | Tape unit check and ready reset are not set but the sense bits that set them are set. | B3 |
| 0052 | FC98 | Tape unit check and ready reset are not set but the sense bits that set them are set. | 133 |
| 0052 | FC99 | Tape unit check and ready reset are not set but the sense bits that set them are set. | B4 |
| 0052 | FC9A | Tape unit check and ready reset are not set but the sense bits that set them are set. | 133 |
| 0052 | FC9C | Tape unit check and ready reset are not set but the sense bits that set them are sct. | C1 A2/A3 D4 B3 |
| 0052 | FC9E | Tape unit check and ready reset are not set but the sense bits that set them are set. | C1 |


| $\begin{array}{\|l} \text { SRC } \\ 1234 \end{array}$ | 5678 | Definition | For result code definitions, see A12 thru A15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | FCA1 | Tape unit logic failure. | B4 B5 |
| 0052 | FCA2 | Tape unit logic failure. | B3 |
| 0052 | FCA3 | Tape unit logic failure. | A2/A3 B3 B5 |
| 0052 | FCA5 | Tape unit logic failure. | B5 |
| 0052 | FCA6 | Tape unit logic failure. | B3 |
| 0052 | FCA8 | Tape unit logic failure. | B3 |
| 0052 | FCA9 | Tape unit logic failure. | B4 |
| 0052 | FCAA | Tape unit logic failure. | B3 |
| 0052 | FCAC | Tape urit logic failure. | C1 A2 ! 13 D4 B3 |
| 0052 | FCAE | Tape unit logic failure. | C1 |
| 0052 | FCB0 | Capstan velocity varicd more than $5 \%$ during write. | D1 C5 C4 C1 |
| 0052 | FCC0 | ARA circuits failed to set read heat circuits to correct gain. | H1 D6 |
| 0052 | FCCl | ARA circuits failed to set read head circuits to correct gain. | 35 B4 H1 D2 |
| 0052 | FCC2 | ARA circuits failed to set read head circuits to correct gain. | B3 H1 |
| 0052 | FCC3 | ARA circuits failed to set read head circuits to correct gain. | A2/A3 B3 B5 |
| 0052 | FCC5 | ARA circuits failed to set read head circuits to correct margin. | B5 |
| 0052 | FCC6 | ARA circuits failed to set read head circuits to correct margin. | B3 |
| 0052 | FCC8 | ARA circuits failed to set read head circuits to correct gain. | B3 H1 D2 C1 |


| $\left\lvert\, \begin{gathered} \text { SRC } \\ 1234 \end{gathered}\right.$ | 5678 | Definition | For result code definitions, see A12 thru A15 Card 2 Result Code |
| :---: | :---: | :---: | :---: |
| 00.52 | HCC9 | ARA circuits failed to set read head circuits to correct gain. | B3 |
| 0052 | FCCA | ARA circuits failed to set read head circuits to correct gain. | 133 |
| 0052 | FCCC | ARA circuits failed to set read head circuits to correct gain. | C1 A2/A3 D4 H1 |
| 0052 | FCCE | ARA circuits failed to set read head circuits to correct gain. | D2 C1 F6 H1 |
| 00.52 | FCDO | Lost vacuum in left column after tape was loaded. | G5 D1 E4 C4 |
| 0052 | FCEO | Lost vacuum in right column after tape was loaded. | G5 D1 Cl F1 |
| 0052 | FCF() | A driver control failure occurred. | D1 C5 C4 C1 |
| 0052 | FD00 | Both write voltage and erase are on during read status. | D2 C1 D6 D3 |
| 0052 | FD10 | Power feedback failure to capstan and reel motors. | D1 C5 C1 C4 |
| 0052 | FD20 | Iliegal operation. | C1 A2/A3 A5 B1 |
| 0052 | FD30 | Reset or interlock switches activated during disconnected command. | C1 F3 F2 |
| 0052 | FD41 | Tape unit check sense bit active without any tape unit error sense bit on. | B4 B5 |
| 0052 | F1)42 | Tape unit check sense bit active without any tape unit error sense bit on. | B3 |
| 0052 | FD43 | Tape unit check sense bit active without any tape unit error sense bit on. | A2/A3 B3 B5 |
| 0052 | FD45 | Tape unit check sense bit active without any tape unit error sense bit on. | B5 |
| 0052 | FD46 | Tape unit check sense bit active without any taple unit error sense bit on. | B3 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Delinution | For result code definitions, see A12 thru A15 Card 2 Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | 1-1)48 | Tape unit check sense bit active without any tape unit error sense bit on. | I:3 |
| 0052 | FI)49 | Tape unit check sense bit active without any tape unit error sense bit on. | 134 |
| 00.52 | F C 4 A | Tape unt check sense bit active without aliy tape unit error sense hit on. | I33 |
| 00.52 | I'1)4C | Tape unit check sense bit active without any tape unit error sense bit on. | C1 A2/A3 D4 33 |
| 0052 | $\mathrm{F} \mathrm{l}) 4 \mathrm{E}$ | Tape unit check sense bit active without any tape unit error sense bit on. | C1 A2/A3 35 |
| (0).5: | FD 50 | Reset or interlock switeh activated during a connnected command. | C1 F3 [2 |
| 00.52 | I'1)60 | Interrupt received without any supporting sense. | $12 / \mathrm{A} 3 \mathrm{Cl\mid 16}$ |
| 00.52 | F1)70 | laterrupt, unit check, or ready reset wibout any tape unit sense dats. | G5C1C5C4 |
| 00.52 | 1-1)71 | Interrupt, mit check, of ready reset without any tape unit sense data. | 134 |
| 00.52 | FI)73 | Interrupt, unit check, or ready reset without any tape unit sense data. | A2/A3 |
| 0052 | F1079 | Interrupt, unit check, or ready reset without any tape unit sense data. | 134 |
| 0052 | HI 7 C | Interrupt, unit check, or ready reset without any tape unit sense data. | A2/A3 D4 |
| 00.52 | F1080 | Capstan direction is opposite of what it shorikl be. | D) C 5 C 4 |
| 0052 | FD90 | No vacuum is sensed. | C1 C4E3 |
| 0052 | FDA0 | No vacuum is sensed. | D1 C5 C1 C4 |
| 0052 | FIDI30 | Left column capacitive sense circuits unable to contol tape. | D) Cl C5C4 |


| $\begin{array}{\|l\|l\|} \hline \text { SRC } \\ 1234 \end{array}$ | 5678 | Definition | For result code definitions, see A12 thri A15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | FDC0 | Right column capacitive sense circuits unable to control tape. | D) Cl C 5 C 4 |
| 0052 | FDID0 | Tape unit detected that the vacurm up switch failed. | G3 G2 G1 C4 |
| 0052 | FDEO | Interrupt without supporting sense. | C1 A2/A3 H6 |
| 0052 | FE00 | A driver control failure during a load operation. | D) C 5 C 4 C 1 |
| 0052 | FE10 | Both write and erase head voltages are on during a load op. | D2 C1 D6 D3 |
| 0052 | FE20 | Power feedback failure to capstan and reel motors. | D1 C5 C1 C4 |
| 0052 | FE31 | Unit check and ready reset sense miscompare. | B4 B5 |
| 0052 | FE32 | Unit check and ready reset sense miscompare. | B3 |
| 0052 | FE33 | Unit check and ready reset sense miscompare. | A2/A3 B3 135 |
| 0052 | FE35 | Unit check and ready reset sense miscompare. | B5 |
| 0052 | FE36 | Unit check and ready reset sense miscompare. | B3 |
| 0052 | FE38 | Unit check and ready reset sense miscompare. | B3 Cl A2/A3 B4 |
| 0052 | FE39 | Unit check and ready reset sense miscompare. | B3 |
| 0052 | FE3A | Unit check and ready reset sense miscompare. | B4 |
| 0052 | FE3C | Unit check and ready reset serise miscompare. | B3 |
| 0052 | FE3E | Unit check and ready reset sense miscompare. | Cl |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For resnlt code definitions, see A12 thru A15 Card 2 <br> Result Code |
| :---: | :---: | :---: | :---: |
| 0052 | FE40 | Tape unit load failure but no hardware failure detected. | D) 1 Ed C 5 Cl |
| 0052 | FE50 | Reset or interlock switch failure. | C1 F3 F2 |
| 0052 | FE60 | Reset or interlock switch failure. | C1 F3 F2 |
| 0052 | FE70 | Capstan speed varics more than $5 \%$ during load operation. | D) $\mathrm{C} 5 \mathrm{C4Cl}$ |
| 0052 | FE80 | Interrupt without supporting sense data during load failure. | Cl |
| 0052 | FE90 | Capstan direction opposite of what it should be during load failurr. | D) $\mathrm{C} 5 \mathrm{C4} \mathrm{Cl}$ |
| 0052 | FEA0 | No vacuum is sensed. | $65 \mathrm{H1C1C5}$ |
| 0052 | FEB0 | ivo vacuum is sensed. | C5 C1 C4 E5 |
| 0052 | FEC0 | Left column capacitive sense circuits failuce. | G5 D1 C5 C1 |
| 0052 | FED0 | Right column capacitive sense circuits failure. | D1 C5C1 C4 |
| 0052 | FEE0 | Vacıum up switch did not nick within 5 seconds during load. | G3 G2 G1 G4 |
| 0052 | FFC0 | HDC feature card ( $01 \mathrm{AB} 2-\mathrm{R} 4$ ) in the System/38. | P3 |
| 0052 | FFD0 | Tape adapter card (01AB2-Q2) in the System/38 | P1 |
| 0052 | FFE0 | Tape adapler card (01AB2-Q2) or HDC card ( $01 \mathrm{AB} 2-\mathrm{R} 4$ ), or the top card topnectors in SYS/38. | P1 P3 1/2 |

## 3430 RC Table

| Result Code | Definition | MIM Reference | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: | :---: |
| A1 | Chamel adapter card (A-A\|B2) | Carr 490 |  |
| A2. | Device adapter card (A-AlC2) | Ref 120 |  |
| A3 | $\begin{aligned} & \text { Device adapter leature card } \\ & (\mathrm{A}-\mathrm{AlD2}) \end{aligned}$ | Ref 120 |  |
| A4 | Power on resel card (A-AIE2) | Carr 490 |  |
| A5 | Clock and detection card (A-AlG2) | Carr 490 |  |
| 131 | Reed skew card (A-Al!12) | Carr 490 |  |
| 132 | Erros correction card (A-A1J2) | Carr 490 |  |
| B3 | Write card (A-A\|K2) | Carr 490 |  |
| 134 | Micro processor card (A-AiM2) | Carr 490 |  |
| 135 | Miero code storage card (A-A1N2) | Carr 490 |  |
| C1 | Tape unit logic board | Carr 490 |  |
| C2 | Bus card | Carr 480 |  |
| C3 | Tag card | Carr 480 |  |
| Cd | Motion control card | Loc 100 |  |
| C5 | Mintion control board | Carr 110 |  |
| D1 | Capstan motor | Carr 300 |  |
| D2 | R/W card assembly | Carr 370 |  |
| D) 3 | File protect assembly | Carr 90 |  |
| D) 4 | Interface cables | Ref 120, ref 150 |  |
| D5 | Interface ca.bles | Ref 110, ref 140 |  |


| Result Code | Definition | MIM Refersince | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: | :---: |
| D) 6 | R/W head assembly | Carr 371 |  |
| E1 | Interface cables | Loc 70 |  |
| I2 | BOT/EOT assembly | Carr 160 |  |
| Le3 | I. colvacuum sw | Carr 170 |  |
| E4 | Left reel motor | Carr 350 |  |
| E5 | Right col valcuum sw | Carr 170 |  |
| F 1 | Right reel motor | Carr 350 |  |
| F 2 | Op pancl sw card | Carr 4.5) |  |
| F3 | Interlock sw | Carr 410 |  |
| F4 | Right capacitive sense assembly | Carr 120 |  |
| 15 | Left capacitive sense assembly | Carr 120 |  |
| F6 | $\mathrm{R} / \mathrm{W}$ interface cables | Ref 280 |  |
| G1 | Vacuum up sw | Carr 250 |  |
| C. 2 | Vacuum pump belt | Carr 270 |  |
| G3 | Vacuum motor | Carr 280 |  |
| G4 | Vacuum pump | Carr 260 |  |
| G5 | Vacuum column door | Carr 200 |  |
| G6 | Vacuum motor relay | Carr 200 |  |
| 41 | Clean the tape path |  |  |
| H2 | Inspect top card connectors |  |  |
| H3 | Change t1/ patch modules/card A-Al P4 |  |  |
| H4 | Go to MAP 3000 |  |  |


| Result Code | Definition | MIM Reference | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: | :---: |
| H5 | Check vacuum column cover for tightness |  |  |
| 116 | CE panel |  |  |
| J1 | MAP 1350, entry A |  |  |
| J2 | No FRUS called out |  |  |
| K I | Waiting for C.E. cntry - run again | Fscel 20 |  |
| K2 | Invalid C.E. entry - refer fsed 20 code 0B-4 |  |  |
| K3 | Routing is running - no action requited |  |  |
| N0 | $\begin{aligned} & \text { Power control falure - run MAP } \\ & 6603 \end{aligned}$ |  |  |
| P1 | Tape adapter cand (01AB2-Q2) |  | 2771192 |
| P 2 | Channel interface cards - tape unit |  |  |
| P3 | HDC feature card (0\|AB2-R4) |  | 2445462 |
| P4 | Block size on tape not compatible with the HDC feature. |  |  |
| Q0 | Online/oflline switch |  |  |
| Q1 | Power on/off switch |  |  |
| Q2 | Go to the 3430 MlM start section |  |  |
| R0 | Expected data |  |  |
| R1 | Use FSC generatedfor this error |  |  |
| R2 | System/38 ribbon cable (01AB2-Q2W2) |  |  |
| R3 | Tailgaie connectors (01 DA2-bottom) |  |  |
| R4 | External lug cable |  |  |


| Result Code | Definition | MIM Reference | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: | :---: |
| R5 | FSC for device |  |  |
| R6 | System/38 : ibbon cable (01AB2-Q2X2) |  |  |
| R7 | T i.ilgate connectors (01 DA2-top) |  |  |
| R8 | External cable (bus) |  |  |
| R9 | Channel interface cards in tape control unit |  |  |
| S0 | VMC failure (IOM) |  |  |
| S 1 | VMC failure |  |  |
| S2 | SCA related failure (IOM or interface) |  |  |
| S3 | Decompress data error |  |  |
| S4 | Failed during vary on |  |  |
| S5 | Go to the 3430 MIM stari section |  |  |
| 57 | Command reject |  |  |
| S8 | Post event |  |  |
| S9 | FOB time out |  |  |
| U4 | Expected result or error caused by operator |  |  |
| U5 | Unexpected result for F:DC feature |  |  |
| Z1 | Go to MAP 0500, entry E |  |  |
| L2 | Go to channel error log analysis MAP |  |  |
| 73 | Interposer (01AB2 Q2Y and R4W, Q2Z and R4X) |  |  |
| 27 | Volume SDR counters were reset |  |  |

3422 SRC Table

## 3422 SRC Tatile ( $\mathrm{OU}=52$ )

| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see ( 17 Ilom ) 5 (ard 2 <br> Resnife Code |
| :---: | :---: | :---: | :---: |
| 00.52 | 0000 | Error accursed during power on off | N099 |
| 0052 | 1000 | Post creont. | P140 P340 /320 |
| 0052 | 1200 | Unsuccesiful post event. | RI50 U450 |
| 0052 | 1300 | Invialid disconnect. | S $1.50 / .250$ |
| 0052 | 2000 | FOB crorr. | S1\% |
| 0052 | 2300 | Error conde indicates semse required. | S090 P110 |
| 0052 | 2500 | Tape unit besy. | S54.5 P12.5 P32.5 Slo.5 |
| 0052 | 3000 | Sefect in error. | N()$_{40} \mathrm{Q} 030 \mathrm{Q} 130$ |
| 0052 | \$200 | Disconneet in crou. | Q299 |
| ()0.52 | 3300) | Interface control check. | P235S530 P120 P315 |
| 0052 | 3400 | Adapter bus in cheek. | R635 R825 R925 |
| 0052 | 3500 | Adapter detected bus out check. | P140 P340 $/ 320$ |
| 0052 | 3600 | Unexpected status. | P100 S530 P310 |
| (0) 52 | 3700 | Expand datia error. | S370 P330 |
| 0052 | 3800 | HDC card failure. | P370 / 3 30 |
| ()052 | 3900 | Interface crror. | /240 P130 P330 |
| 0052 | 3 AOO | HDC card error. | 113707330 |
| 0052 | 31300 | 1OM error: DSTAT byte 4 in hex OA. OB, OC or OD. <br> Hardware error: Dstat bytu 4 is hex 20, 21, 22; or 23. <br> Unexpected error: Detat byte +1 is lex 25 , or 26. <br> Adaptor error: Ditat byte 4 is hex 15 , 27 , or 26. | $\begin{aligned} & \text { S199 } \\ & P 399 \\ & P 399 \\ & P 199 \end{aligned}$ |


|  |  | For result code definitions, <br> see A14 thru A16 Card 4 |
| :--- | :--- | :--- |
| 9335 <br> SRC <br> 1234 |  | Result Code <br> by Prionty |
| $81 \times x \times 30$ F |  | B2 B4 B6 E1 <br> B3 B5 B7 E1 |
| $81 \times x \times 310$ |  | B2 B6 <br> B3 B7 |
| $81 \times x \times 311$ |  | B2 B4 B6 E1 <br> B3 B5 B7 E1 |
| $81 \times x \times 312$ |  | B2 B4 B6 E1 <br> B3 B5 B7 E1 |
| $81 \times x \times 313$ |  | B6 E1 B2 B4 <br> B7 E1 B3 B5 |
| $81 \times x \times 314$ |  | B6 E1 B2 B4 <br> B7 E1 B3 B5 |
| $81 \times x \times 315$ |  | B2 B4 B6 E1 <br> B3 B5 B7 E1 |
| $81 \times x \times 316$ |  | B2 B4 B6 E1 <br> B3 B5 B7 E1 |
| $81 \times x \times 317$ |  | B2 B4 B6 E1 <br> B3 B5 B7 E1 |
| $81 \times x \times 318$ |  | B2 B4 B6 E1 <br> B3 B5 B7 E1 |
| $81 \times x \times 319$ |  | B6 B2 <br> B7 B3 |
| $81 \times x \times 31 \mathbf{A}$ |  | B2 B6 <br> B3 B7 |

9335 SRC Table

| $\begin{array}{ll} 9335 & \\ \text { SRC } & \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see Al4 thru A16 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $81 \times x \times 3113$ |  | $\begin{array}{llll}\text { B2 B4 B6 E1 } \\ \text { B3 } & \text { B5 B7 } & \text { B7 }\end{array}$ |
| 81xx x 31 C |  | $\begin{aligned} & \text { B2 B4 B6 E1 } \\ & \text { B3 B5 B7 E1 } \end{aligned}$ |
| $81 \times x \times 31 \mathrm{D}$ |  | $\begin{aligned} & \mathrm{B} 6 \mathrm{E} 1 \mathrm{~B} 2 \mathrm{~B} 4 \\ & \mathrm{~B} 7 \mathrm{E} 1 \mathrm{~B} 3 \mathrm{~B} 5 \\ & \hline \end{aligned}$ |
| 81xx x 31 E |  | $\begin{aligned} & \text { B2 B6 } \\ & \text { B3 B7 } \\ & \hline \end{aligned}$ |
| $81 \times x \times 31 \mathrm{~F}$ |  | $\begin{aligned} & \text { B2 B4 B6 E1 } \\ & \text { B3 B5 B7 E1 } \end{aligned}$ |
| $81 \times x$ |  | $\begin{aligned} & \mathrm{B} 2 \mathrm{~B} 4 \mathrm{~B} 6 \mathrm{E} 1 \\ & \mathrm{~B} 3 \mathrm{~B} 5 \mathrm{~B} 7 \mathrm{E} 1 \end{aligned}$ |
| $81 \mathrm{xx} \times 321$ |  | $\begin{aligned} & \text { B2 B6 } \\ & \text { B3 B7 } \end{aligned}$ |
| $81 \mathrm{xx} \times 322$ |  | $\begin{array}{\|l\|lll\|} \hline \text { B2 B4 } & \text { B6 } & \text { E1 } \\ \text { B3 } & \text { B5 } & \text { B7 } & \text { E1 } \\ \hline \end{array}$ |
| $81 \times x \times 323$ |  |  |
| $81 \times x \quad \times 324$ |  | B 6 <br> E 11 B 2 B 4 <br> B 7 |
| $81 \times x \quad \times 325$ |  | $\begin{aligned} & \mathrm{B} 2 \mathrm{~B} 4 \mathrm{B6} \mathrm{Ei} \\ & \mathrm{~B} 3 \mathrm{~B} 5 \mathrm{~B} 7 \mathrm{E} 1 \end{aligned}$ |
| $81 \times x \times 326$ |  | E1 B4 B2 B6 E1 B5 B3 B7 |

EO2 Card 3

| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & 12345678 \\ & \hline \end{aligned}$ | Defanition | For result code defititions, see Alt thru il6 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $81 \times 8.3040$ |  | $\begin{aligned} & 131134 \\ & 131135 \end{aligned}$ |
| $81 \times .8400$ |  | $\begin{aligned} & 134 B 1 A 2 \\ & 135 B 1 A 2 \\ & \hline \end{aligned}$ |
| $81 \times 8 \times 401$ |  | 132 |
| $81 \times 8 \times 402$ |  | 132 136 |
| $81 \times 8$ 403 <br> 80  |  | B4 |
| $81 \times 8$ $\times 404$ <br> 80  |  | 134 |
| $81 \times 8.10$ |  | $\begin{aligned} & 134131 \text { A2 } \\ & 135131 \text { A2 } \end{aligned}$ |
| 8185311 |  | P.2 |
| $81 \times x+12$ |  | 132136 |
| 81 $x \times 413$ |  | 134 |
| $81 \times x+14$ |  | 134 |
| 81 xx 1500) |  | $\begin{array}{r} 134131 A 2 \\ 135131 A 2 \\ \hline \end{array}$ |
|  |  | $\begin{aligned} & 134131 A 2 \\ & 135131 ~ A 2 \end{aligned}$ |
| 8814x $\times 601$ |  | B1 |
| $81 \times 8 \times 603$ |  | $\begin{array}{llll} \text { E1 } 136 & \text { B4 } \\ \text { E1 } & \text { B7 } & \text { B5 } \\ \hline \end{array}$ |
| $81 \times x$ |  | $\begin{aligned} & 132 \\ & \text { B3 } \end{aligned}$ |

9335 SRC Table

| $\begin{array}{ll} 9335 & \\ \text { SRC } & \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see Alt thirn Al6 C'ard 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $81 \times x \times 605$ |  | $\begin{aligned} & 134 \\ & 135 \end{aligned}$ |
| $81 \times 8 \times 606$ |  | $\begin{aligned} & 132 \\ & 133 \end{aligned}$ |
| $81 \times x \quad 8607$ |  | $\begin{array}{llll} 134 & 132 & 136 \\ 135 & 133 & 137 \end{array}$ |
| $81 \times 8.8700$ |  | $\begin{aligned} & 131136 A 1 \\ & 131137 ~ A 1 \end{aligned}$ |
| $81 \times 8.801$ | Damaged sectors have been detected. | $\begin{aligned} & 131136 A 1 A 2 \\ & 131137 A 1 A 2 \end{aligned}$ |
| $\times 1 \times x \quad \times 702$ | Ponsible failure caused by incorrectly formated eylinder (000) heads : and ? | $\begin{array}{lll} \text { E } 136 \\ \text { E } 1 \end{array}$ |
| $81 \times 8703$ | Damaged sectors have been detected on cylinder 0000. | $\begin{aligned} & 131136 \text { A } 12 \\ & 131137 A 1 A 2 \end{aligned}$ |
| $81 \times 8 \times 12$ | The Mooicl A0! has failed or the disk needs its sector IDs rewritten. | A1A2A3 |
| $81 \times x \quad \times 713$ | The Model A0I has failed or the disk needs its sector IDs rewriten. | A1 A2 43 |
| $81 \times 8 \times 75$ | The Model A01 has failed | A1 12 A3 |
| $81 \times 8 \times 16$ |  | $\begin{array}{llll} 136 & 132 & 134 \\ 137 & 133 & 135 \\ \hline \end{array}$ |
| $81 \times x \quad \times 717$ |  | $\begin{array}{lllllll} A & B & B & B 2 \\ \text { A1 B1 B5 B } \\ \hline \end{array}$ |
| $81 \times 8 \times 718$ |  | $\begin{aligned} & \text { B4 } \\ & \text { B5 } \end{aligned}$ |


| $\begin{array}{l\|l} \hline 9335 & \\ \text { SRC } \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see A14 thru A16 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $81 \times \mathrm{x} \times 719$ |  |  |
| $8 \mathrm{ix} \times \times 71 \mathrm{~A}$ |  | $\begin{array}{llll} \text { A1 } 136132 \\ \text { A1 } 137133 \end{array}$ |
| $81 \times \mathrm{x} \times 71 \mathrm{~B}$ | The diagnostic sectors on the diagnostic cylinder may have been damaged. Try rewriting the IDs of the sectors on the diagnostics cylinder. | $\begin{array}{llll} \text { Al } & 136 & \text { B1 } \\ \text { A1 } & 137 & 131 \end{array}$ |
| $81 \times x \quad \times 71 \mathrm{C}$ |  | $\begin{array}{\|lllllll} \hline \mathrm{A} & 13 & 134 & 131 \\ \text { A1 } & \mathrm{B} & 7 & 135 & 131 \\ \hline \end{array}$ |
| $81 \mathrm{xx} \times 71 \mathrm{E}$ | The ID fields of the sectors on the diagnostic cylinder need rewriting. | E1 |
| $81 \mathrm{xx} \times 7 \mathrm{~F} 0$ |  | A1 A2[31 D1 D2 D3 |
| $81 \times x \times 7 \mathrm{~F} 1$ |  | A2 B1 D1 D2 D3 |
| $81 \mathrm{xx} \times 7 \mathrm{~F} 2$ |  | $\begin{aligned} & B 4 B 1 A 2 \\ & B 5 B 1 A 2 \\ & \hline \end{aligned}$ |
| 81心, $\times 7 \mathrm{~F} 4$ |  | $\begin{aligned} & 134131 \quad A 2 \\ & 135 \mathrm{B1} \text { A2 } \end{aligned}$ |
| $81 \times x \times 715$ |  | $\begin{array}{\|l\|} \hline 134 \\ 135 \\ \hline \end{array}$ |
| $81 \times x \times 801$ |  | $\begin{aligned} & \text { B1 B4 } \\ & \text { B1 B5 } \end{aligned}$ |
| 81xx $\quad 802$ |  | $\begin{array}{ll} 131 & 134 \\ \text { B31 } & 135 \end{array}$ |
| $81 \times x \times 805$ |  | $\begin{array}{lllllll} B 4 & B 2 & 131 & 136 & A 2 \\ 135 & B 3 & 13 & 1 & B & 7 & A 2 \end{array}$ |

## 9335 SRC Table

| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & 12345678 \end{aligned}$ | Deflinition | For resull code definitions, see $\quad+$ thin Al6 Card 4 Result Code by Prionty |
| :---: | :---: | :---: |
| $\times 1 \times x \quad 810$ |  | $\begin{array}{llll\|} \hline A 1 & \text { E1 B4 } \\ \text { A1 B1 B5 } \end{array}$ |
| $81 \times 8 \times 811$ |  | $\begin{array}{llll\|} \hline \text { A1 B1 B4 } \\ \text { AI B1 B5 } \\ \hline \end{array}$ |
|  |  | $\begin{array}{\|llll} \hline \text { A1 B1 B4 } \\ \text { A1 } 1 \text { B1 } \\ \hline \end{array}$ |
| $81 \times x \quad 820$ |  | $\begin{aligned} & \text { A1 B6 B2 } \\ & \text { A1 B7 B3 } \end{aligned}$ |
| $81 \times x \quad 821$ |  | $\begin{array}{\|llll} \hline A 1 B 6 & B 2 \\ \text { A1 } 137 & \text { B3 } \\ \hline \end{array}$ |
| $81 \times x \times 822$ |  | A1 A2 |
| $81 \times x \times 823$ |  | $\begin{aligned} & \text { B4 B1 } \\ & \text { B5 B1 } \end{aligned}$ |
| $81 \times x \quad 824$ |  | B1 D1 D2 A3 A2 |
| $81 \times \mathrm{xx} \times 26$ | The diagnostic sectors on the diagnostic cylinder may have been damaged. Try rewriting the IDs of the sectors on the diagnostic cylinder. | A2 |
| $81 \times \mathrm{x} \quad 1830$ | The ID fields of the sectors on the diagnostic cylinder need rewriting. | On-site Service Required. |
| $81 \times \times 831$ | The diagnostic sectors on the diagnostic cylinder may have been damaged. Try rewriting the IDs of the sectors on the diagnostic cylinder. | E1 |
| $81 \times x \times 832$ | The ID fields of the sectors on the diagnostic cylinder need rewiting. | On-site Service Required. |
| $81 \times x \quad 833$ | The ID fields of the sectors on the diagnostic cylinder need rewriting. | On-site Service Required. |


| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & 1234 \\ & 5678 \end{aligned}$ | Definition | For result code definitions, see Alf thru Al6 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $81 \times 8$ 834 | The ID fields of the sectors on the diagnostic cylinde: need rewriting. | On-site Service Required. |
| $81 \times \mathrm{xx} 840$ |  | A1 A3 |
| 81xx 8841 |  | A1 A3 |
| 81xx 8842 |  | A1 A3 |
| $81 \mathrm{xx} \times 843$ |  | $\begin{aligned} & \text { B4 A1 B6 B2 } \\ & \text { B5 A1 B7 B3 } \end{aligned}$ |
| $81 \mathrm{xx} \times 844$ |  |  |
| $81 \times \times 845$ |  | A1 A3 |
| 81xx $\times 846$ |  | $\begin{aligned} & \text { B4 A1 } 1 \text { B6 } 62 \\ & \text { B5 A } 11 \mathrm{~B} 7 \mathrm{~B} 3 \end{aligned}$ |
| $81 \times x \times 847$ |  | $\begin{aligned} & \text { B4 A1 } 136 \text { B2 } \\ & \text { B5 A I I } 7733 \end{aligned}$ |
| 81 Lix $\times 848$ |  | A1A3 |
| ¢81xx 8849 |  | A1 A3 |
| $31 \mathrm{xx} \times 84 \mathrm{~A}$ |  | A1 A3 |
| $81 \mathrm{xx} \times 84 \mathrm{~B}$ |  | A! A3 |
| 81xx $\times 84 \mathrm{C}$ |  | $\begin{array}{lll} A 1 & \text { Bi B } \\ \text { A1 Pi B5 } \end{array}$ |
| $81 \times \mathrm{xx} \times 8 \mathrm{D}$ |  | $\begin{aligned} & B 1 \text { B6 A1 } \\ & \text { B1 137 A1 } \end{aligned}$ |
| $81 \mathrm{xx} \times 84 \mathrm{E}$ |  | A2 A1 |

9335 SRC Table

| $\begin{array}{ll} 9335 \\ \text { SIRC } \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see Al4 thm Ai6 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $81 \mathrm{cs} \times 8$. |  | A2 A1 |
| $81 \times 8850$ |  | A2A1 |
| $81 \times 8 \times 85$ |  | A2A1 |
| $81 \times 8 \times 85$ |  | A2 A1 |
| \&13x 8880 |  | $\begin{aligned} & B 1 \text { B4 } \\ & 131 \quad 135 \end{aligned}$ |
| $81 \times 881$ |  | $\begin{array}{lllll} \text { B4 } 42 & \text { B1 } & \text { B6 } & \text { A2 } \\ \text { B5 } & \text { B3 } & \text { B1 } & \text { B7 } & \text { A2 } \end{array}$ |
| 81xi 8882 |  | $\begin{array}{lll} \text { B1 134 D2 } & \text { D3 } & \text { C6 } \\ \text { B1 } 1 & \text { B5 } & \text { D2 } \\ \text { D3 } & \text { C7 } \end{array}$ |
| $81 \times 8883$ |  | $\begin{array}{ll} \text { B1 B4 D2 D3 } & \text { C6 } \\ \text { B1 B5 D2 } & \text { D3 } \end{array}$ |
| $81 \times 884$ |  | $\begin{array}{llll} \text { B1 } 1 \text { B4 } & \text { D2 } 2 & \text { D3 } & \text { C6 } \\ \text { B1 } & \text { B5 } & \text { D2 } & \text { D3 } \end{array}$ |
| $81 \times 8.885$ |  | $\begin{array}{llll} \text { B4 } 42 & \text { B1 B6 } & \text { A2 } \\ \text { B5 } & \text { B3 } & \text { B1 } & \text { B7 } \\ \hline \end{array}$ |
| $81 \times 1886$ |  | $\begin{aligned} & \mathrm{B} 4 \mathrm{~B} 1 \mathrm{~A} 2 \\ & \mathrm{~B} 5 \mathrm{~B} 1 \mathrm{~A} 2 \end{aligned}$ |
| $81 \times 8$ |  | A2 B1 D1 |
| $81 \times \mathrm{x}$ 81 1 |  | A2 Bi D1 |
| $81 \times \mathrm{x} 8 \mathrm{FF} 2$ |  | $\begin{aligned} & \mathrm{B} 4 \mathrm{~B} 1 \mathrm{~A} 2 \\ & \mathrm{~B} 5 \mathrm{~B} 1 \mathrm{~A} 2 \end{aligned}$ |
| $81 \times 8.85$ |  | $\begin{aligned} & \mathrm{B} 4 \mathrm{~B} 1 \mathrm{~A} 2 \\ & \mathrm{~B} 5 \mathrm{~B} 1 \mathrm{~A} 2 \end{aligned}$ |


| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & 123+5678 \end{aligned}$ | Definition | For result code definitions, see Al4 thrin Afo Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| 814.8.81\% |  | $\begin{array}{ll} \mathrm{B} 4 \mathrm{~B} 1 \mathrm{~A} 2 \\ \mathrm{~B} 5 \mathrm{~B} 1 \mathrm{~A} \end{array}$ |
| 818018 F 5 |  | A2 |
| ¢14x 801 |  | M I |
| 81 xa \ 102 |  | $\begin{array}{\|lll\|} \hline 134 & M 1 & 130 \\ 135 & M 11 & 130 \\ \hline \end{array}$ |
| 814 8. 103 | Notor stup tag failed | $\begin{aligned} & 134 \\ & 135 \\ & \hline \end{aligned}$ |
| :14. 104 |  | $\begin{aligned} & \mathrm{B} 4 \mathrm{M} 11 \mathrm{~B} 0 \\ & \mathrm{B5} \text { M1 B0 } \end{aligned}$ |
| 81480805 |  | M11 |
| $81 \times 8.106$ |  | $\begin{aligned} & 134 \\ & 135 \end{aligned}$ |
| $81 \backslash 1.107$ |  | $\begin{aligned} & 134 \\ & B 5 \end{aligned}$ |
| $81 \mathrm{xx} \times 108$ |  | $\begin{aligned} & 134 \\ & 135 \end{aligned}$ |
| $81 \times 1.809$ | Device error during motor start. | $\begin{aligned} & \mathrm{B} 4 \mathrm{M} 1 \\ & \mathrm{B5} \mathrm{M} 1 \end{aligned}$ |
| $815 \times 10 \mathrm{~A}$ | MP detected error or motor uncafe after issuing start. | $\begin{aligned} & \text { B4 M1 } \\ & 135 \mathrm{M} 1 \end{aligned}$ |
| \&1x. XAOB | Motor hense $=80$ after motor start. | $\begin{aligned} & \text { B4 } \\ & \text { B5 } \end{aligned}$ |


| $\begin{aligned} & 9325 \\ & \text { SRC } \\ & 12345678 \end{aligned}$ | Definition | For result code definitions, see Alt thro Al6 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $81 \times x$ x 0 C | Motor running bit not on 67 seconds alter the start tag was issued. | $\begin{aligned} & \text { 134 M1 } \\ & \text { 135 M1 } \end{aligned}$ |
| $81 \times x \quad$ A0D | No-op complete 67 seconds after the start tag was issucd. | $\begin{aligned} & 134 \\ & 135 \end{aligned}$ |
| $81 \times x \quad$ xAF0 | Adapter resct failed | On-site Service Reyuired. |
| 81xx xAF1 | Select failure | On-site Service Required. |
| 81 xx xAF2 | Device reset failed | D)1 D2 1)3 |
| $81 \times x$ xAF3 | Resyne tag failed | On-site Service Requited. |
| $81 \times x$ x 4 | Resyne timeout | On-Site Service Required. |
| 81 xx xAF5 | Device sense tag lated | $\mathrm{P} 1 \mathrm{~B}+135$ |
| $81 \times 8 \times 301$ | Incorrect device selected | On-site Service Required. |
| $81 \times x \times 310$ | Failing direct-access storage unit 0 . | A2 31 |
| $81 \times x$ x ${ }^{11}$ | Failing direct-access storage unit 0 . | A2 131 |
| $81 \times \times \times 12$ | Failing direct-access storage unit 1. | A2 131 |
| $81 \times x \quad \times 1313$ | Failing direct-access storage unit 1. | A2131 |
| $81 \times x \quad \times 314$ | Failing direct-access storage unit 2. | A2 131 |
| $81 \times x \times 1315$ | Failing direct-access storage unit 2. | A2 131 |
| $81 \times x$ xB16 | Failing direct-access storage unit 3 . | A2131 |
| $81 \times \mathrm{xx}$ x 17 | Failing direct-access storage unit 3 . | A2 131 |
| $81 \times x \quad \times 320$ |  | A2 |
| $81 \times x \times 131$ |  | A2 |


| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & \text { 1234 } 5678 \end{aligned}$ | i)efinition | For result code defimtions, see A14 thro A16 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $81 \times x \quad 1322$ |  | A2 |
| $81 \times x \quad \times 1323$ |  | A2 |
| $81 \times x \quad$ x 324 |  | A2 |
| $81 \times x \quad \times 1325$ |  | A2 |
| $81 \times x \quad 1326$ |  | A2 |
| $81 \mathrm{xx} \quad \mathrm{x} 1327$ |  | A2 |
| $81 \times x .1330$ |  | A2 A3 |
| $81 \times x \quad x 1331$ |  | A2A3 |
| $81 \times x \quad \times 1332$ |  | A2 13 |
| $81 \times x \quad \times 133$ |  | A2 13 |
| $81 \times x \quad$ x 1334 |  | A2 A3 |
| $81 \times x \quad \times 1335$ |  | A2 A3 |
| 81×x $\times 1336$ |  | A2A3 |
| 81,xx $\times 1337$ |  | A2 A3 |
| 81 $\times$ x $\quad$ x 1340 |  | A2A3 |
| 81x. $\times 1341$ |  | A2A3 |
| $81 \times x \quad \times 1342$ |  | A2 A3 |
| 81 $\times 8 \times 1343$ |  | A2A3 |
| 81 $\times 8 \times 1341$ |  | A2 A3 |
| $81 \times x \quad$ x 345 |  | A2A3 |
| $81 \times x \quad \times 136$ |  | A2A3 |

Ell Card 3

| 9335 <br> SR( <br> 12345678 | Delinition | For result code decinitions, see Alt thru Alo Card 4 <br> Resinlt Cole by Priority |
| :---: | :---: | :---: |
| $81 \times 13$ |  | A2 A3 |
| Slix - 134 E |  | A2 |
| $\therefore 1 \times x \times 1355$ | Nondevices atailable for testing | On-site Service Required. |
| 81 CN -135E |  | A2 A3 A1 |
| $81 \times 1360$ | Failing direct-iccess storage unit 0 . | A2131 |
| 81x. $\times 1362$ | Failing direct-accesistorage unit 1. | A231 |
| $81 \times 1$ - 364 | Failing direct-atcess sturage unit 2. | A2 131 |
| $81 \times x \times 1366$ | Failing direct-ateess storage unit 3 . | 12131 |
| $81 \times 8.1370$ |  | A2 131 |
| 8180, 1372 |  | 12 131 |
| 81×x 1374 |  | A2 31 |
| 818. 1376 |  | A2 B1 |
|  |  | A2 |
| 81xx $\times 1382$ |  | 12 |
| $\times 10 \times 1384$ |  | A2 |
| $\underline{81 \times x \times 1386}$ |  | 12 |
| $81 \times \times \times 1390$ |  | A2 |
| 818x . 1392 |  | A2 |
| 818x $\times 1394$ |  | A2 |
| $81 \times \times 1396$ |  | A2 |
| 81 xx - BA0 | Failing direct-access storage unit 0 . | B1 A2 |

E12 Card 3

| 93.35 <br> $\therefore R C^{\prime}$ <br> 12.456678 | Detinition | For cmail code definitions. see Alt llirn Alo ('ard $f$ <br> Resill Coda <br> by Priority |
| :---: | :---: | :---: |
| 81xx 1BA2 | Fariling direct-actess somage unt 1. | B1 12 |
| S/xx , B 34 | Failing direct-atcess storage unit 2. | 13112 |
| S14x 13, ${ }^{\text {a }}$ | Failing direct-access storage unit 3. | $131 \times 2$ |
| 82 ar 1000 | Trace mising manline voltage to failing der ice. | On-site Sontice Requmed |
| $\therefore 2 \ 1001$ |  | 11 - |
| 82, 8002 |  | P2P1T1 |
| $82 \times 8003$ |  | 11P2P1C4 |
| ¢20 1004 |  | P2 |
| 82ax 1005 |  | ! 1 |
| ¢2x. 10006 |  | P1 |
|  |  | Pf1 |
| 820 x 5008 |  | P4iP138 139 |
| $882 \times 8.009$ |  | P4P1 M1 |
| - $82 \mathrm{xx} \times 010$ |  | 14 P 1 Ml |
| $88^{2 x x} 8011$ |  | $1^{2}+P^{1} 1 \mathrm{Ml1}$ |
| 828x 8012 |  | P4P1 M11 |
| $8 \mathbf{2 x x ~}^{80} 8013$ |  | PIP4 |
| $82 \mathrm{xx} \times 014$ |  | $P 4 P 1$ |
| $82 \mathrm{xx} \times 015$ |  | P2P112 |
| $88 \mathbf{x x} \times 016$ |  | P1 P2 |
| $82 \times 8 \times 017$ |  | P2 P1 P4 M11 138 139 [34 135 132 133 131 136 137 |

El3 Card 3

| $\begin{aligned} & 9335 \\ & \operatorname{SRC} \\ & 1234 \quad 5678 \end{aligned}$ | Definition | For result code deflinitions, see Alf then AlO Card f <br> Result Code by Priority |
| :---: | :---: | :---: |
| $82 \times x .018$ |  | PI |
| $82 \times x 8019$ |  | 1'2 P4 |
| $82 \times x \quad 0020$ |  | P1 P4 |
| 82 xx , 021 |  | P1 |
| $82 \mathrm{ax} \times 1022$ |  | P1134 |
| $82 \times x \quad 1023$ |  | P1134 |
| $82 \times 31024$ |  | B4M1 P1 P2 P4132 |
| 82 xx . 025 |  | P1 |
| $\therefore 24 x \quad$ x 026 |  | 1'1 P2 132 |
| ¢24x $\times 1027$ |  | P 1 H 2 |
| $82 \times 8.8028$ |  | $\mathrm{P} 1 / 2$ |
| $\because 2 \times 8 \times 029$ | Remote Power on has failed | On-site Service Required. |
| $82 \mathrm{nx} \times 200$ |  | /2130 |
| $82 \mathrm{xx} \times 201$ |  | P1 |
| $82 \mathrm{xs} \times 202$ |  | P1 |
| $82 \times x \quad 203$ |  | 22130 |
| $82 \times x \quad \times 204$ |  | P 1 |
| $82 \times x \quad 205$ |  | B4 P1 |
| $82 \mathrm{xx} \quad 8206$ |  | 135 P1 |
| $82 \times x \quad \times 207$ |  | $P 1$ |
| $82 \times x \times 208$ |  | P1 22 |

El4 Card 3

| 9.335 <br> SRC <br> 12.45678 | Detinution | For restill code definitions, see Alt thru Al6 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $\times 2.11209$ |  | $\begin{aligned} & 134 \\ & 135 \end{aligned}$ |
| 8230211 |  | P1 134 |
| 82481212 |  | P1 135 |
| $82 \times 31215$ |  | P1 M11 B0 |
| 8201216 |  | P1 134 35 |
| 82318 |  | M1 P4 |
| <2以 218 |  | Pi |
| $\therefore 2 \ 1220$ |  | $\begin{aligned} & \mathrm{B} 4 \mathrm{P} 1 \\ & \mathrm{~B} 5 \mathrm{Pl} \end{aligned}$ |
| ¢2い 2221 |  | B1 D1 22130 |
| $\times 2 \times 222$ |  | $\begin{aligned} & 13516 \\ & 13515 \end{aligned}$ |
| $\bigcirc 2 \times 18$ |  | B1 P1 D1 22 B0 |
| 820 824 |  | M1 M2 P2 P1 |
|  |  | $\begin{array}{lllll} \text { B4 } 4 \text { B2 } & \text { B1 } & \text { A2 } & \text { A3 } \\ \text { B5 } & \text { B3 } & \text { B1 } & \text { A2 } & \text { A } \end{array}$ |
| $82 \times 8$ 1500 | Irace open-circuit power cable from main input socket to distributwon box. | On-site Service Required. |
| $82 \times 8 \times 502$ |  | F1 P3 C0 S4 |
| $82 \times x \times 503$ |  | On-site Service Required. |
| $82 \times \times 504$ |  | P3F1C0 |


| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & 1234 \\ & 5678 \end{aligned}$ | Definition | For result code delinitions, see Althru Alo Cirrl 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| 820.8 1505 |  | P3 |
| ¢20x 1506 |  | P3A3A2 A1 |
|  |  | 1 P 3 Cl |
| $82 \times 508$ $\times 80$ |  | 13 $22 \mathrm{A1}$ - |
| $\begin{array}{r}82 \times x \\ \hline 809\end{array}$ |  | P3 A3 A2 A1 |
| $82 \times x \quad \times 510$ <br> 8 |  | P3 |
| $\begin{array}{rll}2 \times x & 1511\end{array}$ |  | P3 A3 |
| $82 \times x$ 8513 |  | P3 12 A3 |
| $82 \times 5$ |  | $13 \wedge 1 / 2$ |
|  |  | S1 |
| $\begin{array}{r}8 \\ \times 2 \times x \\ \times 520 \\ \hline\end{array}$ |  | 21P3 |
| $\begin{array}{r}82 \times x \quad \times 21 \\ \hline 8 .\end{array}$ |  | 2113 |
| $\underline{8} 2 \times x .522$ |  | P3 |
| $\begin{array}{r}82 \times 8 \times 523 \\ \hline 8\end{array}$ |  | 21 |
| 8301.100 |  | $\begin{aligned} & B+\mathrm{B} 2 P 1 \mathrm{P} 1 \\ & \mathrm{~B} 5 \mathrm{~B} 3 \mathrm{P} 1 \mathrm{~B} 1 \end{aligned}$ |
| $83 \times 8101$ |  | $\begin{array}{llll} \mathrm{B} 2 & \text { B6 } & \text { B1 } \\ \mathrm{B} 3 & \text { B7 } & \text { B1 } \end{array}$ |
| $83 \mathrm{xx} \times 102$ |  |  |
| $83 \mathrm{xx} \times 103$ |  | $\begin{array}{llll} B 2 & B 4 & 136 \\ 133 & B 5 & B 7 \\ \hline \end{array}$ |


| 9335 <br> SRC <br> 12345678 | Definition | For result code definitions, see Alt thru Al6 Card 4 <br> Resull Corle by Priority |
| :---: | :---: | :---: |
| $83 \mathrm{xx} \times 104$ |  | $\begin{aligned} & \text { B8 B4 } \\ & \text { B9 B5 } \\ & \hline \end{aligned}$ |
| $83 \mathrm{xx} \times 105$ |  | $\begin{aligned} & 134 \text { B1 } \\ & 135 \quad 131 \end{aligned}$ |
| $83 \mathrm{xx} \times 106$ |  | $\begin{aligned} & \text { P1 B4 } \\ & \text { P1 B5 } \end{aligned}$ |
| $83 \mathrm{xx} \times 107$ |  | $\begin{aligned} & \mathrm{B} 4 \mathrm{MI} \\ & \mathrm{B5} \mathrm{M} \end{aligned}$ |
| $83 \mathrm{xx} \times 108$ | 'a | $\begin{aligned} & \text { P1 } 138 \quad 134 \\ & \text { P1 } 139 \\ & \hline \end{aligned}$ |
| $83 \mathrm{xx} \times 109$ |  | B4 B5 B2 B3 E1 B6 B7 MI |
| $83 \mathrm{xx} \times 10 \mathrm{~A}$ |  | $\begin{aligned} & \text { B6 B2 } \\ & \text { B7 133 } \end{aligned}$ |
| $83 \mathrm{xx} \times 10 \mathrm{~B}$ |  | $\begin{array}{lll} \text { E1 } 136 \\ \text { E1 } 137 \end{array}$ |
| $83 \mathrm{xx} \times 10 \mathrm{C}$ |  | $\begin{array}{lllll} \text { P1 S2 C5 } & \text { B4 } & 132 \\ \text { P1 S2 C5 } & \text { B5 } & \text { B3 } \end{array}$ |
| $83 \mathrm{xx} \mathrm{x} \mathrm{10D}$ |  | $\begin{aligned} & \text { B2 B1 } \\ & \text { B3 B1 } \end{aligned}$ |
| $83 \mathrm{xx} \times 10 \mathrm{E}$ | . | $\begin{aligned} & \text { P1 I38 B4 } \\ & \text { P1 } 139 \quad \text { B5 } \end{aligned}$ |
| $83 \mathrm{xx} \mathrm{x} \mathrm{10F}$ |  | $\begin{array}{llll} \mathrm{B} 4 & \mathrm{~B} 2 & \mathrm{~B} 6 \\ \mathrm{~B} 5 & \text { B3 } & \text { B7 } \end{array}$ |

EI7 Card 3

9335 SRC Table

| $\begin{aligned} & 9335 \\ & \text { sirc } \\ & 12345678 \end{aligned}$ | Definition | For resull code definitions, see Al4 thru A: 6 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $83 \times \times 110$ |  | $\begin{aligned} & \text { B6 E1 } \\ & \text { B7 E1 } \end{aligned}$ |
| $83 \times \times 111$ |  | $\begin{aligned} & 134132138 \\ & 35133139 \end{aligned}$ |
| $83 x \times 12$ |  | $\begin{array}{ll} \mathrm{B} 2 \mathrm{~B} 1 \\ \mathrm{~B} 3 & \mathrm{~B} 1 \end{array}$ |
| 83x : 113 |  | $\begin{array}{\|l\|l\|l\|l\|} \hline \mathbf{E} 1 & 136 & 132 & 138 \\ \text { E1 } & \text { i57 } & \text { B3 } & \text { B9 } \\ \hline \end{array}$ |
| $83 \times \times 114$ |  | $\begin{aligned} & \text { B8 C3 B4 E1 } \\ & \text { B9 C3 } \\ & \hline \end{aligned}$ |
| $83 \times \times 115$ |  | $\begin{aligned} & \text { B2 B4 138 } \\ & \text { B5 } \\ & \hline \end{aligned}$ |
| $83 \mathrm{xx} \times 116$ |  | $\begin{array}{lll} \mathrm{B} 8 \text { M1 B4 } \\ \text { B9 M1 B5 } \end{array}$ |
| $83 \times \times 117$ |  | $\begin{aligned} & \text { B8 B4 S7 } \\ & \text { B9 B5 Si, } \end{aligned}$ |
| $83 \times x \times 118$ |  | $\begin{array}{lllll} \text { P2 S7 } & \text { B4 B2 } & \text { B6 } \\ \text { P2 S7 } & \text { B5 } & \text { B3 } & \text { B7 } \\ \hline \end{array}$ |
| $83 \mathrm{xx} \times 119$ |  | $\begin{aligned} & \text { B4 M1 Pl } \\ & \text { B5 M1 PI } \end{aligned}$ |
| $83 \mathrm{x} \times 11 \mathrm{~A}$ |  | $\begin{array}{llll} \mathrm{B} 2 & 134 & \mathrm{~B} 3 & \mathrm{B5} \\ \mathrm{E} 1 \end{array}$ |
| $83 \mathrm{xx} \times 1113$ |  | $\begin{aligned} & 132 \\ & 133 \\ & \hline \end{aligned}$ |

El8 Card 3

| Attachment SRC <br> 12345678 | Delinition | For result code definitions, see D1 and D2 Card 3 <br> Result Code and Probability \% |
| :---: | :---: | :---: |
| 79xx $\times 104$ | Post event data bus parity error | A274 A124 B101 |
| 79xx $\times 105$ | Post event memory error | A199 |
| 79xx $\times 106$ | Post event address parity error | A274 A124 B101 |
| $79 \mathrm{xx} \times 108$ | Post cvent multiple errors | A174 A224 B101 |
| 79xx $\times 110$ | Post event invalid channel disconnect | A250 A148 B101 |
| $79 \mathrm{xx} \times 111$ | Post event start or halt for an invalid OU | A250 A148 B101 |
| 79xx $\times 112$ | Post event unexpected interrupt | A198 B101 |
| 79xx $\times 1113$ | Post event undefined exception | A199 |
| $79 \mathrm{xx} \times 114$ | Post event spurious interrupt | A174 A224 B101 |
| 79xx $\times 115$ | Post event unidentifiable hardware error | A274 A12.4 B101 |
| $79 \mathrm{xx} \times 119$ | Post event address exception | A278 A110 M210 B101 |
| $79 \mathrm{xx} \times 120$ | Post event microcode exception | A150 M249 |
| $79 \mathrm{xx} \times 121$ | Post event illegal op code | A278 A110 M210 B101 |
| $79 \mathrm{xx} \times 122$ | Post event async lit it excieded | I190 A209 |
| $79 \mathrm{xx} \times 270$ | IOP Scan FRU not found | M499 |
| $79 \mathrm{xx} \times 271$ | IOP RAM load FRU nut found | M499 |
| $79 \mathrm{xx} \times 272$ | IOP diagnostic RAM load FRU not found | M499 |
| $79 \mathrm{xx} \times 273$ | IOA RAM load FRU not found | M499 |
| $79 \times 274$ | 9335 MML . FRU not found | M499 |
| $79 \mathrm{xx} \times 275$ | 933 L IML FRU not found | M499 |


| Attachment SRC <br> 12345678 | Definition | for result code definitions, see D)1 and D2 Card 3 <br> Result Code and Probability "'o |
| :---: | :---: | :---: |
| $79 \times 2 \times 20$ | FOB timeout on a start-up $F O B$ | A109) |
| 79.x $\times 291$ | FOB timeout on an exceute memory and return FOB | A199 |
| $79 \times x \times 292$ | FOB timeotut on a load OU FOB | A19\% |
| $79 \times x \times 293$ | FOB timeout on a write IOA program RAM FOB | A199 |
| $79 \times 2 \times 4$ | Write,red IOA data miscompare | \2か9 M307 \103 |
| $79 \times \mathrm{x} \times 295$ | SCA unable to reset IOP | A160 C139 |
| $79 \times 8.296$ | SCA unable to scan out IOP | A195C104 |
| $79 \mathrm{xx} \times 297$ | SCA unable to scan in IOP | A105C104 |
| $79 \mathrm{xx} \times 298$ | SCA timeout | S199 |
| $79 \times 8 \times 501$ | IOP FRU is failing | A19+Cl05 |
| $79 \times x \quad \times 502$ | IOA FRU is failing | A294 A105 |
| $79 x \times 503$ | Attached device is failing | I194C205 |
| $79 \mathrm{xx} \times 504$ | DFCl cable is failing | C29+ A205 |
| $79 \times 8 \times 98$ | Chamnel husy | A19813101 |
| $79 \mathrm{xx} \times 999$ | Operation program error | M1199 |

## 9332/9335 Attachment RC Table

| Attach Result Code | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| A1 | IOP card (01AB2T2) OUs C0 through CF | 6370282 |
|  | IOP card (02AAIE2) OUs E0 through EF | 6370282 |
| A2 | IOA card (01AB2S2) OUs C0 through C. 7 | 6370240 |
|  | IOA card (01AB2U2) OUs C8 through CF | 6370240 |
|  | IOA card (U2AAID2) OUs E0 through E7 | 6370240 |
|  | IOA card (02AAIK2) OUs E8 through EF | 6370240 |
| B1 | l.ogic board OUs C0 through CF (1) 1 AB 2) | $69 \times 5444$ |
|  | Logic Board OUs EO through EF: (02AA1) | $69 \times 5446$ |
| C1 | IOP cable OUs C0 through CF (01AB2722 to 01AB2Z5) | 6370107 |
|  | IOP cable OUs E0 through EF (from 02AA2E2Z to 02AA2Z2) | 6376110 |
| C2 | Internal signdi cable OUs C0 through C7 (01A's2S2X, Y, Z to 01DA4AI) | $69 \times 5495$ |
|  | internal signal cable OUs C8 through CF (01AB2U2Y, Y, Z to 01DA4BI) | 2445921 |
|  | Internal signal cable OUs E0 through E7 (02AA1D2X, Y, Z to 02BA2A1) | $69 \times 5413$ |
|  | Internal single cable OUs E8 through EF (02AAIK2X, Y, Z to 02BA2BI) | $69 \times 5414$ |
|  | External signal cable (0.6 meter) | 6495253 |

9332/9335 Attachment RC Table

| Allach Result Code | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
|  | External signal cable ( 1.5 meter) | 6495254 |
|  | External signal cable (2.5 meter) | 6495250 |
|  | External signal cable (4.0 meter) | 6495252 |
|  | External signal cable ( 6.0 meter) | 6495251 |
|  | External signal cable ( 10.0 meter) | 6495255 |
|  | External signal cable ( 20.0 meter) | 6495256 |
|  | External signal cable ( 30.0 meter) | 6495257 |
|  | External signal cable ( 40.0 meter) | 6495258 |
|  | External signal cable ( 60.0 meter) | 6495264 |
| C3 | 1/O channel - run channel maps for FRU isolation | On-Site Service Required |
| II | 1/O Devices |  |
| MI | VNIC microcode |  |
| M2 | IOP Microcode |  |
| M3 | External DASD configuration in the MCR is not correct |  |
| M4 | Missing DFCl subsystem modulc | On-Site Service Required |
| S 1 | $\begin{aligned} & \text { SCA - run SCA maps for FRU } \\ & \text { isotation } \end{aligned}$ | On Site Service Required |

## 9335 SRC Table

Positions 2, 6, 7, $\mathbf{8}$ of the System SRC contain positions 1, 2, 3 and $\mathbf{4}$ respectuvely of the 9335 Unit Reference Code. Refer to the IBM 9335, Direct-Access Storage Subsytem: Guide to Umt Reference Codes, SY33-0143., For service actions wheln may he required before or after FRU reptacement.

Use the Result Code and Probability in the table below to determine the service action or parts required.

Position $3,4=$ Actuator OU Number.
$C O=1$ st actuator OU number
$C 1=2$ nd actuator OU number
$C 2=3$ rd actuator OU number
$C 3=4$ th actuator OU number
$C+=5$ th actuator OU number
$C 5=6$ th actuator OU number
$C 6=7$ th actuator OU number
$C 7=8$ th actuator OU number
$C 8=9$ hactuator OU number
$C 9=10$ th actuator OU number
$C A=11$ hactuator OU number
$C B=12$ th actuator OU number
$C C=13$ hatuator OU number
$C D=14$ th actuator OU number
$C E=15 \mathrm{~h}$ actuator OU number
$C F=16 \mathrm{~h}$ actuator OU number
$E 0=17$ th actuator OU number
$E_{1}=18$ th actuator OU number
$E 2=19$ th actuator OU number
$E 3=20 \mathrm{~h}$ actuator OU number
$E t=21$ st actuator OU number
E5 $=22$ nd actuato OU number
$\mathrm{E}_{6}=23$ rad actuator OU number
$E 7=2$ th actuator OU number
$\mathrm{E} 8=25$ th actuator OU number
$\mathrm{E} 9=26$ th actuator OU number
$\mathrm{EA}=27$ th actuator OU number
$E B=28$ th actuator OU number
$E C=29$ th actuator $O U$ number
$E D=30$ th actuator OU number
$E E=31$ st actuator OU number
$\mathrm{EF}=32 \mathrm{nd}$ actuator OU number

Position $5=$ Model Type
$0=$ Cannot determine
$1=$ Model 100
$2=$ Model 200
$B=$ Model 400
$\mathrm{D}=$ Model 500
$\mathrm{E}:=$ Model 700
$F=$ Model 300

| 9335 <br> SRC <br> $1234567 \%$ | Detinition | For result code definitions, see Ai4 liru Aío Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $80 \times 8.8000$ | No error was detected by the diagnostic test programs |  |
| 81.x x0FC | This is: a microcode problem. Call for support. | A3 |
| 81.0 .01 L | Diagnostic test run option not set correctly. "Abort," "bypass crror," or "stop on error" not set. Set "stop on erro" and restart. | On-site service required. |
| $81 \times 8.800$ |  | B1 D1 D2 D3 A2 |
| 81x. 8101 |  | On-site service required. |
| \& $1 \times x \times 102$ |  | P1 |
| $81 \mathrm{xx}, 103$ |  | On-site service required. |
| 181xx .104 |  |  |
| $881 \times 8$ |  | $\begin{array}{\|l\|l\|l\|} \hline \text { M1 B4 B0 } \\ \text { M1 B5 B0 } \\ \hline \end{array}$ |
| 815 107 |  | $\begin{aligned} & \text { M1 B4 } \\ & \text { M1 B5 } \end{aligned}$ |
| 815x 8108 |  | $\begin{aligned} & \text { B4 M1 B0 } \\ & \text { B5 M1 B0 } \end{aligned}$ |
| $81 \times 8.109$ |  | P1 |
| $81 \times \mathrm{x} \times 10 \mathrm{~A}$ |  | P1 |
| $81 \mathrm{xx} \times 1013$ |  | $\begin{array}{lll} \text { M1 B4 B0 } \\ \text { M1 B5 B0 } \end{array}$ |
| 81xx $\times 10 \mathrm{C}$ |  | $\begin{aligned} & \text { B4 B0 } \\ & \text { B5 B0 } \end{aligned}$ |


| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & 12345678 \\ & \hline \end{aligned}$ | Deflinition | For result code definitions, see Alt thru Al6 Card 4 <br> Result Cude by Priority |
| :---: | :---: | :---: |
| 818x $\times 101)$ |  | M11 |
| $815 x$, 10E |  | $\begin{aligned} & 134130 \\ & 135130 \end{aligned}$ |
| $81 \mathrm{xx} \times 10 \mathrm{~F}$ |  | P1 B4 |
|  |  | M2 |
| $81 \mathrm{xx} \times 111$ |  | $\begin{aligned} & 134!2 \\ & 135!1 \end{aligned}$ |
| $81 \mathrm{xx} \times 112$ |  | Z2 M1 |
| 813 |  | $\begin{array}{lll} \mathrm{B} 4 \mathrm{~B} 1 \mathrm{D} 1 \mathrm{D} 2 & \mathrm{D} 3 \mathrm{~A} \\ \mathrm{~B} 5 \mathrm{~B} 1 \mathrm{D} 1 \mathrm{D} 2 & \mathrm{D} 3 \mathrm{~A} \end{array}$ |
| :1xx . 114 |  | $\begin{aligned} & 134130 \\ & 135130 \\ & \hline \end{aligned}$ |
| :1x x 115 |  | $\begin{array}{lllll} \hline \text { P1 } & 134 & B 0 \\ \text { P1 } & 135 & B 0 \\ \hline \end{array}$ |
| $81 \times x \times 116$ |  | $\begin{aligned} & 136 \mathrm{C} 6 \\ & 137 \mathrm{C} 7 \end{aligned}$ |
| $\begin{array}{ll}81 \times x & \times 117\end{array}$ |  | P1 /22 131135 |
| $81 \times x \times 118$ |  | B1 D1 D2 D3 |
| $81 \times \mathrm{x} \times 119$ |  | P4 P2 P1 B4 M1 132 <br> B1 <br> P4 P2 P1 B5 M1 B3 B1 |
| $81 \times x \times 11 \mathrm{~A}$ |  | $\begin{array}{\|l\|lll} \text { M1 } 1 \text { B4 } & \text { B0 } \\ \text { M1 } & \text { B5 } & \text { B0 } \\ \hline \end{array}$ |

9335 SRC Table

| $\begin{aligned} & 0335 \\ & \text { SRC } \\ & 123+5678 \end{aligned}$ | Deflinition | For resull code definitions, see Alf thru Al6 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
|  |  | $\begin{aligned} & \mathrm{B} 4 \mathrm{M} 1 \mathrm{~B}) \\ & \mathrm{B5} \mathrm{M} 1 \mathrm{~B} \end{aligned}$ |
| 81x : 11 C |  | $\begin{aligned} & 134130 \\ & 135130 \end{aligned}$ |
| Six $\times 111$ ) |  | M1 |
| $81 \times x .11 \mathrm{E}$ |  | $\begin{aligned} & 134 \\ & 135 \end{aligned}$ |
| 21x, 1115 |  | M1 P1 130 |
| \&148 $\times 120$ |  | P1 134 |
| ¢14 x 121 |  | $\begin{aligned} & 134 \\ & 135 \\ & \hline \end{aligned}$ |
| $81 \times x \times 122$ |  | $\begin{aligned} & \text { P1 M1 B4 } \\ & \text { P1 M1 B5 } \end{aligned}$ |
| $81 \times x \times 123$ |  | P1 |
| $81 \times x \times 124$ |  | P1 C3 |
| $81 \times x \times 125$ |  | M1 M2 |
| $8 \times 126$ <br> 8 |  | M1 |
| $81 \mathrm{xx} \times 127$ |  | $\begin{aligned} & \mathrm{B} 4 \mathrm{B2} \mathrm{M} 1 \\ & \mathrm{B5} \\ & \mathrm{IB} 3 \mathrm{M} 1 \end{aligned}$ |
| $81 \times x \times 128$ |  | $\begin{array}{llllll}\text { B2 } & \text { B4 } & \text { M1 } & \text { i36 } & \text { C6 } 6 & \text { B0 } \\ \text { B3 } & \text { B5 } & \text { M1 } & \text { I3 } & \text { C7 } & \text { I30 }\end{array}$ |
| $81 \times x \times 129$ |  | $\begin{array}{\|llllll} \hline \text { B8 C8 } 8 & \text { C3 } & \text { B4 } & \text { E1 } 1 \mathrm{~B} 0 \\ \text { B9 C9 } & \text { C3 } & \text { B5 } & \text { E11 } \\ \hline \end{array}$ |


| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & 1234 \\ & \\ & \hline \end{aligned}$ | Definition | For result code definitions, see A14 thru A16 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $81 \times x \times 12 \mathrm{~A}$ |  | $\begin{aligned} & \text { B6 B4 } \\ & \text { B7 B5 } \end{aligned}$ |
| $81 \times \mathrm{x}$ x 12 B |  | M1 |
| $81 \mathrm{xx} \times 12 \mathrm{C}$ |  | B1 B0 |
| $81 \mathrm{xx} \times 12 \mathrm{D}$ |  | M1 P2 P1 M2 |
| $81 \mathrm{xx} \times 12 \mathrm{E}$ |  | M1 P2 M2 E1 |
| $81 \mathrm{xx} \times 12 \mathrm{~F}$ |  | $\begin{aligned} & 136 \mathrm{C} 6 \\ & \mathrm{B7} 7 \end{aligned}$ |
| $81 \mathrm{xx} \times 130$ |  | B1 |
| $81 \mathrm{xx} \times 170$ |  | $\begin{aligned} & \text { B4 B1 } \\ & \text { B5 B1 } \end{aligned}$ |
| $81 \times x \times 171$ | An open-circuit cable was detected | D1 D2 D3 B1 A2 |
| $81 \mathrm{xx} \times 172$ |  | B4 P1 131 B5 P1 B1 |
| $81 \times x \times 173$ |  | $\begin{aligned} & \mathrm{B4} \\ & \mathrm{B5} \end{aligned}$ |
| $81 \mathrm{xx} \times 174$ |  | $\begin{aligned} & \text { B4 } \\ & \text { B5 } \end{aligned}$ |
| $81 \mathrm{xx} \times 175$ |  | $\begin{aligned} & \text { B4 B1 } \\ & \text { B5 B1 } \end{aligned}$ |
| $81 \mathrm{xx} \times 176$ |  | B4 P1 B1 A2 B5 P1 B1 A2 |
| 81xx 177 |  | $\begin{array}{\|ll} \text { B1 B4 } \\ \text { B1 B5 } \end{array}$ |


| $\begin{aligned} & \text { 9335 } \\ & \text { SRC } \\ & 12345678 \end{aligned}$ | Definition | For result code definitions, see A14 thru Al6 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $81 \times \times \times 17 \%$ |  | $\begin{aligned} & \mathrm{B} 1 \mathrm{~B} 4 \mathrm{~A} 2 \\ & 31 \mathrm{~B} 5 \mathrm{~A} \end{aligned}$ |
| $81 \times \times \times 179$ |  |  |
| $81 \mathrm{xx} \times 17 \mathrm{~A}$ |  | $\begin{array}{llll\|} \hline B 2 & B 4 & \text { B0 } \\ \text { B3 } & \text { B5 } & \text { B0 } \\ \hline \end{array}$ |
| $81 \mathrm{xx} \times 1713$ |  | $\begin{array}{\|llllll} \hline \mathrm{B} 4 & \mathrm{~B} 2 & \mathrm{P} 1 & \mathrm{~B} 6 & 138 \\ \mathrm{~B} 5 & \mathrm{~B} 3 & \mathrm{P} 1 & \mathrm{~B} 7 & \mathrm{~B} 9 \\ \hline \end{array}$ |
| $81 \times \mathrm{x} \times 17 \mathrm{C}$ |  | $\begin{aligned} & \text { B2 B6 } \\ & \text { B3 B7 } \end{aligned}$ |
| $81 \times \mathrm{x} \times 17 \mathrm{D}$ |  | $\begin{aligned} & \text { B2 B6 } \\ & \text { B3 B7 } \end{aligned}$ |
| 81xx $\times 17 \mathrm{E}$ |  | B1 |
| $81 \times x \times 180$ |  | $\begin{array}{ll} \mathrm{B} 1 \mathrm{B4} \\ \mathrm{~B} 1 \mathrm{~B} 5 \end{array}$ |
| $81 \mathrm{xx} \times 181$ |  | $\begin{aligned} & \text { B1 B4 } \\ & \text { B1 B5 } \end{aligned}$ |
| $81 \times x \times 182$ | The "device select" line is active on a device | B1 A2 |
| $81 \mathrm{xx} \times 183$ |  | A1 |
| $81 \mathrm{xx} \times 185$ |  | $\begin{array}{lllll} \text { P4 } & \text { P2 P1 B4 } & \text { M1 } & \text { B2 } \\ \text { B1 } & & & \\ \text { P4 } & \text { P2 P1 } & \text { B5 } & \text { M1 B3 } \\ \text { B1 } & & & \\ \hline \end{array}$ |

DIO Card 3

|  |  | For result code definitions, <br> see A14 thru A16 Card 4 <br> 9335 <br> SRC <br> 1234 <br> 5678 |
| :--- | :--- | :--- |
| $81 \times \times \times 186$ | Definition |  |
| 8 Result Code |  |  |
| by Priority |  |  |

DII Card 3

| $\begin{aligned} & \text { 9335 } \\ & \text { SRC } \\ & 1234 \\ & \hline 12678 \end{aligned}$ | Definition | For result code definitions, <br> sec A14 thri A16 Card 4 <br> Result Code <br> by Priority |
| :---: | :---: | :---: |
| $81 \times x \times 20 A$ |  | $\begin{aligned} & 138 \\ & \text { B9 } \end{aligned}$ |
| $81 \times x$ x2013 |  | $\begin{array}{\|l\|} \hline 134 \\ 135 \\ \hline \end{array}$ |
| $81 \times x$ x20C |  | $\begin{array}{\|r} \text { B4 } \\ \text { B5 } \\ \hline \end{array}$ |
| $81 \times x$ 20D |  | $\begin{aligned} & \text { B4 } \\ & \text { B5 } \end{aligned}$ |
| $81 \mathrm{xx} \times 20 \mathrm{E}$ |  | $\begin{array}{\|l\|l\|l} \hline \text { B4 B2 } \\ \text { B5 B3 } \\ \hline \end{array}$ |
| $81 \times \mathrm{x} \times 20 \mathrm{~F}$ |  | $\begin{aligned} & \text { 134 B2 } \\ & \text { B5 B3 } \\ & \hline \end{aligned}$ |
| $81 \mathrm{xx} \times 210$ |  | $\begin{aligned} & \mathrm{B} 2 \mathrm{E}: \mathrm{B} 6 \\ & \mathrm{~B} 3 \mathrm{E} 1 \\ & \hline \end{aligned}$ |
| $81 \times x \times 214$ |  | $\begin{array}{\|l} \hline \text { B4 B2 E1 B6 } \\ \text { B5 B3 E1 B7 } \\ \hline \end{array}$ |
| $81 \times x \times 215$ |  | $\begin{array}{ll} \text { B4 B2 } \\ \text { B5 B3 } \end{array}$ |
| $81 \times x \times 216$ |  | B2 E1 B6 B4 P1   <br> B3 E1 B7 B5 P1 |
| $81 \times \times 217$ |  | $\begin{array}{\|l\|l\|} \hline \mathbf{B 2} \\ \text { B3 } \\ \hline \end{array}$ |
| $81 \times x \times 218$ |  | $\begin{aligned} & \text { B6 B2 B4 } \\ & \text { B7 B3 B5 } \\ & \hline \end{aligned}$ |

DI 2 Card 3

| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & 123+5678 \\ & \hline \end{aligned}$ | Deflimition | For remult code definitions, <br>  <br> Resull Code bey I'riority |
| :---: | :---: | :---: |
| 814x 8219 |  | $\begin{aligned} & 138134 C 3 \\ & 139135(3 \end{aligned}$ |
| $81 \times 21.1$ |  | $\begin{aligned} & 13+138 \\ & 135139 \end{aligned}$ |
| $81 \times 3 \quad 2113$ |  | $\begin{array}{\|l\|l} 136 & 138 \\ 137 & 139 \end{array}$ |
| $81 \times 21 \mathrm{C}$ |  | $\begin{aligned} & 134 \\ & 135 \end{aligned}$ |
| $814.211)$ |  | $\begin{aligned} & 132130 \\ & 133 \\ & \hline \end{aligned}$ |
| $81 \times \times 21 \mathrm{E}$ |  | $\begin{aligned} & 13213+131 \\ & 133135131 \end{aligned}$ |
| S10x vzili |  | $\begin{aligned} & 134 \\ & 135 \end{aligned}$ |
| $81 \times x \times 220$ |  | 11 |
| $81 \times 8 \times 221$ |  | $\begin{aligned} & 13+138 \\ & 135139 \end{aligned}$ |
| $81 \mathrm{xx} \times 222$ |  | $\begin{aligned} & 134 \\ & 135 \end{aligned}$ |
| $81 \times \times \times 223$ |  | $\begin{array}{\|llll} 132 & 136 & 134 & 138 \\ 133 & 137 & 135 & 139 \\ \hline \end{array}$ |
| $81 \times \times 224$ |  | $\begin{aligned} & 134 \\ & 135 \end{aligned}$ |
| $81 \times x \times 225$ |  | $\begin{aligned} & 134132 \\ & 135133 \end{aligned}$ |

933．5 SR（＇rable

| 93.35 <br> SRC <br> 123456,78 | Delinition | For resull code delinitiom， see Alt llum A16 Caril th Resull code by Prionty |
| :---: | :---: | :---: |
| 814．81226 |  | $\begin{aligned} & 13+132136 \\ & 135133137 \end{aligned}$ |
| 814 227 |  | $\begin{aligned} & 134128 \mathrm{P} \\ & 135139 \mathrm{P} 1 \end{aligned}$ |
| ぐい 228 |  | $\begin{aligned} & 13.4 \\ & 135 \end{aligned}$ |
| \＄111 229 |  | $\begin{aligned} & 134 \text { P' } 130 \\ & B 5 \mathrm{P} 180 \end{aligned}$ |
| $\times 141220$ |  | 134 1＇1 138 135 Pl 139 |
| $\because 13122 \mathrm{C}$ |  | B1 |
| 814 220 |  | $\begin{aligned} & 134 \\ & 135 \end{aligned}$ |
| 8158 |  | $\begin{aligned} & 13.4 \\ & 135 \end{aligned}$ |
| $818 \times 221$ |  | 131 |
| S14x $\times 230$ |  | $\begin{aligned} & 134138132 \\ & 135139133 \end{aligned}$ |
| $81 \times 8.231$ |  | $\begin{aligned} & 134136 \\ & 135 \\ & 137 \end{aligned} 133889$ |
| 81×x $\times 232$ |  | $\begin{array}{llllll} 136 & B 1 & A 1 & 132 \\ B 7 & 131 & A & 133 \end{array}$ |
| $81 \times 8 \times 233$ |  | $\begin{aligned} & \text { E } 1136 \\ & \text { E } 1187 \end{aligned}$ |

D14 Card 3

| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & 123.36568 \end{aligned}$ | Delinition | For resull code delinitions， see Alt lhmi Alo Call 4 <br> Result code by I＇riorily |
| :---: | :---: | :---: |
| 81481234 |  | 131 |
| S小¢ 1235 |  | $\begin{aligned} & 134132 \\ & 135133 \end{aligned}$ |
| ミ小 23.36 |  | $\begin{aligned} & 132134 \\ & 133135 \end{aligned}$ |
| 814 1237 |  | $\begin{aligned} & 132136 \\ & 133137 \end{aligned}$ |
| \11 1238 |  | $\begin{aligned} & 1341361: 1 \\ & 1351371: 1 \end{aligned}$ |
| S14．239 |  | $\begin{aligned} & 134 \\ & 135 \end{aligned}$ |
| 81 11123.1 |  | $\begin{aligned} & 186 \quad 13+132 \\ & 137 \quad 135 \quad 133 \end{aligned}$ |
| ¢1 23313 |  | $\begin{aligned} & 132134 \\ & 133135 \end{aligned}$ |
| 8111 |  | $\begin{aligned} & 134138 \mathrm{P} 1 \\ & 135 \mathrm{~B} 9 \mathrm{P} 1 \end{aligned}$ |
| 818x（230） |  | $\begin{aligned} & 13+132 \\ & 135133 \end{aligned}$ |
| $81 \times 2.31 \%$ |  |  |
| $81 \times 8.85$ |  | $\begin{aligned} & 134 \mathrm{~B} 8 \mathrm{P} 2 \\ & 35 \mathrm{B9P2} \end{aligned}$ |
| $81 \times x \times 240$ |  | $\begin{array}{llll} \mathrm{B} 2 & \mathrm{~B} 6 & \mathrm{~B} 4 & \mathrm{P} 2 \\ \mathrm{~B} 3 & \mathrm{~B} 7 & \mathrm{~B} 5 & \mathrm{P} 2 \end{array}$ |

933 § SRC ’able

| $\begin{aligned} & 9335 \\ & \operatorname{sic} \\ & 1234 \quad 5678 \end{aligned}$ | Definioion | For resnlt code detinitions， see Alt thro Al6 C＇ard 4 <br> Resmilt Code by Priority |
| :---: | :---: | :---: |
| ¢111 241 |  | $\begin{aligned} & 134132 \\ & 135133 \end{aligned}$ |
| S131242 |  | $\begin{array}{llll} \begin{array}{lll} 3 & 131 & 30 \\ 13 & 13 & 130 \end{array} \end{array}$ |
| 810 1243 |  | $\begin{aligned} & 134132 \\ & 135 \\ & \hline \end{aligned}$ |
| $81 \times 1244$ |  | $\begin{array}{\|l\|l\|l\|} \hline 136 \quad 132 \\ 137 \quad 133 \\ \hline \end{array}$ |
| 又小い 215 |  | $\begin{aligned} & 134 \\ & 1358 \\ & 135 \end{aligned} 139233$ |
| $\therefore 1 \backslash$ ，246 |  | $\begin{array}{lll} \mathrm{B} & \mathrm{~B} 2 \\ \mathrm{~B} & \mathrm{~B} & \mathrm{~B} \end{array}$ |
| $81 \times 1.247$ |  | 131 A2 A1 |
| 8141048 |  | $\begin{aligned} & \text { E1B6 } \\ & \text { E } 1 \text { B7 } \end{aligned}$ |
| 8110249 |  | $\begin{aligned} & 136132 \\ & 137133 \end{aligned}$ |
| $\therefore 14.240$ |  | $\begin{aligned} & 134 \mathrm{~B} 1 \mathrm{~A} 2 \mathrm{A1} \\ & 135 \mathrm{~B} 1 \mathrm{~A} \mathrm{~A} \end{aligned}$ |
| $814 \times 2413$ |  | $\begin{aligned} & 138 \quad 134 \\ & 139135 \end{aligned}$ |
| $81 \mathrm{x} \times 24 \mathrm{C}$ |  | $\begin{aligned} & 138136 \\ & 139 \quad 137 \\ & \hline \end{aligned}$ |
| $81 \times 8 \times 240$ |  | $\begin{aligned} & \text { B2 } \\ & \text { B3 } \end{aligned}$ |


| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & 1234 \\ & 5678 \end{aligned}$ | Definition | For result code delinitions, see Alt thrm Al6 Caril 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $81 \mathrm{xx} \times 24 \mathrm{E}$ |  | $\begin{aligned} & \mathrm{E} 1 \mathrm{~B} \\ & \mathrm{E}: \mathrm{B} 7 \end{aligned}$ |
| $81 \times \mathrm{x} \times 24{ }^{5}$ |  | $\begin{array}{lll} \text { E } 1136 \\ \text { E } 1 & 137 \end{array}$ |
| $811 \times x \times 250$ |  | A2 |
| $81 \mathrm{xx} \times 251$ |  | $\begin{array}{\|l\|l\|} \hline \text { B4 } \\ \text { B5 } \end{array}$ |
| $81 \mathrm{xx} \times 252$ | The diagnostic programs have detected a response time-out. This may be caused by a recent external cable plugging. | $\begin{aligned} & 134131 \\ & 135131 \end{aligned}$ |
| 81xx $\times 253$ |  | A2B1D1 D2 D3 |
| $81 \times \mathrm{xx} \times 254$ |  | $\begin{aligned} & \mathrm{B} 3 \mathrm{~B} 1 \mathrm{~A} 2 \\ & \mathrm{B5} \mathrm{~B} 1 \mathrm{~A} 2 \end{aligned}$ |
| $81 \times \mathrm{x} \times 255$ |  | $\begin{aligned} & 134 \\ & 135 \end{aligned}$ |
| $81 \times x \times 256$ |  | A2 |
| $81 \times x \times 257$ |  | B1 A2 |
| $81 \times x \times 300$ |  | $\begin{array}{lll} \text { B4 } & \text { B6 } & \text { B2 } \\ \text { B5 } & \text { B7 } \end{array}$ |
| $81 \times x \times 301$ |  | $\begin{aligned} & \text { E1 } 1 \text { B4 B2 B6 } \\ & \text { E1 } 1 \text { B5 } 33 \text { B7 } \end{aligned}$ |
| $81 \times x \times 302$ |  | E1 B4 B2 B6 E1 B5 B3 B7 |


| $\begin{aligned} & \text { 9335 } \\ & \text { SRC } \\ & 12345678 \\ & \hline \end{aligned}$ | Definition | For result code definitions, see AIt limu AlG Carid 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $81 \mathrm{xx} \times 303$ |  | $\begin{aligned} & \text { E1B6 B6 } \\ & \text { E1 B7 } \end{aligned}$ |
| $81 \times x \times 304$ |  | $\begin{aligned} & \text { E1 B4 B6 B2 } \\ & \text { E. } 135 \text { B7 B3 } \end{aligned}$ |
| $81 \mathrm{xx} \times 305$ |  | $\begin{aligned} & \text { B6 E1 B2 } \\ & \text { B7 E1 B3 } \end{aligned}$ |
| $81 \mathrm{xx} \times 306$ |  | $\begin{array}{lllll} \text { E1 } 1 \text { B4 B2 B6 } \\ \text { E1 } 12 & \text { B3 } \\ \hline \end{array}$ |
| $81 \times x \times 30 \%$ |  | $\begin{array}{\|l\|llll} \hline \text { L1 } 134 & \text { B2 } & \text { B6 } \\ \text { E1 } 135 & \text { B3 } & \text { B7 } \\ \hline \end{array}$ |
| $81 \times x \times 308$ |  | $\begin{aligned} & \text { B6 E1 B2 } \\ & \text { B7 E1 B3 } \end{aligned}$ |
| $81 \times x \times 309$ |  | $\begin{aligned} & \text { E1 B4 B2 B6 } \\ & \text { E1 B5 B3 B7 } \end{aligned}$ |
| $81 \times x$ x 30 A |  | $\begin{aligned} & \text { E1 B4 B2 B6 } \\ & \text { E1 B5 B3 B7 } \end{aligned}$ |
| $81 \times \mathrm{x} \times 30 \mathrm{~B}$ |  |  |
| $81 \mathrm{xx} \times 30 \mathrm{C}$ |  | $\begin{aligned} & \mathrm{B} 2 \mathrm{~B} 4 \mathrm{~B} 6 \\ & \mathrm{~B} 3 \mathrm{~B} 5 \\ & \mathrm{~B} 7 \mathrm{E} \end{aligned}$ |
| $81 \mathrm{xx} \times 30 \mathrm{D}$ |  | $\begin{aligned} & \text { B2 B6 } \\ & \text { B3 B7 } \end{aligned}$ |
| $81 \times x$ x 30 E |  | $\begin{aligned} & \text { B2 B4 B6 E1 } \\ & \text { B3 B5 B7 E1 } \end{aligned}$ |

D18 Cadd 3

| $\begin{array}{\|c\|} \hline \mathrm{SRC} \\ 1234 \\ \hline \end{array}$ | 5678 | Definition | For result code delimitions, see C9 thru C1I Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| F001 | 0321 | Save revtore adapter dynamic test | A099 |
| For. | 0332 | IOC connole data storage test | A199 |
| Fo(0) | 03338 | Load SCA Ram 1 | Y145S145 A10513105 |
| 10001 | 033C | Inad SCA Ram2 | $\begin{aligned} & \mathrm{Y} 1+5 \mathrm{~S} 1+5 \mathrm{Al} 105 \mathrm{~B} 103 \\ & \mathrm{E} 102 \end{aligned}$ |
| 1.001 | 03440 | I.wad keyrovad translate table | Y1+5SI+5 $1105 \mathrm{B105}$ |
| Foon | 0342 | Channel bus tent | $\begin{aligned} & \text { E135 A125 M1110 } \\ & \text { M210 ZY06 } / 206 \\ & \text { ZX06 ZW02 } \end{aligned}$ |
| 10061 | 0344 | (PU) ISSD) | $\begin{aligned} & \mathrm{E} 185 \mathrm{B105C} \mathrm{C} 105 \mathrm{~J} 103 \\ & \mathrm{~S} 102 \end{aligned}$ |
| 10001 | 0348 | CPLICStent Models | $\begin{aligned} & \text { T170 E120 } 11105 \text { U103 } \\ & \text { K102 } \end{aligned}$ |
| 1001 | 0349 | CPLCStent-Model 3 CS | $\begin{aligned} & \text { T170 E120 H105 U103 } \\ & \text { K102 } \end{aligned}$ |
| 1.001 | 03413 | Chan Conterter card diagnostics | $\begin{aligned} & \text { E125 M125 M1225 } \\ & \text { N105 Z205 ZX05 } \\ & \text { ZY05 H105 } \end{aligned}$ |
| +(0) | 0350 | Intialize BSM scan rings | $\begin{aligned} & \text { E185 B105C105. } 1103 \\ & \text { S102 } \end{aligned}$ |
| FOO) | 0351 | Luad CPU CS - Planar diagnostic | $\begin{aligned} & \mathrm{E} 180 \mathrm{B1} 105 \mathrm{C} 105 \mathrm{~J} 105 \\ & \text { S103 K102 } \end{aligned}$ |
| FOO) | 0.552 | Planar test | $\begin{aligned} & \text { E160 T130 U105 A102 } \\ & \text { J101 Yiot Slot } \end{aligned}$ |

Machine Cleck SRC Table

| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code delinitions, see C9 thru Cl) Card 3 <br> Result Codes and Probalility " $\%$ |
| :---: | :---: | :---: | :---: |
| 1.001 | 0354 | L.mad CPU CS - VAT, MS test | $\begin{aligned} & \mathrm{E} 180 \mathrm{~B} 105 \mathrm{Cl} 05 \mathrm{~J} 104 \\ & \mathrm{~S} 104 \mathrm{Y} 102 \end{aligned}$ |
| 10001 | 0356 | 1, 11 MS 心- | $\begin{aligned} & \text { B675 E120 H1102 X102 } \\ & \text { K101 } \end{aligned}$ |
| 1.10) | 0358 | -nad CPU CS VAT Chamel test | $\begin{aligned} & \text { E180 B105 C105 J104 } \\ & \text { S10+Y102 } \end{aligned}$ |
| 1001 | 035 C | V'at channel 心- | $\begin{aligned} & \text { E135 A125 M110 } \\ & \text { M210 Z206 ZX06 } \\ & \text { ZYo6 ZW02 } \end{aligned}$ |
| 10001 | 0360 | L.oad main storage | $\begin{aligned} & \text { B635 E130 \115T110 } \\ & \text { S110 } \end{aligned}$ |
| 1001 | 0368 | Stat Vinc initialization | $\begin{aligned} & \text { B640 I125 E125 X105 } \\ & \text { U105 } \end{aligned}$ |
| 1001 | 0.410 | Read nense cmd to n2PC | $\begin{aligned} & \text { A125 } 13: 25 \text { Y125S115 } \\ & \text { D110 } \end{aligned}$ |
| 1001 | 0.414 | Recalibrate cmd to 62PC Device | Y145S1+5 A105 B105 |
| 1901 | 0.18 | Index tert to 62PC Device | Y145S145 A105 B105 |
| F(0)1 | $0+1 \mathrm{~A}$ | Reser amd io 62PC Device | $\begin{aligned} & \text { Y140 S135 A107 B107 } \\ & \text { E102 D102 C402 ZY02 } \\ & \text { M102 K101 } \end{aligned}$ |
| F001 | 0.420 | Rd werify emd to 62PC Device (TRK $3.59)$ | V145S145 A105 B105 |
| F001 | 0.28 | Mode cmd to 62PC attachment | Y190 A105 3105 |
| F001 | 042 A | SCA tist | $\begin{aligned} & \mathrm{B} 140 \text { C130 A120 D105, } \\ & \text { E105 } \end{aligned}$ |


| $\begin{array}{\|c} \text { SRC } \\ 1234 \\ \hline \end{array}$ | 5678 | Dctinition | For result code definitions, see C9 thru Cll Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| F001 | 042C | Load SCA directory from h2PC Device | $\begin{aligned} & \text { Y145S140 A105 B105 } \\ & \text { C105 } \end{aligned}$ |
| F001 | 0421) | IOC test | $\begin{aligned} & \mathrm{A} 150 \mathrm{~B} 120 \mathrm{C} 1: 0 \mathrm{~B}, \mathrm{i} 05 \\ & \mathrm{E} 105 \end{aligned}$ |
| F001 | 042E | Power up/check status of Feature | A140 B130 C130 |
| F001 | 042F | Save, restore adapter dynamic test | $\mathrm{AO}_{99}$ |
| F001 | 0432 | IOC console data storage test | A199 |
| F001 | 0438 | Load SCA Raml | Y145S 145 A105 13105 |
| F001 | 043C | Load SCA Ram2. | $\begin{aligned} & \text { Y145S145 A105 B103 } \\ & \text { E102 } \end{aligned}$ |
| F001 | 0440 | Load keyboard translate tahle | Y145S145 A105 B105 |
| F001 | 0450 | Initialize BSM scan rings | $\begin{aligned} & \text { E185 B105 C105 J103 } \\ & \text { Sl02 } \end{aligned}$ |
| F001 | 0451 | Load CPU CS - Planar diagnostic | $\begin{aligned} & \text { E180 B105 C105 J105 } \\ & \text { S103 Y102 } \end{aligned}$ |
| F001 | 0452 | Planar test | E160 T130 U105 Al02 Jloi Yi01 Sloi |
| F001 | 0454 | Load CPU CS - VAT/MS test | $\begin{aligned} & \text { E180 B105 Cl05 J104 } \\ & \text { S104 Y102 } \end{aligned}$ |
| F00! | 0456 | VAT/MS test | $\begin{aligned} & \text { B675 E120 II102 X102 } \\ & \text { K101 } \end{aligned}$ |
| F001 | 0458 | Load CPU CS - VAT/Channel test | $\begin{aligned} & \text { E180 B105 C105 J104 } \\ & \text { S104 Y102 } \end{aligned}$ |


| $\begin{array}{\|ll\|} \hline \text { SRC } & \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see C9 thru Cll Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: |
| F001 045C | Vatichannel test | $\begin{aligned} & \text { E135 A125 M110 } \\ & \text { M210 Z206 ZX } 06 \\ & \text { ZY06 ZW02 } \end{aligned}$ |
| F001 0460 | Load main storage | $\begin{aligned} & \text { B635 EI } 130 \text { Y } 115 \mathrm{~T} 110 \\ & \text { S110 } \end{aligned}$ |
| F001 0468 | Start VMC initialization | $\begin{aligned} & \text { B640 T125 E125 X105 } \\ & \text { U105 } \end{aligned}$ |
| F001 0501 | Read sense cmd to 72MD | $\begin{aligned} & \text { Y245 DI } 145 \text { A105 Y904 } \\ & \text { E101 } \end{aligned}$ |
| F001 0502 | Orient autoloader cmd | $\begin{aligned} & \text { Y 335 D125 Y420 Y215 } \\ & \text { Y905 } \end{aligned}$ |
| F001 0504 | Select St, ViA diskette | Y535 Y625 Y725 Y415 |
| F001 0506 | Recalibrate cmd to 72MD | D) 170 Y 230 |
| F001 0508 | Reset 72 MD | Y299 |
| F001 050A | Read diskette volume label | $\begin{aligned} & \text { Y935 Y220 H920 Y415 } \\ & \text { Y705 Y605 } \end{aligned}$ |
| F001 05013 | Wrong diskette, ejected diiskette | Y990 Y810 |
| F001 050C | Wrong diskette, eject diskete failure | Y980 Y815 Y505 |
| F001 050E | Read SCA directory | Y960 Y215 Y415 1910 |
| F001 052A | SCA test | B140 C130 A120 D105. E105 |
| F001 052D | IOC test | Al50 B120 C120 D105 E105 |

Machine Check SRC Table

| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see C9 thiu C11 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| F001 | 052E | Power up/eheck status of Feature | A140 13130 C130 |
| F001 | 052F | Save/restore adapter dynamic test | A099 |
| F001 | 0532 | IOC console data storage test | A199 |
| F001 | 0538 | Load SCA Raml | $\begin{aligned} & \text { A050 Y420 Y210 Y710 } \\ & \text { Y610 } \end{aligned}$ |
| F00i | 053C | Load SCA Ram2 | $\begin{aligned} & \text { A050 Y420 Y210 Y710 } \\ & \text { Y } 610 \end{aligned}$ |
| F001 | 0540 | Load keyboard translate table | $\begin{aligned} & \text { A050 Y420 Y210 Y710 } \\ & \text { Y610 } \end{aligned}$ |
| F001 | 0542 | Channel bus test | E135 A125 M110 M210 ZY06 Z206 ZX06 ZWV02 |
| F001 | 0544 | CPU LSSD test | $\begin{aligned} & \text { E185 B105 C105 J103 } \\ & \text { Y902 } \end{aligned}$ |
| F001 | 0548 | CPU CS test | $\begin{aligned} & \text { T170 E120 H105 U103 } \\ & \text { K102 } \\ & \hline \end{aligned}$ |
| F001 | 0549 | CPU CS test-Model 3 CS | $\begin{aligned} & \text { T170 E120 H105 U103 } \\ & \text { K102 } \end{aligned}$ |
| F091 | 054B | Chan Converter card diagnostics | $\begin{aligned} & \text { E125 M125 M225 } \\ & \text { N105 Z205 ZX05 } \\ & \text { ZY05 H105 } \end{aligned}$ |
| F001 | 0550 | Initialize BSM scan rings | $\begin{aligned} & \text { E185 B105 C105 J103 } \\ & \text { Y902 } \end{aligned}$ |
| F001 | 0551 | Load CPU CS - Planar diagnostic | $\begin{aligned} & \text { E180 B105 C105 J105 } \\ & \text { Y203 Y902 } \end{aligned}$ |

Machine Check SRC Table

| $\begin{array}{ll} \text { SRC } & \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see C9 thru C11 Card 3 <br> Result Codes and Pro:jability \% |
| :---: | :---: | :---: |
| F001 0.552 | Planar test | $\begin{aligned} & \text { E160 T130 Y205 A103 } \\ & \text { J10! Y901 } \end{aligned}$ |
| F001 0554 | Load CPU CS - VAT/MS test | E180 B105 C105 J105 Y203 Y902 |
| F001 0556 | VAT/MS test | $\begin{aligned} & \text { B675 E120 H102 X102 } \\ & \text { K101 } \end{aligned}$ |
| F001 0558 | Load CPU CS - VAT/Channel test | $\begin{aligned} & \text { E180 B105 C105 J105 } \\ & \text { Y203 Y902 } \end{aligned}$ |
| F001 055C | Vat'channel test | E135 A125 M110 <br> M210 Z206 ZX.06 ZY06 ZW02 |
| $F 0010560$ | Load main storage | $\begin{aligned} & \text { B635 E130 Y215 T110 } \\ & \text { Y910 } \end{aligned}$ |
| F001 0568 | Start VMC initialization | $\begin{aligned} & \text { B640 T125 E125 X105 } \\ & \text { U105 } \end{aligned}$ |
| F001 0601 | Read sense emd to 72MD | Y245 D145 A105 Y904 E101 |
| F001 0602 | Orient autoloader cmd | $\begin{aligned} & \text { Y335 D125 Y420 Y2.15 } \\ & \text { Y905 } \end{aligned}$ |
| F001 0604 | Select SLVIA diskette | Y535 Y625 Y 25415 |
| F001 0606 | Recalibrate cmd to 72MD | D170 Y230 |
| F001 0608 | Reset 72MD | Y299 |
| F001 060A | Read diskette volume label | $\begin{aligned} & \text { Y935 Y220 H920 Y415 } \\ & \text { Y705 Y605 } \end{aligned}$ |


| $\begin{array}{\|l\|} \mathrm{SRC} \\ 1234 \end{array}$ | 5678 | Definition | For result code definitions, see C9 thril Cll Card 3 <br> Result Codes and Probalility \% |
| :---: | :---: | :---: | :---: |
| F00) | 06013 | Wrong diskette, cjected diskette | Y990 Y810 |
| F001 | 060 C | Wrong diskette, cject diskettc failure | Y980 Y815 Y505 |
| F001 | 060 E | Read SCA dircetory | Y960 Y21.5 Y415 H910 |
| F001 | 062 A | SCA tert | $\begin{aligned} & \text { B140 C130 A120 D105, } \\ & \text { E105 } \end{aligned}$ |
| 1:001 | 062.1) | IOC test | $\begin{aligned} & \text { A150 B120 C120 D) } 105 \\ & \text { E105 } \end{aligned}$ |
| F001 | 062E | Power up check status of Feature | A140 3130 C130 |
| FOOI | 062F | Save restore adapter dynamic test | A699 |
| F001 | 0632 | IOC console data storage test | A199 |
| FG0) | 0638 | Load SCA Raml | $\begin{aligned} & \mathrm{A} 050 \mathrm{Y} 420 \mathrm{Y} 210 \mathrm{Y} 710 \\ & \mathrm{Y} 610 \end{aligned}$ |
| Fl0 1 | 063 C | Load SCA Ram2 | $\begin{aligned} & \mathrm{A} 050 \mathrm{Y} 420 \mathrm{Y} 210 \mathrm{Y} 710 \\ & \mathrm{Y} 610 \end{aligned}$ |
| F001 | 0640 | Loarl keyboard transhate table | $\begin{aligned} & \mathrm{A} 050 \mathrm{Y} 420 \mathrm{Y} 210 \mathrm{Y} 710 \\ & \mathrm{Y} 610 \end{aligned}$ |
| F00i | 0650 | Initialize BS:M scan rings | $\begin{aligned} & \text { E185 B105 C105 J103 } \\ & \text { Y902 } \\ & \hline \end{aligned}$ |
| F001 | 0651 | Loo: CPU CS - Planar diagnostic | $\begin{aligned} & \text { E180 B105 C105 J105 } \\ & \text { Y203 Y902 } \\ & \hline \end{aligned}$ |
| F001 | $065 \%$ | Planar test | $\begin{aligned} & \text { E160 T130 Y205 A103 } \\ & \text { J101 Y901 } \end{aligned}$ |
| F001 | 0654 | Load CPU CS - VAT/MS test | $\begin{aligned} & \text { E180 B105 C105 J1 } 05 \\ & \text { Y203 Y902 } \end{aligned}$ |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see C9 thrn Cll Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| F001 | 0656 | VAT MS test | B675 L120 H102 X 102 K101 |
| F001 | 0658 | Load CPU CS - VAT/Channel test | $\begin{aligned} & \text { E180 B105 C105 J105 } \\ & \text { Y203 Y902 } \end{aligned}$ |
| F001 | 065 C | Vatichannel test | $\begin{aligned} & \text { E } 135 \text { A } 125 \text { M110 } \\ & \text { M1210 Z206 ZX06 } \\ & \text { ZY06 ZW02 } \end{aligned}$ |
| F001 | 0660 | Load main storage | $\begin{aligned} & \text { B635 E130 Y } 21.5 \mathrm{~T} 110 \\ & \mathrm{Y} 910 \end{aligned}$ |
| F001 | 0668 | Starl VAiC initialization | B640 T125E125 X105 U105 |
| F001 | 0815 | 62PC Failure | S199 |
| F00) | 0817 | SCA not able to cause SRC | R199 |
| F001 | 0818 | SCA not able to cause SRC | R199 |
| F001 | 0819 | SCA not able to cause SRC | R199) |
| F001 | 0828 | SCA not able to cause SRC | R199 |
| Fool | 0829 | SCA not able to cause SRC | R199 |
| F001 | 082A | SCA not able to cause SRC | R199 |
| F001 | 082B | SCA not able to cause SRC | R199 |
| F001 | 082C | SCA not able to cause SRC | R199 |
| F001 | 0831 | HMC Machine Check followed by a Hardware Detected Machine Check | $\begin{aligned} & \text { N130 B630 Z630 E104 } \\ & \text { T102 M101 M201 } \\ & \text { Z201 X101 } \end{aligned}$ |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see C9 thril Cll Card 3 <br> Resull Codes and Probahility \% |
| :---: | :---: | :---: | :---: |
| F001 | 0834 | 9332/9335 Device Failure | Q599 |
| F001 | 0835 | 9332/9335 Attachment Failure | $\begin{aligned} & \text { Q140 Q240 ZY08 } \\ & \text { Q305 Q405 ZX02 } \end{aligned}$ |
| F001 | 0836 | 9332,9335 Subsystem Failure | $\begin{aligned} & \text { Q140 Q240 ZY'08 } \\ & \text { Q305 Q405 } / \mathbf{X} 02 \end{aligned}$ |
| F001 | 0837 | 9332,9335 Subsystem startup failure | $\begin{aligned} & \text { Q140 Q240 } Z \mathrm{Y}_{0} \\ & \text { Q305 Q405 W. } 02 \end{aligned}$ |
| F001 | 0838 | 9332,9335 Device startup failure | Q699 |
| F001 | 1901 | Read sense cmd to 72MD | Y245 D)1+5 1105 Y 905 |
| F001 | 1902 | Orient autoloader cmd | $\begin{aligned} & \text { Y } 335 \mathrm{D}) 125 \mathrm{Y} 420 \text { Y } 215 \\ & \text { Y } 905 \end{aligned}$ |
| F001 | 1904 | Select SLVIA diskette | Y535 Y 625 Y 725 Y 41.5 |
| F001 | 1906 | Recalibrate emd to 72MD | D170 Y 230 |
| F001 | 1908 | Reset 72MD | Y299 |
| F001 | 190 A | Read diskette volume label | $\begin{aligned} & \text { Y935 Y220 } 11920 \mathbf{Y}+1.5 \\ & \text { Y705 Y605 } \end{aligned}$ |
| F001 | 190B | Wrong diskette, cjucted diskette | Y990 Y810 |
| F0, 1 | 190C | Wrong diskette, cject diskette failure | Y980 Y815 Y505 |
| F001 | 190 E | Read SCA directory | Y960 Y215 Y415 Y910 |
| F001 | 190F | Change main store dump magazine |  |
| F001 | 1928 | Mode cmd to 62PC attachment | Y190 A105 B105 |
| F001 | 192C | Load SCA directory from 62PC device | Y145S145 A10513105 |

Machine Check SRC Table

| $\begin{array}{ll} \text { SRC } & \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see C9 thru Cll Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: |
| F001 1938 | Load SCA RAM I | Y 145 Z245 A105 $\mathbf{3} 105$ |
| F001 1919 | Dump main store error | Y235 Y935 I3620 E110 |
| F001 1FXX | Machine check | E160 T130 K107 H103 |
| Fo()1 xxxx | If your code camot be found on this list then on-site service is required. |  |

## Machine Check RC Table

| $\begin{aligned} & \text { Result } \\ & \text { Code } \end{aligned}$ | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| A0 | On-site service is required |  |
| A1 | SCA/Consolc IOC Card (01AA1 B2) | 4361306 |
| B1 | SCA/console card 1 (01^AI C2) | 4202410 |
| B6 | One or more main storage cards (01ABI G2, H2, J2, K2, L2, M2, N2, P2) | $\begin{aligned} & 4599188(\operatorname{Mod} 3,4) \\ & 7378022(\operatorname{Mod} 5 \text { Type A) } \\ & 5658718(\operatorname{Mod} 5 \text { Type B) } \\ & 5120271(\operatorname{Mod} 7) \\ & 4772607(\operatorname{Mod} 1-2,6,8-9, \\ & \text { A-B,F) } \\ & \mathbf{2 4 4 4 9 6 2}(\operatorname{Mod} C-D) \\ & \mathbf{6 5 X} 5750(\operatorname{Mod} E) \end{aligned}$ |
| Cl | SCA/consoic card 2 (01AAI D2) | $\begin{aligned} & 4223707 \\ & 4223808 \text { (Katakana) } \end{aligned}$ |
| C4 | One or more IOC cards | 4361306 |
| D1 | Mag mr , clock card (01AAI J2) | 2771064 |
| E1 | Planar (u. $\quad 1$. | $\begin{aligned} & 8328300(\operatorname{Mod} 3,4,5) \\ & 4361076(\operatorname{Mod} 1-2,6-8) \\ & 6370231(\operatorname{Mod} 9, \text { A-F) } \end{aligned}$ |
| F1 | Pwr controller card (01BA1 D2) | 4176537 |
| H1 | Interposer (01APL) | $\begin{array}{\|l\|l\|} 2549343 \text { (power) } \\ 2549344 \text { (signal) } \\ \hline \end{array}$ |
| H9 | 72MD head carriage assembly | 2462650 |
| J1 | $\begin{aligned} & \text { SCA/CPU cablc (0\|AAI Z1 in } 01 \mathrm{AB} \mid \\ & \text { W3) } \end{aligned}$ | 2549851 |


| Result Code | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| K1 | Array board (01AB1) | $\begin{aligned} & 2550894(\operatorname{Mod} 3-5) \\ & 8328320(\operatorname{Mod} 1-2,6-8) \\ & \mathbf{2 4 4 5 8 5 4}(\operatorname{Mod} A-F) \\ & \mathbf{6 2 0 3 3 8 0}(\operatorname{Mod} 9) \end{aligned}$ |
| M1 | Channel converter card (0\|AB| F2) | $\begin{aligned} & 4202240(\operatorname{Mod} 1-8) \\ & 2445900(\operatorname{Mod} 9, A-F) \end{aligned}$ |
| M2 | Channel converter card (0\|AB| E2) | $\begin{aligned} & 4202240(\operatorname{Mod} 1-8) \\ & 2445902(\operatorname{Mod} 9, A-F) \end{aligned}$ |
| NI | Any 1/O adapter on-site service is; reyuired |  |
| Q1 | 9332,9335 1OP | 6370282 |
| Q2 | 933293351 OA | 6370240 |
| Q3 | 9332,9335 IOP cable, see Cl result code at Cl7 on Card 3 |  |
| Q4 | 9332 9335 interface cable, see C2 result code at C17 on Card 3 |  |
| Q5 | 9332 or 9335 device |  |
| Q6 | 9332 or 9335 device is not powered on or enable/disable switch is in disable position during startup |  |
| R1 | 3370 device problem, on-site service is required |  |
| S1 | 62PC device problem, on-site service is required |  |
| T1 | One or more control storage cards (0\|AB1 S2, T2, U2, V2, R2) | $\begin{aligned} & 5122494(\operatorname{Mod} 3) \\ & 6229416(\operatorname{Mod} 1-2,4-9, \text { A-F }) \\ & 8691997(\operatorname{Mod} A-E) \end{aligned}$ |
| U1 | CS terminator card (01AB1 R2, R4) | $\begin{aligned} & 8328248(\operatorname{Mod} 1-2,6-8) \\ & \mathbf{2 5 4 9 0 5 6}(\operatorname{Mod} 3-5,9) \end{aligned}$ |

Cl 2 Card 3

| Result Code | Definition | P/N |
| :---: | :---: | :---: |
| X1 | MS terminator carts (01ABIQ2, Q4) | 5122482 |
| Y 1 | (12PC adapter (01AAI G2) | 2771438 |
| Y2 | 72 MD ) adapter (01AA1 1121 | 4360810 |
| Y 3 | 72M1) cover open swith | 2550696 |
| Y 4 | 72MD control card | $\begin{aligned} & 5563731 \text { (old style) } \\ & 5563735 \text { (new style) } \end{aligned}$ |
| Y5 | 72N0 picker assembly | $\begin{array}{\|l\|} \hline 2462682 \text { (old sisle) } \\ 24626 i 6 \text { (new style) } \\ \hline \end{array}$ |
| Y6 | 72Mi) index LE: 1 a asembly | 2462573 |
| Y7 | 72M1D index PIX assembly | 2462600 |
| Y8 | 72N1D bed drive hele | 246249! |
| Y9 | 72MD on-site service required |  |
| 7.2 | Chamel terminator card |  |
| 7.6 | Sohtwat in HMC or VMC could catuse crror |  |
| ZX | Buard problem |  |
| 2 Y | Channel cable problem |  |
| ZW | Adapter problem |  |

## SCA SRC Table

Use positions 5, 6, 7, 8 of the SRC to find the Result Code.
Use the Result Code and Probability to determine the attion or parts reymired.

| $\begin{array}{ll} \text { SRC } & \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see Cl3 C'ard 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: |
| F002 xxxx | SCA crror | A133 \232 A332 \403 |

## SCA Result Tamle

| Result <br> Corle | Definition | P/rid |
| :---: | :---: | :---: |
| A1 | IOCI (0)AAI ts $)$ | $4361306$ |
| 12 | SCA console card 1 (0) AA1 C? ${ }^{2}$ ) | 4202.410 |
| 13 | SCA consoic card 2 (01AAI P2) | 4223707 or 4223708 |
| 14 | On-ste sentice required |  |

C. 15

## 9332/9335 Attachment SRC Table

Whe pontion 2, 6, 7, $\mathbf{8}$ of the SRC tol lind the Attachment Resul Code.
Wae the Result Code and Probability in the table below to determine the service action on parts requirad.

```
Pmitum 3.4 Actuator Number
    0() (ammot Determine OU
    90 Strings 1&21OP OU
    number
    (%) latactumar OU number
    Cl 2nd actuathor OU number
    C2 - .rd actuator OU number
    C3- Whactuator OU rumber
    C4 - 5th actuater OU number
    C5 - wh actumar OU number
    (%) - 7th actuatur OU number
    07 - sht actuator OU number
    CS 9htactuator OU number
    (9) - 10th actuator OU number
    CA Ilib actuator OU number
    CB - 12thactuator OU number
    CC=13h actuator OU number
    Cl) = 14h actuator OU number
    CE = 15thactuator OU number
    C&- iGth actuator OU number
```

Position $5=$ Model Type
$0=$ Cammot determine
$1=$ Model 100
$2=$ Model 200
$B=$ Model 400
1) $=$ Model 600
$E=M$ rudel 700
$F=$ Model 300

Position $5=$ Model Type
$0=$ Cannot determine
$1=$ Model 100
$2=$ Model 200
$B=$ Model 400
$1)=$ Model 600
$E=$ Mralel 700
$F=$ Model 300
$97=$ Strings 384 CPO OU number
$\mathrm{E} 0=17 \mathrm{th}$ actuator OU number
$E 1=$ isth actuator OU number
$\mathrm{E} 2=19 \mathrm{~h}$ actuator OU number
$\mathrm{E} 3=20$ th actuator OU number
$1: 4=21.5$ actuator OU number
$1: 5=22$ nd actuator OU number
$\mathrm{E}_{6}=23 \mathrm{rd}$ :ctuator $\mathrm{O}(1$ number
$\mathrm{E} 7=241 \mathrm{l}$ actuator OU number
$\mathrm{E} 8=25$ th actuator OU number
$\mathrm{E} 9=26$ th actuator OU number
$\mathrm{F}: \mathrm{A}=27 \mathrm{th}$ actuator OU number
$E B=2$ Sth actuator CU number
$E C=29$ th actuator $O U$ number
$E D=30$ th actuator OU number
$E E=31$ st actuator OU number
$E F=32$ nal actuator $O U$ number

| $\begin{aligned} & \text { Altachment } \\ & \text { SRC } \\ & 12345678 \\ & \hline \end{aligned}$ | Deflumion | For resislt code detintions, sec l: 1 and I) 2 Card 3 <br> Resull Code and I'robahility $0 / 1$ |
| :---: | :---: | :---: |
|  | Invalid unit reference code - run subsystem GMI'. OU number could be () 0 . | On-site service required |
| 7(0)x $\times 999$ | Invalial BSTAT | A197 A201 3101 |
| $71 \times 8.801$ | Channel DMA address crror | A197 A201 3101 |
| $71 \times x \quad 0002$ | Chamnel RAM address error | A197 A20) B101 |
| 71xx $\times 1003$ | Bus error on channel DMA | A197 A201 B101 |
| $71 \times \mathrm{x}$ | Multiple RAM and DMA addres errors | A197 A201 13101 |
| $71 \times 8 \times$ | Invalid sequence error | A197 A20113101 |
| $71 \times 8 \times 006$ | Invalid wakeup sequence | \26.3 \125 M210 13101 |
| $71 \times 8.008$ | Bypassable IOP self test fature | A19713101 M101 |
| $71 \times x \times 009$ | Diagnostic RAM self test lailure | A197 3101 M101 |
| $71 \times x \times 004$ | Invalid 10A channel parm | \260 A119 M210 |
| $71 \times 8 \times 0013$ | DASD sequence error | A160) M239 |
| $71 \times \mathrm{x}$ x00C | Channel disconneet limit exceeded | $\begin{aligned} & \text { A250 A130 } 3110 \text { C306 } \\ & \text { M102 M201 } \end{aligned}$ |
| $71 \times \mathrm{x} \times 101$ | Invalid OU | M189 M210 |
| $71 \times \mathrm{x} \times 102$ | Invalid command | M189 M210 |
| $71 \times \times 103$ | Invalid write memory address or length | M189 M210 |
| $71 \times 8 \times 104$ | Invalid read memory address or length | M189 M210 |
| $71 \times x \quad \times 105$ | Invalid RAM size required field in start-up FOB | M189 M210 |


| Attachament SRC. <br> 12.345678 | Definition | For result come definitions, see D) and I)2 (Card 3 <br> Resnll Code and Probability \% |
| :---: | :---: | :---: |
| $71 \mathrm{xx} \times 110$ | Invalist page offict | M189 M210 |
| $71 \mathrm{xx} \times 187$ | Start device OU not equal to FOB byte : | M189 M210 |
| 71xx $\times 1 / 1$ | Multiple load OU info received | M189 M210 |
| $\begin{array}{llll}71 \mathrm{xx} & \times 1 / 2\end{array}$ | Invalis IOA number | M1189 M210 |
| $71 \mathrm{xx} \times 1 \mathrm{~A} 3$ | Multiple load IOA inforeceived | M189 M210 |
| $71 \mathrm{xx} \times 1 \mathrm{~A} 4$ | Invalid device type | M189 M210 |
| $71 \times \times 1$ 5 | Invalid command 1/O RAR specified | M189 M210 |
| $71 \mathrm{xx} \times 1 \mathbf{4}$ | Invalid OU range | M189 M307 M1203 |
| $72 \mathrm{xx} \times 001$ | IOA reset - op cancelled | A297 A101 B101 |
| $72 \mathrm{xx} \times 002$ | 10P, IOA parity error | A250 A148 B101 |
| $72 \mathrm{xx} \times 003$ | Interface status error | A294 C202 1102 B101 |
| $72 \mathrm{xx} \times 004$ | IOA timeout error | A295 $1103 \mathrm{B101}$ |
| $72 \mathrm{xx} \times 005$ | Slave transfer status error | A290 C204 [104 B101 |
| $72 \mathrm{xx} \times 006$ | IOA general status error | A290 C205 A103 B101 |
| $73 \mathrm{xx} \times 001$ | Interface completion error | I197 A201 C201 |
| $73 \mathrm{xx} \times 002$ | DASD error - controller/device | I190 A209 |
| $73 \mathrm{xx} \mathrm{x003}$ | Device powered off or disabled | 1190 A 209 |
| $73 \mathrm{xx} \mathrm{x004}$ | Device status available | 1197 A201 C201 |
| 79xx x08C | FOB timenut | $\begin{array}{\|l\|l\|} \hline \text { A140 A240 C308 C105 } \\ \text { C202 M202 M102 } \\ \hline \end{array}$ |
| $79 \mathrm{xx} \times 103$ | Post event bus timeout error | A174 A224 B101 |


| $\begin{array}{ll} \mathrm{SRC} & \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see B6 thru B9 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: |
| $49 \mathrm{xx} \times 008$ | Control bus in parity error. | P160 P340 |
| $49 \mathrm{xx} \mathrm{x00A}$ | The wrong device (drive) was sclected. | P740 J525 J220 J 115 |
| $49 \mathrm{xx} \times 00 \mathrm{~B}$ | Busy was missing after seck start operation was issued. | J545 J330 J125 |
| 49xx x00C | A no block found condition occurred. | $\begin{array}{\|llll} \hline \mathbf{J} 235 & \text { J125 P1 } \\ \text { P310 } \end{array}$ |
| $49 \mathrm{xx} \times 00 \mathrm{D}$ | An HDA attention occurred during device (drive) reconnection for a disconnected command chain. | P345 J330 J125 |
| $49 \mathrm{xx} \times 00 \mathrm{E}$ | A pre-selection control interface bus check occurred. | P360 L140 |
| $49 \mathrm{xx} \mathrm{x00F}$ | An unresettable interrupt occurred. | $\begin{aligned} & \mathbf{J} 235 \text { J320 J515 J712 } \\ & \text { P710 } \mathbf{~ J 4 0 8} \end{aligned}$ |
| $49 \mathrm{xx} \times 011$ | Uriexpected busy from device. | $\begin{aligned} & \text { J435 J520 J615 J312 } \\ & \mathbf{J} 210 \mathrm{~J} 708 \end{aligned}$ |
| $4{ }^{6} \times \times 11 \mathrm{~F}$ | A microcontroller non-recoverable, check occurred. | $\begin{aligned} & \text { P535 P220 P115 P312 } \\ & \text { J110 P808 } \end{aligned}$ |
| 49xa $\times 200$ | Possible failure of CTLI Line S. | P399 |
| $49 \mathrm{xx} \times 201$ | A prom storage check occurred. | P260 P540 |
| $49 \mathrm{xx} \times 202$ | Possible failure of CTLI Line S. | P399 |
| $49 \mathrm{xx} \times 203$ | Possible failure of CTLI Line S. | P309 |
| $49 \mathrm{xx} \times 204$ | A microcontroller check 2 occurred. | P545 P230 P125 |
| $49 \mathrm{xx} \times 205$ | A microcontroller check 2 occurred. | P545 P230 P125 |
| 49xx $\times 206$ | Possible failure of CTLI Line S. | P399 |


| $\begin{array}{\|ll\|} \hline \text { SRC } \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see B6 thru B9 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: |
| $49 \mathrm{xx} \times 207$ | Possible faiiure of CTLI Line S. | P399 |
| $49 \mathrm{xx} \times 208$ | A microcontroller check I occurred. | $\begin{aligned} & \text { P535 P220 P115 P312 } \\ & \text { J110 P808 } \end{aligned}$ |
| $49 \mathrm{xx} \times 209$ | A microcontroller check 1 occurred. | $\begin{aligned} & \text { P535 P220 P115 P312 } \\ & \mathbf{J 1 1 0} \text { P808 } \end{aligned}$ |
| $49 \mathrm{xx} \times 20 \mathrm{~A}$ | Possible failure of CTLI lines. | P399 |
| $49 \mathrm{xx} \times 20 \mathrm{~B}$ | Possible failure of CTLI lines. | P399 |
| 19xx $\times 20 \mathrm{C}$ | A microcontroller check 2 occurred. | $\begin{aligned} & \text { P535 P220 P115 P312 } \\ & \mathbf{J 1 1 0 ~ P 8}_{108} \end{aligned}$ |
| $49 \mathrm{xx} \times 20 \mathrm{D}$ | A microcontroller check 2 occurred. | $\begin{aligned} & \text { P535 P220 P115 P312 } \\ & \text { J110 P808 } \end{aligned}$ |
| $\begin{aligned} & 49 \mathrm{x}: \times 20 \mathrm{E} \\ & \text { thru } \\ & \times 2 \mathrm{FF} \\ & \hline \end{aligned}$ | Possible falure of CTLI lines. | P399 |
| $\begin{aligned} & 40 \mathrm{vx} \times 300 \\ & \text { thru } \\ & \times 3 \mathrm{FF} \end{aligned}$ | A CTLI parity check occurred. | P399 |
| $\begin{array}{\|cc\|} \hline 49 \mathrm{xx} & \mathrm{x} 401 \\ & \text { thru } \\ & \times 407 \end{array}$ | Device interface cable check (MD bus in parity check) occurred. | P160 P540 |
| $\begin{array}{\|cc\|} \hline 49 \times x & \times 408 \\ & \text { thru } \\ & \times 40 \mathrm{~F} \\ \hline \end{array}$ | A device bus in parity check oecurred. | J360 J140 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see B6 thru B9 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 49 xx | $\times 502$ | A transfer check (program error) occurred or interface A. | P345: $\mathbf{1 3 0}^{3} \mathbf{P 5} 25$ |
| 49 xx | $\times 506$ | A transfer check (program error) occurred on interface B. | P145 P530 P325 |
| $49 \mathrm{xx}$ | $\times 600$ thru $\times 6 \mathrm{FF}$ | A parity check occurred. | P140 P525 J120 P315 |
| $49 \mathrm{xx}$ | $\begin{aligned} & x 740 \\ & \text { thru } \\ & \times 77 F \end{aligned}$ | A counter data parity check occurred. | J140 P125 J420 P715 |
| $49 \times x$ | $\begin{aligned} & \times 700 \\ & \text { thru } \\ & \times 7 F E \end{aligned}$ | A SERDES crror or data check occurred. | $\begin{aligned} & \text { P135 P825 P517 J113 } \\ & \text { P310 } \end{aligned}$ |
| $49 \mathrm{xx}$ | $800$ <br> thru $\times 8 \mathrm{FF}$ | A device selection error occurred. | P745 J130 J325 |
| 49 xx | x901 | A branch to an unused storage location counter. | P560 P240 |
| 49 xx | x902 | Micro-controller detected error occurred. | P745 J330 J125 |
| 49 xx | $\times 910$ | An exccute instruction was returned to the program counter +1 on trap 2 . | P560 P240 |
| 49 xx | x911 | Trap 6 was entered, but no errors were found. | $\begin{aligned} & \text { P535 P220 P115 P812 } \\ & \text { J110 P308 } \\ & \hline \end{aligned}$ |
| 49 xx | x912 | An execute instruction returned to program control +1 on trap 3 . | P560 P240 |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see B6 thru B9 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 49 xx | x913 | An execute instruction returned to program control +1 on trap 4. | P560 P240 |
| 49 xx | $\times 914$ | An unexpected counter trap eccurred. | J140 P125 P820 P515 |
| 49 xx | $\times 916$ | Tag exccution took too long, microcode hang cendition occurred. | P560 P240 |
| 49 xx | x918 | Sync out timing error occurred while not in data transfer mode. | P145 P330 P8:5 |
| 49xx | x919 | An execute instruction was returned to the program | P560 P240 |
| 49 xx | $\times 920$ | An execute instruction failed on select device tag processing. | P560 P240 |
| 49 xx | $\times 921$ | The register immediate command failed in the ECC routine. | P545 P230 P825 |
| 49 xx | $\times 924$ | Device response was active with all the select lines not active. | P745 J330 J 125 |
| 49xx | x928 | Two select device tags occurred with no de-selection. | P345 P530 L125 |
| 49 xx | $\times 932$ | The device did not return tag valid to set read/write tag. | P240 J225 J620 J115 |
| 49 xx | $\times 933$ | Tag valid dropped during a read/write operation. | P645 P730 J325 |
| 49xx | $\times 934$ | The device counter failed during exccution of transmit ID tag. | J140 P125 P820 P515 |
| 49xx | $\times 936$ | Transmit ID timed out while waiting for sync out. | P345 P130 J 125 |
| 49 xx | $\times 937$ | Did not get sync out timing error for second sync in or out pair during calculation of sync in lead time. | P145 P330 J125 |
| 49 xx | $\times 938$ | A second sync out was returned during either bit ring 0 or 7 . | P145 P330 J125 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see B6 thru B9 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 49 xx | $\times 942$ | A read tag was received, but it was not a read ID or a read ID buffer. | P345 P530 L125 |
| 49 xx | $\times 946$ | The data check indicator was not reset after executing an ECC preset. | P840 J125 P120 P315 |
| 49 mx | $\times 947$ | The data check indicator was not set after an ECC read transfer. | P840 J125 P120 P315 |
| 49 xx | x94A | The tag gate did not drop soon enough to allow the data field transfer to begin. | P399 |
| 49 xx | $\times 950$ | The first read or write tag was not preceded by transmit ID or by clock ID. | P345 P530 L125 |
| 49 xx | $\times 952$ | The next read or write tag was not proceded by transmit ID or by clock 1D. | P345 P530 L125 |
| 49 xx | $\times 9513$ | Sync in selection was not calculated for write data. | P145 P330 P825 |
| 49 xx | x95C | Sync in lead time was not calculated for write data. | P145 P330 P825 |
| 49 xx | x951) | Sync in lead time was not calculated for oriented transmit 1D. | P145 P330 P825 |
| 49 xx | $\times 962$ | The ECC signaled data checks, but all nine bytes were zero. | P840 J125 P120 P315 |
| 49 xx | $\times 972$ | A write ID was entered before executing prerequisite functions. | P345 P530 L125 |
| 49 xx | $\times 974$ | A write ID was entered before exccuting a valid verify 1D. | P345 P530 L125 |
| 49 xx | $\times 975$ | A tag overrun occurred on write ID operation. | P345 P530 L125 |
| 49 xx | $\times 980$ | A set level command did not change the program counter. | J14; P530 P225 |


| $\begin{aligned} & \text { SRC } \\ & 1234 \quad 5678 \end{aligned}$ | Definition | For result code definitions, see B6 thru B9 Card 3 <br> Result Codes aliad Probability \% |
| :---: | :---: | :---: |
| $49 \mathrm{xx} \times 982$ | No sync out latched were set after a sync out was detected in a transmit ID operation. | P145 P320 P825 |
| $49 \mathrm{xx} \times 983$ | Hardware model sense bits do not agree. | Y 1 |
| $49 \mathrm{y} \mathrm{X} \times 984$ | Data integrity interlock indicates down level storage control diskette. | Z3 |
| $49 \mathrm{xx} \times 998$ | Operation crror. | 7.499 |
| $49 \mathrm{xx} \times 999$ | Operation program ertor. | 2.49\% |
| $49 \mathrm{xx} \times 9 \mathrm{~F} 4$ | The ECC indicated a miscompare, but the displacement was zero. | P840 J 125 P120 P315 |
| $49 \mathrm{xx} \times 9 \mathrm{~F} 5$ | An ID compare on a miove ID operation was detected. | P340 P125 P520 L115 |
| $49 \mathrm{xx} \times 9 \mathrm{~F} 6$ | invalid code. | P140 P525 P330 P815 |
| $49 \mathrm{xx} \times 9 \mathrm{~F} 7$ | Invalid code. | P140 P525 P30 P815 |
| $49 \mathrm{xx} \times 9 \mathrm{~F} 8$ | Invalid code. | L145 P330 P525 |
| $49 \mathrm{xx} \times 9 \mathrm{FE}$ | Invalid code. | J240 P225 J620 J315 |
| $49 \mathrm{xx} \times \mathrm{A} 00$ | Any other controller check. | P340 J225 J420 J51) |
| $49 \mathrm{xx} \times \mathrm{x} 01$ | Any other controller check. | P340J225 J420 J515 |
| $49 \mathrm{xx} \times \mathrm{A} 02$ | Any other controller check. | P340 J225 J420 J515 |
| $49 \mathrm{xx} \times$ - 03 | Any other controller check. | P340.J225 J420 J515 |
| $49 \mathrm{xx} \times \mathrm{x} 04$ | A CTLI register check has occurred. | P560 P340 |
| $49 \mathrm{xx} \times 105$ | A control interface register check has occurred. | $\begin{aligned} & \text { P335 P125 J117 P313 } \\ & \text { P510 } \end{aligned}$ |


| $\begin{array}{ll} \text { SRC } & \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see B6 thru B9 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: |
| $\begin{array}{\|ll} 49 \mathrm{xx} & \mathrm{xA06} \\ & \text { thru } \\ & \text { xAFF } \end{array}$ | Any other controller check. | P340 J225 J420 J515 |
| $\begin{array}{\|ll\|} \hline 49 \mathrm{xx} & \mathrm{xF} 00 \\ & \text { thru } \\ & \mathrm{xFFF} \end{array}$ | A false controller error occurred. | $\begin{array}{\|l} \text { P335 P125 J117 P813 } \\ \text { P510 } \end{array}$ |
| $4 \mathrm{Axx} \times 000$ | Cannot determine error bul OU is displayed. | F399 |
| $4 \mathrm{Axx} \times 001$ | Controller select failed. | L130 K220 P515 P112 <br> P310 J108 P805 |
| $4 \mathrm{Axx} \times 003$ | Immediate commands failed. | L160 C230 A310 |
| 4 Axx x 004 | Reset failed. | L199 |
| $4 \mathrm{Axx} \times 005$ | Cannot complete start clocks. | L150 A140 C210 |
| $4 \mathrm{Axx} \times 009$ | 3370 model type does not match MCR. | F399 |
| $4 \mathrm{Axx} \mathrm{x00B}$ | Cannot load LSSD pattern. | L145 A130 C225 |
| $4 \mathrm{Axx} \times 00 \mathrm{C}$ | Write/read compare error. | L199 |
| F000 3EC0 | 3370 R/W switch appears to be in read only mode (string 1). | F399 |
| F000 3EC1 | 3370 tag timeout (strin: 1). 3370 appears powered off. | F399 |
| F000 3EL0 | 3370 R/W switch appears to be in read only mode (string 2). | F399 |
| F000 3ED1 | 3370 tag timeout ( s ! $\cdot \mathrm{rin}_{6} 2$ ). 3370 appears powered off. | F399 |
| F000 xxxx | If your code cannot be found on this list, then on-site service is required. |  |

3370 RC Table

## 3370 RC Table

| Result Code | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| A1 | SCA/console IOC card (01AA1 B2) | 4361306 |
| A3 | SCA/console card 2 (01AAI D2) | $\begin{aligned} & 4223707 \\ & 4223708 \text { (Katakana) } \end{aligned}$ |
| 131 | Planar (01APL) | $\begin{aligned} & 8328300(\operatorname{Mod} 3-5) \\ & 4361076(\operatorname{Mod} 1-2,6-8) \\ & 6370231(\operatorname{Mod} 9, A-F) \end{aligned}$ |
| 132 | One or more control store cards (01AB1 S2, $\mathrm{T} 2, \mathrm{U} 2, \mathrm{~V} 2, \mathrm{R} 2$ ) | $5122494(\operatorname{Mod} 3)$ $6229416(\operatorname{Mod} 1-2,4-9$, A-D,F) $8691997(\operatorname{Mod} A-D)$ |
| 136 | Oine or more mainstore cards (01AB! G2, H2, J2, K2, L2, M2, N2, P2 | ```4599188 (Mod 3-4) 7378022 (Mod 5 Type A) 5658718 (Mod 5 Type B) 5120271(Mod 7) 4772607 (Mod 1-2, 6, 8-9, A-B, F) 2444962 (Mod C-D) 65X5750(Mod E)``` |
| C2 | Clock cord (System/38) <br> (01AA1 . 22 for OU C0-C7) <br> (01AA1 N2 for OU C8-CF) | $\begin{aligned} & 2771064 \\ & 2550550 \text { old style } \\ & 2771064 \end{aligned}$ |
| F2 | VMC microcode |  |
| F3 | Cannot determine FRUs. | On-site service required. |
| J1 | FCI card 3370 K 2 card in controller gate $01 \mathrm{AA} \mid$ | Variable |


| $\begin{aligned} & \text { Result } \\ & \text { Code } \end{aligned}$ | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| J2 | HARCAR 3370 C2 card in drive gate OXCAI (note 1) | Variable |
| J3 | R,W CTL 3370 132 card in drive gate OXCAI (note 1) | Variable |
| J4 | Difl 3370 D2 card in drive gate OXCAI (note 1) | Variable |
| J5 | Access 3370 E2 card in drive gate OXCAI (note 1) | Variable |
| J6 | Servo clock 3370 H 4 card in drive gate OXCA: (note 1) | Variable |
| J7 | Servo analog 3370 card in drive gate OXCAI (note 2) | V sable |
| J8 | CE mode seq card 3370 phre sey card in OX frame (note 1) | Variable |
| J9 | Power sense (drive gate eXCAI) (note 1, H 2 ) | Variable |
| K2 | 3370 power is off |  |
| LI | System 38 adapter <br> (01AAI K2 for OU C0-C7) <br> (0IAAI M2 for OU C8-CF) | $\begin{aligned} & 2444872 \\ & 2444872 \end{aligned}$ |
| P1 | SERDES 3370 H 2 card in controller gate OIAAI | Variable |
| P2 | Sector 3370 F2 card in drive gate 0XCA1 (note 1) | Variable |
| P3 | CTE1 3370 E2 card in controtler gate 01AAI | Variable |
| P5 | Process 3370 G2 card in controller gate 01AAI | Variable |
| P6 | Read 3370 K 4 card in drive gate 0XCAl (note 1) | Variable |
| P7 | Sclect 3370 K 2 card in arive gate 0XCAI (note 1) | Variable |


| Resuit <br> Code | Definition | $\mathbf{P / N}$ |
| :--- | :--- | :--- |
| P8 | ECC 3370 J2 card in controller gate OIAAI | Variable |
| P9 | Power amplifier P160 (3370-note 1) |  |
| $\mathbf{Y 1}$ | Solid fault condition in 3370 controller or <br> drive logic |  |
| $\mathbf{Y 2}$ | Solid fault condition in 3370 drive logic <br> (0XICAI) or HDA |  |
| $\mathbf{Z 2}$ | String switch feature in: alid code for this <br> system |  |
| $\mathbf{Z 3}$ | Program error |  |
| $\mathbf{Z 4}$ | 3370 adapter or in 3370 controller logic <br> problem. An I/O adapter could be holding a <br> channel a bit active. |  |

Notes:

1. The tocation of the 3370 drive boards depend on the OU number associuted with the FSC.

| OU\# | Frame/Cate/Board (String 1) | OU\# | Frame/Gate/board (String 2) |
| :---: | :---: | :---: | :---: |
| C0 | 01 CA : | C8 | $01 C A 1$ |
| Cl | ()1CAI | C9 | OLCAI |
| C2 | 02CAI | CA | 02 CAI |
| C3 | 02 CAl | CB | 02CAI |
| C 4 | 03 CAI | CC | 03 CAI |
| C5 | 03 CAI | CD | 03 CAI |
| C6 | 0 OCAI | CE | 04 CAI |
| C7 | 04 CAI | CF | 04 CAI |

2. The location of the hourdicard location depend on the OUFF.

| OU\# | Location (String 1) | OU\# | Location (String 2) |
| :---: | :---: | :---: | :---: |
| C0 | 01 CAI 12 | C8 | 01CAI-J2 |
| C1 | 01CAI-G2 | C9 | $01 \mathrm{CAI}-\mathrm{G} 2$ |
| C2 | 02CA1-J2 | CA | $02 \mathrm{CAI}-\mathrm{J} 2$ |
| C3 | 02CA1-G2 | CB | 02 CAl -G2 |
| C4 | (13CAI-J2 | CC | $03 \mathrm{CAI}-32$ |
| C5 | 03CAI-G2 | CD | $03 \mathrm{CAI}-\mathrm{G} 2$ |
| C6 | $04 \mathrm{CAI}-\mathrm{J} 2$ | CE | $04 \mathrm{CAI}-\mathrm{J} 2$ |
| C7 | ()4CAI-G2 | CF | 04 CAl -G2 |

## 1/O Power SRC T'able

## I/O Power SRC Table

Une pasition 6,7,8 of the SRC to find the Result Conde.
Wee the Renult Code and Probability to determine the action or parts reyuired.

## Pasition 5 - Mosel Type

```
0 Cannot determine
! Nodel for)
2 Model 200
3 Model 3 A = Model 20
4 Model 4 B = Model 400
5 Monicl 5 }\quadC=\mathrm{ Mode! 40
6= Moncel 6, D = Model 600
7= Nodel 7 E = Model 700
S=Model}8\quadF=\mathrm{ Model 300
9 = Nowel 18
```

| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For resull code definitions, sec B1I Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 60000 | X012 | 3262,5211 No. 2 power failure. | K760 A320 A220 |
| 6000 | $\times 013$ | 3262/5211 No. 1 power failure. | K860 A320 A220 |
| 6000 | X015 | 5424 power failure. | K960 A320 A 220 |
| 6000 | x018 | 3410 power failure. | L260 A320 A 2.20 |
| 6000 | $\times 015$ | SCA/Device interface bus failing. | F360, A320, A220 |
| 60000 | X040 | System power down failed due to failure in SCA to power controller interface. | K160 K225 A310 A205 |
| 6000 | $\mathrm{X0FF}$ | No power error detected by SCA. | Z199 |

## I/O Power RC Table

| Result <br> Code | Definition | P/N |
| :--- | :--- | :--- |
| A2 | SCA console card I (01AA1 C2) | $\mathbf{4 2 0 2 4 1 0}$ |
| A3 | SCA console card 2 (01AAI D2) | 4223707 <br> 4223708 (Katakana) |
| F3 | Any 1/O adapter |  |
| K1 | Power controller card at 01 BA1 D2 | 4176537 |
| K2 | Power sense/store card at 01BA1 C2 | 4176447 |
| K7 | $3262 / 5211$ No. 2 at 01AB2 M2 | 4202259 |
| K8 | $3262 / 5211$ No. I at 01AA1 P2 | 2771576 |
| K9 | 5424 adapter card at 01AB2 D2 | 8328070 |
| L2 | 3410 adapter card at 01AB2 N2 | 4354098 |
| Z1 | On-site service is required |  |

Console Status Indicators SRC Table

## Console Status Indicators SRC Table

Use positions 4 of the SRC to find tife proper microfiche card and frame.

| $\begin{array}{ll} \text { SRC } & \\ 12.34 & 56,78 \end{array}$ | Row/Frame/Card | Condition |
| :---: | :---: | :---: |
| Foo0) 3Exx | A7 Card $3^{1}$ | 3370 |
| Fo00 $\mathrm{xxxx}^{\text {cos }}$ | On-site service is required. | CPU Halt |
| Fo0) 1 xxxx | B13 Card 31 | Machine Check |
| F号2 xxax | C13 Card 3 | SCA |
| FCOJ xuxx | B13/Cl3 Card 31 | SCA/Machine Check <br> Note: F003 <br> (SCA/machine check) is hamiled as a F001 (machine check) |
| F004 ${ }^{\text {xxx }}$ | On-site service is required. | Thermal |
| F008 $\mathrm{xxxx}^{\text {c }}$ | On-site service is required. | Power |
| F00C $\mathrm{xxxx}^{\text {F }}$ | On-site service is required. | Power/Thermal |
| - If your code cannot be found on this list then on-site service is required |  |  |

BI 4 Card 3

## Machine Check SRC Table

Lise position 5,6,7,8 of the $\operatorname{SRC}$ to find the Result Code.
Use ille Result Cole and Probability to determine the action or parts required.
Position 6 - MPL Made

```
2 = IMPL
3 = Auto IMP!
4 = IMPL Abbr
5 = Alternate |!!L
6= Alternatc !A!PL Abbr
```

| $\begin{array}{ll} \text { SRC } & \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, sec C9 thru C11 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: |
| F001 0000 | IOC data store test (SCA portion) | /16013120 C120 |
| F001 0100 | IOC test | A150 B120 C120 E105 D104 A00! |
| F001 0104 | SCA test | $\begin{aligned} & \text { F140 C130 A120 D105 } \\ & \text { E105 } \end{aligned}$ |
| F001 0108 | LED test | B150 C125F125 |
| F001 010C | Power controller test | B150C125 F125 |
| F001 0110 | SCA/CPU interface test | $\begin{aligned} & \text { E150 C140 A108 H101 } \\ & \text { J101 } \end{aligned}$ |
| F001 0114 | Magnetic media test ( 72 MD ) | D150 Y 230 B 110 A 105 Cl05 |
| F001 0118 | Magnetic media test (62PC) | Y170 D110 A110 B105 C105 |

Machine Check SRC Table

| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Detinition | For result code definitions, see C9 thrı Cll Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 1:001 | 011 C | 10 bus test | $\begin{aligned} & \text { A130 E130 N130 C105 } \\ & \text { M105 } \end{aligned}$ |
| F001 | 0210 | Read sense cond to 62PC | $\begin{aligned} & \text { A125 B125 Y125 S115 } \\ & \text { D110 } \end{aligned}$ |
| F001 | 0214 | Recalibrate emd to 62PC Device | Y145 S145 A105 B105 |
| Fo01 | 0218 | Index test 1062 PC Device | Y145 S145 A105 B105 |
| Fool | 021A | Renet and to 62PC Device | $\begin{aligned} & \text { Y140 S } 1: 5 \text { A } 107 \text { B107 } \\ & \text { E102 D102 C402 ZY02 } \\ & \text { M102 K101 } \end{aligned}$ |
| F001 | 0220 | $\begin{aligned} & \text { Rd verify cmil to } 62 \mathrm{PC} \text { Device (TRF } \\ & 359 \text { ) } \end{aligned}$ | Y145 S145 A105 B105 |
| F001 | 0228 | Mode cmd to 62PC atachment | Y190 A105 E105 |
| Fo0) | 022A | SCA test | $\begin{aligned} & \text { B140 C130 A120 D105, } \\ & \text { E105 } \end{aligned}$ |
| F001 | 022C | Loal SCA directory from 62PC Device | $\begin{aligned} & \text { Y145 S140 A105 B105 } \\ & \text { C105 } \end{aligned}$ |
| F001 | 0221) | IOC test | $\begin{aligned} & \text { A150 B120 C120 D105 } \\ & \text { E105 } \end{aligned}$ |
| F001 | 02.2E | Power up/cheek status of Feature | A140 B130 C130 |
| F001 | 022F | Save/restore adapter dynamic test | A099 |
| F001 | 0232 | IOC console data storage test | A199 |
| F001 | 0238 | Load SCA RAMI | Y145 S145 A105 B105 |
| F0n1 | 023C | Load SCA RAM2 | $\begin{aligned} & \text { Y145 S145 A105 B103 } \\ & \text { E102 } \end{aligned}$ |

Wachine Check SRC Table

| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Dectunition | For result code definitions, see (') lhmu Cll (and 3 <br> Resull Codes and "'robability "\% |
| :---: | :---: | :---: | :---: |
| F001 | 0240 | L.oad keybard translate table | Y145S145 \105 B105 |
| F00) | 0242 | Chamel bus test |  |
| Fowi | 0244 | CFU L.SSD test | $\begin{aligned} & \mathrm{E} 185 \mathrm{B1} 105 \mathrm{Cl} 105 \mathrm{Jl} 03 \\ & \mathrm{SI} 102 \end{aligned}$ |
| F(0)I | 0248 | CPU CS tent - Models | $\begin{aligned} & \text { T170 E120 I I } 05 \mathrm{U} \text { U103 } \\ & \mathrm{KI} 102 \end{aligned}$ |
| F001 | 0249 | CPUCS | $\begin{aligned} & \mathbf{T 1 7 0 )} \mathrm{E} 120 \mathrm{IH} 105 \mathrm{U} 103 \\ & \mathrm{~K} 102 \end{aligned}$ |
| F001 | 02413 | Chan Conserter card diagnontio | $\begin{aligned} & \mathrm{E} 12.5 \mathrm{M} 112 \mathrm{M} 122.5 \\ & \mathrm{~N} 10.5 \mathrm{Z20.5} \mathrm{ZX} 0.5 \\ & \mathrm{ZY} 0.5 \mathrm{II} 10.5 \end{aligned}$ |
| F00) | (0250 | Initialize BSM scan rings | $\begin{aligned} & \mathrm{E} 185 \mathrm{Bl} 105 \mathrm{C} 105 \mathrm{Jl} 03 \\ & \mathrm{~S} 102 \end{aligned}$ |
| F001 | 0251 | Luad CPU CS - planar diagnostic | $\begin{aligned} & \mathrm{K} 180 \mathrm{~B} 105 \mathrm{C} 105 \mathrm{~J} 104 \\ & \mathrm{~S} 10+\mathrm{Y} 102 \end{aligned}$ |
| F001 | 0252 | Planar test | $\begin{aligned} & \text { E155 T130 U105 A103 } \\ & \text { J103 Y!02 S102 } \end{aligned}$ |
| F001 | 0254 | Load CPU CS - VAT/MS test | $\begin{aligned} & \text { E180 B10: C105 J103 } \\ & \text { S103 Y102 } \end{aligned}$ |
| F00) | 0256 | VAT, MS test | $\begin{aligned} & \text { B675 E120 H102 X102 } \\ & \text { K101 } \\ & \hline \end{aligned}$ |
| F001 | 0258 | Load CPU CS - VAT/channel test | $\begin{aligned} & \text { E1s0 13105 C105 J105 } \\ & \text { S103 Y102 } \end{aligned}$ |


| $\begin{aligned} & \mathrm{sRC} \\ & 12.34 \end{aligned}$ | 5678 | Delinition | For result inde dedishlions. see (9) lliru C'l) (ard 3 <br> Revalt Codes and Prohatility " |
| :---: | :---: | :---: | :---: |
| 1.00) | 125C | Vat channel text | $\begin{aligned} & \mathrm{E} 135 \text { Л125 M1110 } \\ & \mathrm{M} 210 \mathrm{Z} 206 \mathrm{ZX}(0) \\ & \mathrm{ZY} \mathrm{Y}_{0} \mathrm{ZW} \mathrm{~V}_{02} \end{aligned}$ |
| 1 (10)1 | 0260 | L.asd main storage | $\begin{aligned} & \text { 13635 E130 Y'11.5 } \mathrm{T} 110 \\ & \mathrm{~S} 110 \end{aligned}$ |
| F00) | 0268 | Stant VNI itialiation | $\begin{aligned} & \begin{array}{l} 136+0 \text { T125:125 X10.5 } \\ \text { U105 } \end{array} \end{aligned}$ |
| Fool | 0310 | Read neme cond to 62PC | $\begin{aligned} & \text { A125 B125 Y } 125 \text { S } 115 \\ & \mathrm{D} 110 \end{aligned}$ |
| $1 \cdot 001$ | 0314 | Recallibate emd th 62PC Device | Y14.5SIts \105 13105 |
| 1/00) | 0318 | Inder tent to 62PC Device | Y14.5S145 A105 3105 |
| Fo() | $031 /$ | Resel and to 6, PC Device | $\begin{aligned} & \mathrm{Y} 140 \mathrm{~S} 135 \text { A107 } 13107 \\ & \mathrm{E} 102 \mathrm{D} 102 \mathrm{Cf}(1) \mathrm{ZN}(02 \\ & \text { M102 K101 } \end{aligned}$ |
| F.00) | 0320 | Rd verily emd to 62PC Device (TRK 359) | Y145S145 \105 B105 |
| Fotel | 0328 | Mode emd to 62PC attachment | Y190 A105 3105 |
| FOO1 | 032A | SCA test | $\begin{aligned} & \mathrm{B} 140 \mathrm{C} 130 \mathrm{Al} 20 \mathrm{D}) 105 \\ & \mathrm{E} 105 \end{aligned}$ |
| F001 | 032C | Loal SCA directory from 62PC Device | $\begin{aligned} & \text { Y145S140A105 B105 } \\ & \text { C105 } \end{aligned}$ |
| F001 | 0321) | IOC test | $\begin{aligned} & \text { A150 B120 C120 D105 } \\ & \text { E105 } \end{aligned}$ |
| F001 | 032E | Power upicheck status of Feature | A140 B130 C130 |


| Result <br> Code | Definition | P/N |
| :--- | :--- | :--- |
| $\mathbf{Z Y}$ | Channel Cable |  |
| $Z Z$. | Channel cable on second channcl. See <br> MAP Ref 019-070 through 019-090 for <br> locations by configuration. |  |

## 62PC SRC Table

## 62PC SRC Table

Use position 5, 6, 7, $\mathbf{8}$ cf the SRC to find the Result Code.
Use the Result Code and Probability to determine the action or parts required.
Position $2=$ Adapter Number
$0=$ Cannot determine failing 62PC number
$1=62 \mathrm{PC}-1$ (Adapter 1 in slot 01AA1 G2)
$2=62 \mathrm{PC}-2$ (Adapter 1 in slot 01AA1 G2)
$3=62 \mathrm{PC}-3$ (Adapter 1 in slot 01AA1 G2)
$4=62 \mathrm{PC}-4$ (Adapter 1 in slot 01AA1 G2)
$5=62 \mathrm{PC}-5$ (Adapter 2 in slot 01AA1 F2)
$6=62 \mathrm{PC}-6$ (Adapter 2 in slot 01AAI F2)

Position 3, 4 = Device OU Number
11 = 1st device OU number
$51=2$ nd device OU number
$91=3$ rd device OU number
$\mathrm{DI}=4 \mathrm{th}$ device OU number
$10=5$ th device OU number
$50=6$ th device OU number

## Position $5=$ Model Type

$0=$ Cannot determine
$1=$ Model 100
$2=$ Model 200
$3=$ Model 3
$4=$ Model 4
$5=$ Model 5
$6=$ Model 6
$7=$ Model 7
8 = Model 8
$9=$ Model 18
A $=$ Model 20
$B=$ Model 400
$\mathrm{C}=$ Model 40
$\mathrm{D}=$ Model 600
$\mathrm{E}=$ Model 700
$\mathrm{F}=$ Model 300

| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see A5 and A6 Carl 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 3xxx | . 004 | FOB time out error | C180 Dílo D310 |
| 3 xxx | x 006 | Program error | C580 C120 |
| 3 xXX | x 007 | Channel oterrun error | 7.899 |
| 3 xxx | x008 | Device error | III60 CI40 |
| 3 XXX | 800 A | Device error | 11299 |
| 3 xrx | . 000 C | Device error | 11199 |
| 3 xxx | $\times 010$ | Adapter error | $\begin{aligned} & \mathrm{C} 175 \mathrm{C} 2,10 \text { ZV } 04 \mathrm{D} 105 \\ & \text { B105 Z701 } \end{aligned}$ |
| 3 xxx | x012 | Device error | H438 H536, 16610 H209 H1107 |
| 3 xxx | $x 1014$ | Device crror | $\begin{aligned} & 1143+1122911121 \\ & 11516 \end{aligned}$ |
| 3 xxx | $x 01 \mathrm{C}$ | Device error | H159 H24! |
| 3 xxx | $x 01 \mathrm{E}$ | Device error | H1501H23, C115 |
| 3 xxx | . 022 | Device error | 1147111229 |
| 3 xxx | x028 | Device error | H449 H130 H221 |
| 3 xxx | . 022 C | Device crior | H445 H138 $\mathbf{1 2 1 7}$ |
| 3 xxx | $\times 034$ | Device error | 11181 11219 |
| 3 xxx | $\times 036$ | Device error | H174 H226 |
| 3 xxx | $\times 038$ | Device error | H184 H216 |
| 3 xxX | $\times 03 \mathrm{E}$ | Device error | H 757 H 619 H 409 <br> H 206 H 104 H 802 <br> H 502 H 902 H 301 |

$62 \mathrm{P} C$ SRC Table

| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see A5 and A6 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 3 xxx | x040 | Device error | H499 |
| 3 xxx | x042 | Device error | H:454 H241 11505 |
| 3 xxx | x 044 | Device error | H480 H812 H208 |
| 3 xxx | $\mathrm{v}^{0} 48$ | Device error | H427 H823 |
| 3 xxx | $\times 050$ | Device earor | $\begin{aligned} & \mathrm{H} 445 \mathrm{H} 231 \mathrm{H} 507 \\ & \mathrm{HH} 06 \mathrm{H} 805 \mathrm{H} 605 \\ & \mathrm{CH} 01 \end{aligned}$ |
| 3 xyx | $\times 052$ | Desice error | $\begin{aligned} & \mathrm{H} 550 \mathrm{H} 220 \mathrm{H} 110 \\ & \mathrm{H} 410 \mathrm{H} 910 \end{aligned}$ |
| $3 \mathrm{3xxa}$ | .054 | Device error | H253 H137 H410 |
| 3 xax | $\times 056$ | Device error | $\begin{aligned} & \mathrm{H} 663 \mathrm{H} 427 \mathrm{H} 108 \\ & \mathrm{H} 302 \\ & \hline \end{aligned}$ |
| 3 xax | $\times 058$ | Device error | H399 |
| 3 xrx | $\times 05 \mathrm{~A}$ | Device error | $\begin{aligned} & H 856 \mathrm{H} 230 \mathrm{H} 412 \\ & \mathrm{H} 302 \end{aligned}$ |
| 3 xyr | . 05 E | Device error | H 245 H 608 H 422 H H 819 H 501 H 301 |
| 3 xxx | $\times 064$ | Device error | H164 H229 C110 |
| 3 xyx | $\times 066$ | Device error | H999 |
| 3 xyx | $\times 068$ | Device error | H150 H250 |
| 3 xax | $\times 06 \mathrm{~A}$ | Device error | $\begin{aligned} & \mathrm{H} 1_{65} \mathrm{H} 415 \mathrm{H} 215 \\ & \mathrm{H} 907 \end{aligned}$ |


| $\begin{aligned} & \mathrm{SRC} \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code delimitions, see A5 and A6 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 3 xcx | x06E | Device error | H454 H215 H512 H810 H105 H904 |
| 3 xax | . 074 | Device error | H140 H932 H228 |
| 3 xxx | $\times 076$ | Device error | H136 H429 H218 <br> H813 H504 |
| 3 xxy | $\times 07 \mathrm{~A}$ | Device error | H436 H220 H815 <br> H112 H509 H608 |
| 3 xxx | $\times 07 \mathrm{C}$ | Device error | H154 H237 C110 |
| 3 xxx | .07E | Device error | H250 H1125 H825 |
| 3 xxx | $\times 080$ | Device crror | H845 H2 ${ }^{1}$ H114 |
| 3 xxx | x08A | Device error | H470 11230 |
| 3 xrx | $\times 090$ | Device error | H452 H136 H212 |
| 3 xax | $\times 094$ | Device error | H664 C114 11913 H 108 |
| 3 xax | x09A | Device error | His0 H225 H425 |
| 3 xrx | x0A0 | Device crror | $\begin{aligned} & \mathrm{H} 650 \mathrm{H} 925 \mathrm{H} 110 \mathrm{Cl} 10 \\ & \mathrm{H} 205 \end{aligned}$ |
| 3 xxx | $\times 0 \mathrm{~A} 2$ | Device crror | H683 H917 |
| 3 xxx | $\times 0 \mathrm{~A} 4$ | Device error | H599 |
| 3 xxx | x0A6 | Device error | H665 H913 H212 <br> H506 C102 H302 |
| 3 xxx | ^0A8 | Device crror | H260) H130 C110 |
| 3 xxx | $\times 0 \mathrm{AA}$ | Device error | $\begin{aligned} & \mathrm{H} 673 \mathrm{H} 912 \mathrm{H} 105 \\ & \mathrm{H} 205 \mathrm{C} 105 \end{aligned}$ |


| $\begin{array}{\|l\|l} \mathrm{SRC} \\ 1234 \end{array}$ | 5678 | Delinition | For result code definitions, see A5 and A6 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 3 xxx | x0AE | Device error | $\begin{aligned} & \mathrm{H} 920 \mathrm{H} 120 \mathrm{II} 220 \\ & \mathrm{H5} 20 \mathrm{H} 420 \end{aligned}$ |
| 3 xxx | x(130 | Device error | $\begin{aligned} & \mathrm{H} 920 \mathrm{H} 120 \mathrm{H} 220 \\ & \mathrm{H5} 20 \mathrm{H} 20 \end{aligned}$ |
| 3 xxx | x0132 | Device error | $\begin{aligned} & \mathrm{H} 920 \mathrm{H} 120 \mathrm{H} 220 \\ & \mathrm{H} 520 \mathrm{H} 20 \end{aligned}$ |
| 3 xxx | . 0134 | Device error | H462 H138 |
| 3 xmy | .0136 | Device crror | H172 11928 |
| 3 xxx | , 1)13E | Device crior | $\begin{aligned} & \mathrm{H} 617 \text { Z915 H915 H115 } \\ & \text { IH415 C1 } 12 \mathrm{H} 20 \times \mathrm{H} 504 \\ & \mathrm{H} 301 \end{aligned}$ |
| 3 xax | .0C4 | Device error | 1146211138 |
| 3 xxx | x0D0 | Device error | 11950 C150 |
| 3 xxx | .0D2 | Device error | H945 C145 H110 |
| 3 xxx | x0D4 | Device error | H250 H450 |
| 3 xxx | x0DE | Device error | $\begin{aligned} & \mathrm{H} 532 \\ & \mathrm{H} / \mathrm{H} 424 \mathrm{H} \mathbf{H} 23 \\ & \mathrm{H} 20 \\ & \mathrm{H} 803 \end{aligned}$ |
| 3xx. | x0E2 | Device error | $\begin{aligned} & \mathrm{H} 125 \mathrm{H} 225 \mathrm{H} \mathbf{1 5} 25 \\ & \mathrm{H} 425 \end{aligned}$ |
| 3 xxx | x0EE | SCA MCLB analysis failed due to read CPU failure | B1:5 B630 B225 A110 |
| 3 xxx | x0FF | No machine check $\log$ buffer data to analyze | Z799 |

## 62PC RC Table

| Result Code | Definition | P/N |
| :---: | :---: | :---: |
| A1 | SCA/console IOC card (01AAI B2) | 4361306 |
| B1 | Planar (01APL) | $\begin{array}{\|l\|} \hline 8328300(\operatorname{Mod} 3-5) \\ 4361076(\operatorname{Mod} 1-2,6-8) \\ 6370231(\operatorname{Mod} 9, A-F) \\ \hline \end{array}$ |
| B2 | One or more control store cards ( 01 ABI S2, T2, U2, V2, R2) | $\begin{aligned} & \mathbf{5 1 2 2 4 9 4}(\operatorname{Mod} 3) \\ & \mathbf{6 2 2 9 4 1 6}(\operatorname{Mod} 1-2,4-9, \text { A-F) } \\ & 8691997(\operatorname{Mod} \text { A-F) } \end{aligned}$ |
| B6 | One or more main store cards (01AB1 G2, H2, J2, K2, L2, M2, N2, P2) | $\begin{aligned} & 4599188(\operatorname{Mod} 3,4) \\ & 7378022(\operatorname{Mod} 5 \text { Type A) } \\ & \mathbf{5 6 5 8 7 1 8}(\operatorname{Mod} 5 \text { Type B) } \\ & \mathbf{5 1 2 0 2 7 1}(\operatorname{Mod} 7) \\ & \mathbf{4 7 7 2 6 0 7}(\operatorname{Mod} 1-2,6,8-9, \\ & \text { A-B, F) } \\ & \mathbf{2 4 4 4 9 6 2}(\operatorname{Mod} \text { C-D }) \\ & \mathbf{6 5 X 5 7 5 0}(\operatorname{Mod} \text { E }) \end{aligned}$ |
| C1 | 62PC F2) | 2771438 |
| C2 | Clock card (01AA1 J2) | $\begin{array}{\|l\|} 2771064 \\ 2550550 \text { old style } \\ \hline \end{array}$ |
| C5 | Yrogram errors |  |
| C6 | 62PC device cables A1A3/A4 (A33-080), <br> AlA5 (A33-090) |  |
| D1 | Channel converter card (01AB1 F2) | $\begin{array}{\|l\|} 2549055(\operatorname{Mod} 1-8) \\ \mathbf{2 4 4 5 9 0 0}(\operatorname{Mod} 9, \\ \hline \end{array}$ |
| D3 | Channel converter card (01AB1 E2) | $\begin{aligned} & 4202240(\operatorname{Mod} 1-8) \\ & \mathbf{2 4 4 5 9 0 2}(\operatorname{Mod} 9, A-F) \\ & \hline \end{aligned}$ |


| Result Code | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| H1 | 62 PC device card AlC2 (B16-500) | 5147210 |
| H2 | 62PC device: card A1 D2 (B16-500) | 5147537 |
| H3 | 62PC device system power supplies (A33-050) |  |
| H4 | 62PC device card A1F2 (B16-500) | 8230323 |
| H5 | 62 PC device card AlE2 (B16-500) | 8230315 |
| H6 | 62 PC device disk enclosure (B16-320) | 5563756 |
| H7 | 62PC device disk drive mechanism (A33-020) |  |
| H8 | 62PC device coil driver card (B16-520) | 5145307 |
| H9 | 62 PC device card A1 B2 (B16-500) | 8230324 |
| Z7 | On site service is required. |  |
| Z8 | Channel overrun error, sec (B16-030). |  |
| Z9 | Possible data related error. Use hardccpy MAP 3390 and pack utility maintenance to analyze the data format on 62PC. |  |
| 7.V | Channel cable on first channel. Sec MAP Ref 019-010 through 019-040 for location by configuration. |  |

## 3370 SRC Table

Use position 2, 6, 7, $\mathbf{8}$ of the SRC to find the Result Code.
Use the Result Code and Probability to determine the action or parts required.

Position 3, $4=$ Actuator Number
$\mathrm{C} 0=1$ st actuator OU number
$\mathrm{Cl}=2$ nd actuator OU number
$\mathrm{C} 2=3$ rd actuator OU number
$\mathrm{C} 3=4$ th actuator OU number
$\mathrm{C} 4=5$ th actuator OU number
$C 5=6$ th actuator OU number
$\mathrm{C} 6=7$ th actuator OU number
$\mathrm{C} 7=8$ th actuator OU number
$\mathrm{C} 8=9$ th actuator OU number
C9 $=10$ th actuator OU number
$C A=11$ th actuator $O U$ number
$C B=12$ th actuator $O U$ number
$C C=13$ tr actuator OU number
$C D=14$ th actuator $O U$ number
$C E=15$ th actuator $O U$ number
$C F=16$ th actuator $O U$ number
$00=$ Cannot Determinc OU

$$
\text { Position } 5 \text { = Model Type }
$$

$0=$ Cannot determine
$1=$ Model 100
$2=$ Model 200
$3=$ Model 3
$4=$ Model 4
$5=$ Model 5
$6=$ Model 6
$7=$ Model 7
$8=$ Model 8
$9=$ Model 18
$1=$ Model 20
$B=$ Model 400
C $=$ Model 40
D $=$ Model 600
$\mathrm{E}=$ Model 700
$\mathrm{F}=$ Model 300

|  |  | For result code definitions, <br> see B6 thru B9 Card 3 |
| :--- | :--- | :--- |
| SRC |  |  |
| $\mathbf{1 2 3 4} \quad 5678$ | Definition | Result Codes <br> and Probability \% |
| $40 \mathrm{xx} \times \mathbf{0 0 1}$ | Bad return code from read CPU. | B135 B630 B225 A110 |
| $40 \mathrm{xx} \times \mathbf{0 0 2}$ | The format type logged is not a valid <br> format. | F399 F201 |
| $40 \mathrm{xx} \times \mathbf{0 0 3}$ | The error code logged is not a valid <br> errot code. | F245 L130 C225 |


| $\begin{array}{\|ll\|} \hline \text { SRC } & \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see B6 thru B9 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: |
| $40 \mathrm{xx} \times 011$ | FOB timcout (disconnect). | L199 C201 |
| $40 \mathrm{xx} \times 012$ | FOB timeout (OU mismatch). | H199 C201 |
| 40xx $\times 013$ | FOB timeout (halt). | L199 C201 |
| $40 \mathrm{xx} \times 020$ | Command reject. | Z399 L101 |
| 40xx x030 | Attachment parity error. | L199, C201 |
| $40 \mathrm{xx} \times 040$ | RAM checkout. | L198 C201 Z301 |
| $40 \mathrm{xx} \times 050$ | OU mismatch. | L199 C201 |
| $40 \mathrm{xx} \times 200$ | Pre-selection check error. | L134 P533 P333 |
| $40 \mathrm{xx} \times 2.10$ | Selection error. | L134 P533 P3 33 |
| $40 \times \mathrm{x} \times 215$ | Selection error. | L134 P533 P333 |
| 40xx $\times 220$ | Tag timeout error. | L134 P533 P333 |
| $40 \mathrm{xx} \times 221$ | Tag timeout error. | L134 P533 P333 |
| $40 \mathrm{xx} \times 230$ | I/O interface parity error. | P351 L149 |
| $40 \mathrm{xx} \times 240$ | 1/O interface parity error. | P351 L149 |
| $40 \mathrm{xx} \times 250$ | 3370 controller processor check. | Z499 |
| $40 \mathrm{xx} \times 260$ | Error alert. | Z499 |
| $40 \mathrm{xx} \times 280$ | Error alert in 3370 adapter. | Y199 |
| $40 \mathrm{xx} \times 290$ | Register load error in controller. | Y299 |
| $40 \mathrm{xx} \times 300$ | Register load error in controiler. | Y299 |
| 40 xx x 310 | Not busy on seek. | Y299 |
| $40 \times x \times 320$ | Tag tilneout. | Z499 |


| $\begin{array}{\|ll\|} \hline \text { SRC } & \\ 12.34 & 5678 \\ \hline \end{array}$ | Deshnition | For result code definitions, see P6 thru B9 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: |
| $40 \mathrm{xx} \times 321$ | Sync in error. | P140 P325 L120 C215 |
| $40 \times \times \times 330$ | Not online. | Y299 |
| $40 \mathrm{xx} \times 331$ | Not online. | Y299 |
| 40 xx | Unexpected drive status. | Y299 |
| $40 \times \mathrm{x} \times 353$ | Unexpected drive status. | Y299 |
| $40 \times x \times 356$ | Unexpected drive status. | Y299 |
| 40 xx 3360 | Sync out error. | Z499 |
| $40 \times \mathrm{x} \times 370$ | Cherk end error. | Z499 |
| $40 \times \mathrm{x} \times 375$ | Check end error. | Y199 |
| $40 \times x \times 500$ | Seek check. | Y299 |
| $40 \times \times \times 510$ | ID mismatch. | Y299 |
| $40 \mathrm{xx} \times 520$ | ID mismatch. | Y199 |
| $40 \times \mathrm{x} \times 590$ | Write overrun. | L199 |
| $40 \mathrm{xx} \times 595$ | Read overrun. | L199 |
| $40 \mathrm{xx} \times 601$ | Data check (not ECC correcied). | Y299 |
| $40 \mathrm{xx} \times 603$ | Data check (not ECC corrected). | Y299 |
| $40 \mathrm{xx} \times 605$ | Data check (not ECC corrected). | Y299 |
| $40 \mathrm{xx} \times 607$ | Data check (alternative off cylinder). | Y299 |
| $40 \mathrm{xx} \times 609$ | Data check (ECC corrected). | Y299 |
| $40 \mathrm{xx} \times 661$ | ECC correctable data check (ECC corrected). | Y299 |
| $40 \mathrm{xx} \times 613$ | Data chick (not ECC corrected). | Y299 |

## 3370 SRC Table

| $\begin{array}{\|ll\|} \hline \text { SRC } & \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see B6 thru B9 Card 3 <br> Resull Codes and Probability \% |
| :---: | :---: | :---: |
| $40 \mathrm{xx} \times 615$ | Data check (not ECC corrected). | Y299 |
| $40 \times x \times 616$ | Data check (not ECC corrected). | Y299 |
| $40 \times x \times 999$ | BSTAT not valict. | L198 7.402 |
| $41 \mathrm{xx} \times 000$ | A false device interface check occurred. | J245 J330 J425 |
| $41 \mathrm{xx} \times 001$ | A tag bus parity error occurred. | J140 J225 J420.J515 |
| $41 \mathrm{xx} \times 002$ | A bus out parity error occurred. | J140 J225 J320 J415 |
| $41 \mathrm{xx} \times 003$ | A tag bus and a bus out parity error occurred. | J140 J225 J320 J415 |
| $\begin{array}{\|cc\|} \hline 4 \times x & \times 100 \\ & \text { thru } \\ & \times 11 \mathrm{~F} \\ \hline \end{array}$ | An HDA sequence failure occurred. | J346, J625 J720 J515 |
| $\begin{array}{ll} 4 \mathrm{xx} & \mathrm{x} 120 \\ & \text { thru } \\ & \times 12 \mathrm{~F} \end{array}$ | An HDA sequence failure occurred. | J360 J840 |
| $41 \times x \times 130$ | An HDA sequence failure occurred. | J360 J940 |
| 41 xx x 131 <br> 4 x   | An HDA sequence failure occurred. | J360 J940 |
| $41 \mathrm{xx} \times 132$ | An HDA sequence failure occurred. | J360 J940 |
| $41 \mathrm{xx} \times 133$ | An HDA sequence failure occurred. | J360 J940 |
| $41 \mathrm{xx} \times 134$ | An HDA sequence failure occurred. | J399 |
| $41 \times 8 \times 135$ | An HDA sequence failure occurred. | J399 |
| $41 \mathrm{xx} \times 136$ | An HDA sequence failure occurred. | J334 J933 J633 |
| $41 \mathrm{xx} \times 137$ | An HDA sequence failure occurred. | J334 J933 J633 |


| $\begin{array}{\|l\|l\|} \hline \text { SRC } \\ 1234 \end{array}$ | 5678 | Definition | For result code definitions, see 136 thru B9 Card! 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| $41 \mathrm{xx}$ | $\times 138$ thru $\times 13 \mathrm{C}$ | An HDA sequence failure occurred. | J350 J950 |
| 41 xx | x13D | An HDA sequence failure occurred. | J399 |
| 41 xx | x13E | An HDA sequence failure occurred. | J345 J930 J625 |
| 41 xx | $\times 13 \mathrm{~F}$ | An HDA sequence failure occurred. | J345 J930 J625 |
| $41 \mathrm{xx}$ | $\begin{aligned} & \times 140 \\ & \text { thru } \\ & \times 1 D F \end{aligned}$ | An HDA sequence failure occurred. | J340 J625 J720 J515 |
| $41 \mathrm{xx}$ | $\begin{aligned} & \mathrm{xIE7} \\ & \text { thru } \\ & \times 1 \mathrm{~F} 6 \end{aligned}$ | An HDA sequerice failure occurred. | J399 |
| 41xx | x1F7 | An HDA sequence failure occurred. | J745 J630 J325 |
| 41 xx | x213 | An invalid location was rietected during a rezero. | J740 J425 J520 P915 |
| 4) xx | $\times 21 B$ | An invalid location was asted during a rezero. | $\begin{aligned} & \mathrm{J} 735 \mathrm{~J} 525 \mathrm{J4} 17 \mathrm{~J} 613 \\ & \mathrm{P9} 910 \end{aligned}$ |
| 41 xx | $\times 225$ | A servo off-track was detected during a rezcro. | J740 P625 P920 J515 |
| 41xx | x22B | A servo off-track was detecied during a rezero. | J540 J425 J320 P915 |
| $41 \mathrm{xx}$ | $\begin{aligned} & \mathrm{x} 240 \\ & \text { thru } \\ & \mathrm{x} 24 \mathrm{~F} \end{aligned}$ | An overshoot was detected during a rezero. | J540 J725 J420 P915 |
| 41 xx | $\times 283$ | An access timeout occurred during a rezero. | $\begin{aligned} & \mathbf{J} 735 \mathrm{~J} 425 \mathrm{~J} 517 \mathrm{J9} \mathbf{i} 3 \\ & \mathbf{P 9 1 0} \end{aligned}$ |

3370 SRC Table

| $\begin{aligned} & \text { SRC } \\ & 123.4 \end{aligned}$ | 5678 | Definition | For result code definitions, see B6 thru B9 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 41 xx | $\times 287$ | An access timeout occurred during a rezero. | $\begin{aligned} & \mathrm{J} 735 \mathrm{~J} 425 \mathrm{~J} 517 \mathrm{~J} 913 \\ & \mathrm{P} 910 \end{aligned}$ |
| 41 xx | x289 | An access timeout occurred during a rezero. | $\begin{aligned} & \mathrm{J} 735 \mathrm{~J} 420 \mathrm{~J} 515 \mathrm{~J} 612 \\ & \mathrm{~J} 910 \mathrm{P9} 08 \end{aligned}$ |
| 41 xx | $\times 2813$ | An access timeout occurred during a rezers | $\begin{aligned} & \text { J935 } \mathbf{J 7 2 0} \text { J515 J412 } \\ & \text { J310 P908 } \end{aligned}$ |
| 41 xx | <2A9 | An access timeout and servo off track oceurred during a rezero. | $\begin{aligned} & \mathrm{J} 735 \mathrm{~J} 425 \mathrm{J517} \\ & \mathrm{P} 910 \end{aligned}$ |
| 41 x | $\times 2 \mathrm{AB}$ | An access timeout and servo, ofl track occurred during a rezero. | $\begin{aligned} & \mathrm{J} 535 \mathrm{~J} 725 \mathrm{~J} 417 \mathrm{J913} \\ & \text { P910 } \end{aligned}$ |
| $41 \mathrm{xx}$ | $\begin{aligned} & \times 200 \\ & \text { thrı } \\ & \times 2 \mathrm{FF} \end{aligned}$ | Any other rezero mode servo error. | J540 J725 J420 P915 |
| $41 \mathrm{xx}$ | $\begin{aligned} & \text { x310 } \\ & \text { thru } \\ & \text { x3FF } \end{aligned}$ | An invalid location was deteited during a seek. | $\begin{array}{\|lll} \mathbf{J 5} 35 & \mathbf{J} 725 & \mathbf{J 4} 17 \\ \mathbf{J 3} 10 \end{array}$ |
| 41 xx | $\times 418$ | A setting servo error occurred. | J645 P630 J725 |
| 41 xx | $\times 444$ | A setting servo error occurred. | $\begin{aligned} & \text { J735 J520 J415 P912 } \\ & \text { J310 J908 } \end{aligned}$ |
| 41xx | $\times 484$ | A setting servo crror occurred. | J740 J525 J420 J915 |
| 41 xx | $\times 4 \mathrm{C} 4$ | A setting servo crror occurred. | J740 J525 J420 J915 |
| $41 \mathrm{xx}$ | $\times 400$ thri $\times 47 \mathrm{~F}$ | Any other setting servo error. | J745 J530 J425 |

Al4 Card 3

| $\begin{aligned} & \text { SRC } \\ & 1234 \end{aligned}$ | 5678 | Definition | For result code definitions, see 136 thru B9 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| $41 \mathrm{xx}$ | $\times 480$ thru $\times 4 F F$ | Any other setting servo error. | J540 J725 J420 P915 |
| $41 \mathrm{xx}$ | $\begin{aligned} & x 50 \\ & \text { thru } \\ & \times 53 \mathrm{~F} \end{aligned}$ | An offset servo error occurred. | J545 J430 J725 |
| $41_{\mathrm{xx}}$ | $\times 540$ thru $\times 57 \mathrm{~F}$ | Anl overshoot was detected during a servo olfset. | J545 P930 J725 |
| $41 \mathrm{xx}$ | $\begin{array}{r} \times 580 \\ \text { thru } \\ \times 58 \mathrm{~F} \\ \hline \end{array}$ | An access timeout occurred during a servo offset. | J540.J725 J420 P915 |
| 41 xx | $\times 61 \mathrm{~A}$ | An invalid loration was detected during track following. | J645 J730 P925 |
| 41xx | $\times 62 A$ | A servo off-track was detected during track following. | $\begin{aligned} & \mathbf{J 5 3 5} \mathbf{J 7 2 0} \text { J615 P612 } \\ & \mathbf{J 4 1 0 ~ J 9 0 8 ~} \end{aligned}$ |
| $41 \mathrm{xx}$ | $\begin{aligned} & \mathrm{x} 600 \\ & \text { thru } \\ & \mathrm{x} 6 \mathrm{FF} \end{aligned}$ | Any other track follewing servo error. | J548 J725 J420 P915 |
| $41 \mathrm{xx}$ | $\begin{aligned} & \times 700 \\ & \text { thru } \\ & \times 7 F F \end{aligned}$ | A false drive (access) check occurred. | J560 J340 |
| 41xx | $\times 800$ | A sector compare check occurred. | P245 J630 J 225 |
| 41xx | $\times 8 \mathrm{Cl}$ | A suctor compare check occurred. | J240 P225 J620 P715 |
| $41 \mathrm{xx}$ | x801 <br> thru <br> x8FF | Any other sector compare check. | J240 P225 J420 P715 |


| $\begin{array}{ll} \text { SRC } & \\ 1234 & 5678 \end{array}$ | Definition | For result code definitions, see B6 thru E9 Card 3 <br> Result Codes and Probahility \% |
| :---: | :---: | :---: |
| $41 \times 9 \times 910$ | Format 1 attachment error message. | $\begin{aligned} & \mathbf{P 7 3 5} \mathbf{J} 20 \mathrm{~J} 115 \text { P512 } \\ & \mathbf{J 3 1 0} \mathbf{P 3} 08 \end{aligned}$ |
| $41 \times \mathrm{x} \times 911$ | A transmit target error occurred. | J240 J525 J420 J315 |
| $41 \mathrm{xx} \times 913$ | A transmit difference high efror occurred. | J440 J525 J320 J215 |
| $41 \times \mathrm{x} \times 914$ | A sync out timming error occurred. | $\begin{aligned} & \text { P135 P325 P517 J113 } \\ & \text { L110 } \end{aligned}$ |
| +1xx $\times 915$ | Drive status during initial selection was not expected. | $\begin{aligned} & \mathbf{J} 535 \\ & \mathbf{J} 110 \end{aligned}$ |
| $41 \times x \quad \times 916$ | A transmit car errer occurred. | J240 J525 J420 J315 |
| $41 \mathrm{xx} \times 917$ | A transmit head error occurred. | P240 J225 J520 J415 |
| $41 \mathrm{xx} \times 918$ | A transmit difference offset error occurred. | J440 J5:5 J320 J215 |
| $41 \mathrm{xx} \times 919$ | Drive status during a read IPL or a retry was not expected. | J540 J325 J220 J415 |
| $41 \mathrm{xx} \times 91 \mathrm{~A}$ | A seek verification cheek occurred. | J445 J730 J525 |
| $41 \mathrm{xx} \times 91 \mathrm{~B}$ | A seek imcomplete on retry occurred. | J445 J730 J525 |
| $41 \times x \times 1 \mathrm{C}$ | No interrupt was received from the drive. | J540 J225 J120 J415 |
| $41 \mathrm{xx} \times 91 \mathrm{D}$ | An untecoveled microcontroller check occurred. | Z299 |
| $41 \times x \times 91 \mathrm{E}$ | An unresolved ID miscompare error occurred. | P145 P330 L125 |
| $\left[\begin{array}{ll} 41 \mathrm{xx} & \mathrm{xA00} \\ & \text { thru } \\ & \mathrm{xAFF} \end{array}\right.$ | A transition detection check or HDA read check. | J340 P625 J220 P715 |


| $\begin{aligned} & \text { SRC } \\ & 12.34 \end{aligned}$ | 5678 | Definition | For result code delinitions, see B6 thmi 139 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| $41 x$ |  | A cadd, write safety check. | P640 P725 J320 J215 |
| 41 xx | $\times 1308$ | A control cheek occuricd. | P240 J325 J220 J515 |
| $41 x x$ | $\begin{aligned} & x B 10 \\ & \text { thru } \\ & \times B 1 F \end{aligned}$ | An index check occurred. | P740 P225 J320,J415 |
| $41 \mathrm{xx}$ | $\times 1320$ thirl $\times 132 \mathrm{~F}$ | A write overrun wecurred. | J345 J230 J72.5 |
| $41 \mathrm{xx}$ | $\begin{aligned} & \times B 40 \\ & \text { thru } \\ & \times B 4 F \end{aligned}$ | A carable enable check oucurred. | J340 J525 J720 P215 |
| $41 \times x$ | $\begin{aligned} & \times B 80 \\ & \text { fhru } \\ & \times B 8 \mathrm{~F} \end{aligned}$ | A write mode check occurred. | P640 J325 P720 J215 |
| $41 \mathrm{xx}$ | $\begin{aligned} & \mathrm{xC00} \\ & \text { thru } \\ & \times \mathrm{CFF} \end{aligned}$ | A device interface bus in error occurred. One or more bits are active. | J240 J425 J520 J315 |
| $41 \mathrm{xx}$ | xE00 thru | A false drive (access) check necurred. | $\begin{gathered} \mathrm{J} 535 \mathrm{~J} 325 \mathrm{~J} 217 \mathrm{~J} 113 \mathrm{P} 2 \\ \times \mathrm{EFF} \end{gathered}$ |
| $41 \mathrm{xx}$ | xF00 <br> thru <br> xFFF | A false read/write check occurred. | J360 J140 |


| $\begin{array}{\|l\|l\|} \hline \text { SRC } \\ 1234 \end{array}$ | 5678 | Definition | For result code derinitions, sec B6 thru B9 Card 3 <br> Result Codes and Probability \% |
| :---: | :---: | :---: | :---: |
| 44 xx | $\times 940$ | An ID field data cheek occurred. | $\begin{aligned} & \text { P635 P720 J715 P'112 } \\ & \text { P810 J608 } \end{aligned}$ |
| 44 xx | $\times 941$ | A data field ECC uncorrectable read error occurred. | $\begin{aligned} & \text { P635 P720 J715 Plı12 } \\ & \text { P810 J608 } \end{aligned}$ |
| 44 xx | $\times 944$ | An ID field no sync byte found occurred. | $\begin{aligned} & \text { P635 P720 J215 J412 } \\ & \text { P210 P108 } \end{aligned}$ |
| 44 xx | $\times 945$ | A data field no sync byte found occurred. | $\begin{aligned} & \text { P635 P720 J215 J412 } \\ & \text { P210 Plos } \end{aligned}$ |
| 44 xx | $\times 949$ | A data field ECC uncorrectable error occurred during a check data operation. | $\begin{aligned} & \text { P635 P720. J715 P112 } \\ & \text { P810 J608 } \end{aligned}$ |
| 44 xx | $\times 94 \mathrm{D}$ | A data field no syne byte found error occurred during a check data operation. | $\begin{aligned} & \text { P635 P720 J215 J412 } \\ & \text { P210 Pl08 } \end{aligned}$ |
| 49 xx | .091 | Tag valid was missing on read/write operation. | P240.J225 J620 J115 |
| 49 xx | x002 | No normal end or check end was received after a read or write. | $\begin{aligned} & \mathrm{J} 235 \mathrm{P} 225 \mathrm{~J} 117 \mathrm{~V} 1 \mathrm{3} \\ & \mathrm{P} 510 \end{aligned}$ |
| 49xx | $\times 003$ | No controller responce on a control operation. | P340 J225 P720 J115 |
| 49 xx | x004 | Normal end was received before the required number of bytes were transmitted. | J120 P525 P820 P315 |
| 49 xx | $\times 006$ | No controller or more than one controller was selec.ed. | P340 L125 P520 P115 |
| 49xx | $\times 097$ | Pre-selection check (selected alert, select active, index alert, sync in, normai end, check end, or tag valid was active) before the sevice was selected. | P360 L140 |


| $\begin{aligned} & 9332 \\ & \text { SRC } \\ & 12345678 \\ & \hline \end{aligned}$ | Definition | For result code delinitions, see C5 Card 4 <br> Result Code and Probability \% |
| :---: | :---: | :---: |
| $92 \mathrm{xx} \times 071$ | VCM inhibit | B160 D120 A320 |
| $92 \mathrm{xx} \times 072$ | Settle error | B170 D125 A305 |
| $92 \mathrm{xx} \times 073$ | VCM integration during settle | B170 D125 A305 |
| 92xx $\times 074$ | VCM current check crror (from SAR wrap) | B150 A350 |
| $92 \mathrm{xx} \times 075$ | VCM zero offsct too high | B195 D105 A301 |
| $92 \mathrm{xx} \times 078$ | 24 V power supply measurement too low | B145 P139 D115 A301 |
| $92 \mathrm{xx} \times 079$ | Measured Gamma out of limits | B180 D119 A301 |
| $92 \mathrm{xx} \times 07 \mathrm{~A}$ | Measured Bias out of limits | B160 D135 A405 |
| $92 \mathrm{xx} \times 07 \mathrm{~B}$ | Demodulator calibration out of limits | A399 |
| $92 \mathrm{xx} \times 081$ | Bad coherence during calibration | A399 |
| $92 \mathrm{xx} \times 086$ | Bad coherence for three consecutive sectors | D165 A330 Bi05 |
| $92 \mathrm{xx} \times 087$ | Demod ready line stuck inactive | A395 Di05 |
| $92 \mathrm{xx} \times 088$ | Demod ready line stuck active | A395 D105 |
| $94 \mathrm{xx} \times 001$ | Hall error | B250 D240 A410 |
| $94 \mathrm{xx} \times 002$ | VCM driver unsafe | B260 A440 |
| $94 \mathrm{xx} \times 003$ | DAC bus parity error | A450 B250 |
| $94 \mathrm{xx} \mathrm{x004}$ | Shurted Data Channel control | A460 D230 A205 B205 |
| $94 \mathrm{xx} \times 005$ | Loss of sync | A460 D225 B215 |
| $94 \mathrm{xx} \mathrm{x006}$ | Brake system error | B290 A410 |
| 94xx x007 | SID read inhibit error | D280 A410 A208 B202 |

9332 SRC Table

| $\begin{aligned} & 9332 \\ & \text { SRC } \\ & 123+5678 \end{aligned}$ | Definition | For result code detinitions, see C5 Card 4 <br> Result Code and Probability \% |
| :---: | :---: | :---: |
| $9^{9} 4 \mathrm{xx} 0008$ | SID write intiibit error | D258 13230 A410 A 202 |
| $94 \times x \times 009$ | CMAC read inhibit error | D255 B230 A 410 A205 |
| $94 \times x \times 00 \mathrm{~A}$ | CMAC write inhibit error | D275 B215 A405 A205 |
| $94 \times x \times 0013$ | Write gate, AE level check, and wrap test | A450 D250 |
| $94 \times x x^{(1)} 0$ C | Detector module error | A480 A218 B202 |
| 94 xx . 000 D | Error in safety control line in combination | A490 A208 B202 |
| $94 \mathrm{xx} \times 00 \mathrm{E}$ | SID byte counter parity error | A 499 |
| $94 \times \mathrm{x} \times 00 \mathrm{~F}$ | Altention without SID detected error | A480 A21. 15202 |
| $94 \times 8 \times 010$ | AE'AGC error | A499 |
| $94 \times x \times 011$ | AGC error-open write I select line | A490 D210 |
| $94 \times \mathrm{x} \times 012$ | Fault in AGC write current source or DE | A475 D225 |
| $94 \times x \quad 013$ | AE'AGC error | A460 D225 A210 B205 |
| $94 \mathrm{xx} \times 014$ | AE/AGC error | A245 A445 B210 |
| $94 \times x \times 015$ | Open write data line | A440 A225 B225 D210 |
| $94 \times x \times 16$ | AE/AGC error | A245 $\mathbf{3} 235 \mathrm{~A} 420$ |
| $94 \times x \times 017$ | AE/AGC error | D265 A420 A210 B205 |
| $94 \mathrm{xx} \times 018$ | AE module error | D285 A415 |
| $94 \mathrm{xx} \times 019$ | AE module error | D295 A405 |
| $94 \mathrm{xx} \mathrm{x01C}$ | SID attention with unknown error | A499 |
| $94 \times \times \times 021$ | SID status 5 read erfor | A499 |


| $\begin{array}{\|l\|l} \hline 9332 \\ \text { SRC } \\ 123+5678 \\ \hline \end{array}$ | Delinition | For result code definitions, see C5 Card 4 <br> Result Code and Probability \% |
| :---: | :---: | :---: |
| $94.0 \times 1022$ | SID status A read error | A499 |
| 94x $4 \times 023$ | Missing sector stuck | A499 |
| 0408024 | Sector stuck active before SID enable | A499 |
| $94 \times x \times 025$ | ID in syne stuck active before SID enable | A499 |
| $04 \times 8 \times 029$ | CMAC register read write error | A480 D210 B210 |
| 94x. $\times 02 \mathrm{~A}$ | CMAC RAM crror | A499 |
| 4 4 $\mathrm{Cx} \times 02 \mathrm{~B}$ | CivaC MAC error | A499 |
| 9408802C | CMAC SAR crror | A499 |
| 94.3031 | Demod l's wrap error | A499 |
| $94 \times 8032$ | Demod 0's wrap eiror | A499 |
| 94x. 03039 | CMAC predriver low gain 00 wrap error | B260 A440 |
| $94.6 \times 03 \mathrm{~A}$ | CMAC predriber low gain 50 wrap error | B260 A440 |
| 94x 40313 | CMAC predriser low gain A8 wrap error | 13260 A440 |
| $94 \mathrm{x} \times 03 \mathrm{C}$ | CMAC predriver high gain 00 wrap error | B260 A440 |
| 94*. | CMAC predriser high gain 50 wrap error | B260 A440 |
| 94.503 E | CMAC predriser high gain A8 wrap error | B260 A440 |
| $94 \mathrm{xx} \times 042$ | Motor frozen erior | B250 D240 A410 |
| 94xx $\times 043$ | Motur acceleration too low | B250 D240 A410 |


| $\begin{array}{ll} 9332 & \\ 512 C & \\ 1234 & 5678 \end{array}$ | Delinition | For result code delimitions, see C5 Cand 4 <br> Result Code and Probability \% |
| :---: | :---: | :---: |
| $94 \times \times .34$ | Monor unable to reach speed | 132.50 D)240 A410 |
| $94 \times 8.45$ | SII) unable to come into sync | A450 D240 13210 |
| $94 \times 8.46$ | Actuator was not retracted | 13290 D) 210 |
| 94 48.1047 | Actuator wias not locked | 13290 D)210 |
| ()4 40.48 | Guad band not at OD during recalibrate | D)260 A420 B220 |
| (9.4 $\times 1.49$ | Track () not found (guard band doesn't disappear at OD) | I)260 A430 13210 |
| 94. 3041 | Nutor ober speed on hring-up | [3280 A420 |
| 34080 | Unathe to ybnce at OI) during mealibrate | A44013230 D230 |
| 9408061 | Unsupported command | A250 A449 320) |
| 94083 | Insalua head | A250 1449 13201 |
| $94 \times x \times 63$ | Insatid wack | A250 A449 3201 |
| () $4 \times 8.064$ | Invalid command mode | A250 A449 13201 |
| $94 \times 8065$ | Head shil exceeds maximum amount | A250 A449 13201 |
| $94 \times 8 \times 66$ | Didn't receise serial port reset as lirst command | A250 A449 13201 |
| $94 \times 8067$ | Head offeet not zero when entering command mode (o) or 1 | A250 A449 [3201 |
| $94 \times 8.069$ | Insalid epindle motor current levels | D)24013240 A420 |
| $94 \times 8.8070$ | Inside one track track to go for 100 long | B2.50 D240 A410 |
| $94 \times 8 \times 071$ | VCNI inhibit | 13260 D220 A420 |
| $94 \times 8 \times 72$ | Settle crror | D)275 13220 A405 |


| $\begin{aligned} & 9332 \\ & \operatorname{SRC} \\ & 12345678 \end{aligned}$ | 1）ctiaition | For result code delimitions， see（＇5 card 4 <br> Resull Code and Probahility \％ |
| :---: | :---: | :---: |
| $94 x \quad 1073$ | VCN integration during setule | （32701）225 1405 |
| 94is 0707 | VCN cument chech emmen（homs S Wなap） | 13250 1400 |
| 94× 1075 | VCN1 ceno ofted tow high | ［3205 1）20＋1401 |
| 94.1078 | 2．1゙ power supply measukemem ton 10 w | 132451391）215 1＋01 |
| 94.1 |  | 132001）2191401 |
| 9411107. | Neasumed Biar out of limus | 132001 ） 235.1205 |
| 9413 30713 | Demendulator calitration out of limik | 14\％） |
| 9 ¢ 40.081 | Bad coherence dumme calibsation | Ifur |
| 94\ 1086 | Bad wherence for thee embnyutis とごいい」 | 1） 20.5 A ${ }^{\text {a }}$ 13205 |
| 9411087 | bemod ready line stuck inactioc | 1t951）205 |
| 9411 1088 | Demodready line suck watio | （toj 1）20．5 |
| 91品（10） 0 | Write Read butter hated due 10 interame ernom | $\begin{aligned} & P 160 B 112 A 20=1105 \\ & A 302 A f(1) A 102 D) 102 \\ & C 202 \end{aligned}$ |
|  | iop umable to deate Abome from the interface | \170 \22．13102 |
| $9^{9} \times 1 \times 102$ | Internal interface cable fature | Cly |
|  | IOP slase FRU callout SRC surlaced and na IOP oblid faturen detected | On－site Sorvice Reyumed |
| 91「x－\105 | Slate was operational but is not operational now（no response on bus） | $\begin{aligned} & \mathrm{Plos} 13112 \mathrm{~A} 05 \mathrm{~F} 105 \\ & \mathrm{~A} 302 \mathrm{~A} 102 \mathrm{~A} 102 \mathrm{D} 102 \\ & \mathrm{C} 202 \end{aligned}$ |


| $\begin{aligned} & 93.32 \\ & 5126 \\ & 12.345678 \end{aligned}$ | Delinition | For result code defimitions， see C5（＇arrl 4 <br> Resull Code and <br> Probability＂\％ |
| :---: | :---: | :---: |
|  | Seyuznce ermor | A 160 \238 13102 |
| り小い 【1゙！ | Bua paries ertor | \100 \23． 13102 |
| （1）\10！2 | Syme ln not cogual to Syme Out | \299 |
| りハい \1゙3 | Nastor unsuccosshal manater | On－site Service Required |
| い小い \1F0 | 『lace was uperational but is nol uperational new（norerponer on bus） |  |
| いドい いだ」 | Bror repurad in rexponse packer with 130 ansociated（1PC． | $\begin{aligned} & \text { M150 } 1247 \text { \301 } 1401 \\ & 13101 \end{aligned}$ |
|  | Las meytuen hut mo（1RC． |  |

## 9332 RC Table

|  | Delinition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| \1 | Driver recuiner card (AOAI) | 8264586 |
| 12 | Atarhment card (A0B3) | 2453516 |
| 13 | Sonocam! (A)CI) | 2453500 |
| A4 | Servocard 1 (A0C2) | 2453500 |
| 131 | 1)riner hathd (A0A0)- Moxdel 200 | 2453414 |
|  | Driser batrel (AOA(0) Noxdel to) | $2453+15$ |
| 132 | 1)river homal (A0A0) | $2453+15$ |
| C 1 | 1)1. Cl cable ( 1016 | 8264628 |
| C2 | ACline cond (A0) 1 ) | 9999999 |
| 1)1 | Diヶk encloware carrice (AODE) Nowd 2()) | 2453672 |
|  | Diak inclomure carrier (AOI)E - Nowlel H) | 2453673 |
| 1)2 | 1 inte conclosure carrier (AODE) | 24.53673 |
| F1 | lame (AOFO) | 8264609 |
| H1 | 11021 |  |
| 1.1 |  | $82646-42$ |
| \11 | 9332 microcode |  |
| Pl |  | 8264255 |
| S1 | DFCl Bus address switch (AOSI) | 8264629 |

## 9332 SRC Table

Positions 2, 6, 7, 8 of the System SRC contains positions 1, 2, 3 and $\mathbf{4}$ respectively of the 9332 Unit Reference Code. Refer to the IBNI 9332 Model 200, 400 Disk Drive: Reference Code Guide, GA21-9836., for service actions which may be required before or after FRU replacement.

Use the Result Code and Probability in the table below to determine the service action or parts required.

Position 3, $4=$ Actuator Number
$00=$ Cannot determine OU
$\mathrm{C} 0=1$ st actuator OU number
$\mathrm{Cl}=2$ nd actuator OU number
$\mathrm{C} 2=3$ rd actuator OU numoer
$\mathrm{C} 3=4$ th actuator OU number
$\mathrm{C} 4=5$ th actuator OU number
$\mathrm{C} 5=6$ th actuator OU number
$\mathrm{Co}=7 \mathrm{th}$ actuator OU number
$\mathrm{C7}=8$ th actuator OU number
$\mathrm{C} 8=9$ th actuator OU number
C $9=10$ th actuator OU number
$\mathrm{CA}=11$ th actuator OU number
$C B=12$ th actuator OU number
$C C=13$ th actuator $O U$ number
$C D=14$ h actuator OU number
$C E=15$ th actuator OU number
$C F=16$ th actuator $O U$ number
$E 0=17$ th actuator $O U$ number
$E I=18$ th actuator $O$ O! number
$E 2=19 \mathrm{~h}$ actuator OU number
$\mathrm{E} 3=20 \mathrm{~h}$ atuathe OU number
$E 4=21$ st actuator OL wammer
E5 $=220$ d accuator (30) number
$\mathrm{E}_{6}=23 \mathrm{rdactuat}$ oti number
$\mathrm{E} 7=2$ th actuator $O U$ number
$\mathrm{E} 8=25$ th actuator OU number
$\mathrm{E}_{\mathrm{9}}=2$ 2th actuater OU number
$E A=27 \mathrm{th}$ actuato: OU number
$E B=2$ sth actuator OU number
$\mathrm{EC}=29 \mathrm{ti}$ actuater OU number
$E D=30$ th actuator OU' number
$E E=31$ st athator OU number
$\mathrm{EF}=32$ nd actuator OU number

Position $5=$ Model T, ee
$0=$ Cannot determine
$1=$ Modet 100
$2=$ Model 200
$B=$ Model 400
$D=$ Model 600
$\mathrm{E}=$ Model 700
$E=$ Model 300

| $\begin{aligned} & 9332 \\ & \text { SRC } \\ & 1234 \quad 5678 \end{aligned}$ | Definition | For result code definitions, see (5 Card 4 <br> Result Code and Probability \% |
| :---: | :---: | :---: |
| 90 xx -8013 | FP TCB handshake timeoui | $\begin{aligned} & \mathbf{A} 282 \mathrm{~A} 308 \mathrm{~A} 408 \mathrm{D} 101 \\ & \mathbf{1 3 1 0 1} \end{aligned}$ |
| $9^{9} 0 \times 8 \times 823$ | Invalid command pending bit | A29) |
| $90 \times x \quad 827$ | Invalid TCB pointer value | 1299 |
| 90xx 8828 | Microcode excertion (sanity error) | A299 |
| ${ }^{9} 0 \times \mathrm{x} \quad 82 \mathrm{~A}$ | No CE block size but | A299 |
| ${ }^{9} 0 \times \mathrm{x} \times 8213$ | No data block siza bit | A299 |
| 90 xx 882C | Invalid sequence bit set | A299 |
| ${ }^{9} 0 \mathrm{xx} \times 82 \mathrm{E}$ | Invalid LRC in write bufter | A299 |
| $90 \times 8 \times 830$ | Scan hardware tent lailed | A299 |
| $90 \times \mathrm{x} \times 832$ | Scan hardware timeout | A299 |
| $90 \times 1.835$ | Cllegat jump to tramber table | A299 |
| $90 \times x \times 836$ | Invalid Pacility Mags | A299 |
| $90 \times x \times 837$ | No veritication flags set | A299 |
| $90 \times 8.838$ | Jump to code-free space | A299 |
| $90 \times 8 \times 39$ | Invalid machine state in FACTST | A299 |
| $00 \times 8 \times 8313$ | Facility I selected when not installed | 13289 A404 A104C103 |
| $90 \div 8.840$ | Bus parity enror during transfer to master | A299 |
| $90 \times \times 844$ | Unsupported packet length | A299 |
| $90 \times \times 84 \mathrm{~A}$ | Master terminated transter | A270 A127C102 B101 |
| $90 \mathrm{xx} \times 8413$ | Slave mernal error | A299 |

$\left.\begin{array}{|l|l|l|}\hline & & \begin{array}{l}\text { For result colle definitions, } \\ \text { see C5 Card 4 }\end{array} \\ \begin{array}{l}\mathbf{9 3 3 2} \\ \text { 1234 }\end{array} & 5678\end{array}\right)$

| $\begin{aligned} & 9332 \\ & \text { SRC } \\ & 1234 \quad 56 ; 8 \end{aligned}$ | Definition | For result code definitions, see C5 Card 4 <br> Result Code and Probability \% |
| :---: | :---: | :---: |
| 90 xx 889 E | No mable LOG MED code | A299 |
| $90 \times \times 89 \mathrm{~F}$ | Incorrect or no sequdone bit | A299 |
| $90 \times \mathrm{x}$ 88130 | Self initiated reset due to timeout on FP teb handshake | A299 |
| $90 \mathrm{xx} \times 8 \mathrm{Bl}$ | IP failure-incorrect RC from a module | A299 |
| $40 \times \times 8132$ | Invalid data in the attribute sector | A2 and D1 |
| $90 \mathrm{x}: \times 8 \mathrm{B4}$ | $\begin{aligned} & \text { Microcode crror-FCSTAT has invalid } \\ & \text { data } \end{aligned}$ | A299 |
| $90 \times 8385$ | FP broken | A299 |
| $90 \times x \times 8 B 7$ | Cheeksum failed on IP RAM code | A295 M105 |
| 90x. 8838 | ROS - RAM code incompatible | M189 A 211 |
|  | IP failure-illegal error code generated | A299 |
| $90 \times x$-8B3 ${ }^{\text {a }}$ | Slave not ready - unable to get code load due to facility | $\begin{aligned} & \text { A320 B120 A420 A220 } \\ & \text { D120 } \end{aligned}$ |
| $90 \mathrm{xx} \times 8 \mathrm{BC}$ | Failed to read all the FP code | A335 A435 A229 B101 |
| $90 \mathrm{xx} \times 8 \mathrm{C} 1$ | IP failure - bailout module entered | A299 |
| $90 \mathrm{xx} \times 8 \mathrm{C} 2$ | File failure - 90 second timeout exceeded | $\begin{aligned} & \text { B120 A320 A420 D120 } \\ & \text { A220 } \end{aligned}$ |
| $90 \times \times \times 86$ | Thermal threshold exceeded | F180 A210 B110 |
| $9 \times 8 \times 8$ | Parity error detected on IP CSR | A299 |
| $90 \mathrm{xx} \times 8 \mathrm{C} 8$ | Parity error detected on FP CSR | A299 |
| 90xx -8CA | Failed to read any copy of IP code | $\begin{aligned} & \text { D150 A224 A312 A412 } \\ & \text { B102 } \end{aligned}$ |


| 9332 <br> SRC <br> 12345678 | Detinition | For result code delinitions, see C5 Card 4 <br> Result Code and Prohability \% |
| :---: | :---: | :---: |
| $90 \mathrm{xx} \times 8 \mathrm{CB}$ | VPD data in the field record is invalid | On-site Service Required |
| $90 \mathrm{xx} \times 8 \mathrm{CC}$ | Self initiated reset due to bad TSM data | A299 |
| $91 \times \mathrm{x} \times 001$ | Attention liae from servo cards () and 1 both active | A285 B115 |
| $91 \times x \times 002$ | Altention line from servo card 0 active | A355 A $244 \mathrm{B1} 01$ |
| $91 \times x \times 004$ | Clock check error from servo cards 0 and 1 | A2\%) B110 |
| $91 \times x .8005$ | Clock check error from seroo card 0 | A2.55 A3+4 $\mathrm{Bl}_{101}$ |
| $918 \times 3007$ | Sector checker error | A370 1229 13101 |
| ${ }^{9} 1 \mathrm{xx} \times 008$ | Wrap Plillo 10 error | A295 A30+ ${ }^{\text {B10 }}{ }^{\prime}$ |
| 91xx a 009 | Read and write borh active th encode decode | A 299 |
| $91 \times 8.800 \mathrm{~B}$ | Syme byte not found during write oneration | A299 |
| 91-x 300 C | Command seyuence error | A 299 |
| $91 \times 8 \times 00 \mathrm{D}$ | LRCerror | A299 |
| 91 xx . 000 E | Panty arme on encode decode ECChus | A299 |
| 918x a001: | Suctor pulse foad error | A299 |
| $91 \times x \times 010$ | Hardsare detected thag byte miscompare | A29) |
| $91 \times \mathrm{x} \times 11$ | Power supplies failing | P170 A228 B102 |
| 91x: . 012 | Read and Write both active in command buffer | A 2.99 |
| $91 \times 1.1013$ | ECC hardware error | A299 |


| $\begin{aligned} & 9332 \\ & \text { SRC } \\ & 12345678 \end{aligned}$ | Definition | For result code definitions, see C5 Cand 4 <br> Result Code and Probability \% |
| :---: | :---: | :---: |
| $91 \mathrm{xx} \times 014$ | Read/write counter parity error | A299 |
| 91 xx . 015 | Operations too long | A270 A330 |
| $91 \times x$ | Byte clock checker | A299 |
| 91 xx x 018 | Encode/decode crror | A299 |
| $91 \mathrm{xx} \times 019$ | No R/W hardware error found active in R/W control error registers | A299 |
| $91 \times x \times 14$ | Hardware detected sector number miscompare (index counter out of sync with disk) | 1200 A308 13102 |
| ${ }^{9} 1 \mathrm{xx} \times 01 \mathrm{~B}$ | Index counter found to be out of syne with disk during locate verify | 1205 1305 |
| $91 \times x \times 01 \mathrm{C}$ | FP software injected error | A299 |
| $91 \mathrm{xx} \times 01 \mathrm{D}$ | Sync byte not found during read operation | A350 A230 D)120 |
| $91 \mathrm{xx} \times 01 \mathrm{E}$ | ECC not on the fly error from error interrupt routine | A350 A230 D120 |
| $91 \times x \times 01 F$ | ECC on the fly error not corrected by single burst code | A350 A230 D)120 |
| $91 \times x \times 020$ | Max retries attempted for locate verify | A360 A234D105 3101 |
| $91^{\text {xx }} \times 021$ | IP write inhibit error | A299 |
| $91 \mathrm{xx} \times 022$ | Read inhibit timeout before locate verify | A393 A2051)101 13101 |
| $91 \mathrm{xx} \times 023$ | Sector pulse timeout before locate verify | 1393 A205 D101 3101 |
| $91 \mathrm{xx} \times 024$ | Correct track verification failed during locate verify | A370 A230 |
| $91 \mathrm{xx} \times 027$ | Sector pulse timeout on synchronous start of read/write operation | A280 A310 $\mathbf{B 1 1 0}$ |


| $\begin{aligned} & 93332 \\ & \text { SRC } \\ & 12345678 \end{aligned}$ | Definition | For result code delimitions, see C5 Card 4 <br> Result Code and <br> Probability \% |
| :---: | :---: | :---: |
| $91 \times \mathrm{x}$. 028 | Read inhibit timeout after access or head shift command from segueneer module | A393 A205 D)101 B101 |
| $91 \times \mathrm{x} \times 29$ | Read inhibit timeout after servo processor reset during servo initialization | A398 A20113101 |
| $91 \times 8 \times 020$ | Invalid partial Task Control i3lock (TCB) | A299 |
| 91x. 0 02E | Negative number of spares in return track parameters | A299 |
| $91 \times 8.80$ | Invalid displacement or defeet list foomd in RIP data during locate spare cone:mand | A299 |
| 91.4.8030 | Invalid RIP data found during reallocate with parameters command | A299 |
| $91 \times x$ x 031 | Insatid sector interlease factor | 1299 |
| $91 \times x \quad 8032$ | Fead inhibit timeout before format locate verify | A393 A20. D)101 B101 |
| $91 \times 8 \times 033$ | Scetor pulse timeout before format lecate serily | A280 A310 B110 |
| 9) $\times 1 \times 834$ | Wrong servo processor serial port echo or response | A345 A230 3125 |
| $91 \times x \quad 035$ | Correet track serification fated when lerifying format. | (370 \2291)10i |
| $91 \times x \quad 0036$ | Invalid locate spare parameter | A299 |
| $91 \times \mathrm{x}$ | Sector pulse timeout for so an mox operation | A296 A303 13101 |
| $91 \times x \quad 0038$ | Invalid read modulo parameter | 1299 |
| $91 \times x \quad 039$ | Serio processor serial port echo timeout | 1345 A2.30 13125 |

9332 SRC Table

| $\begin{aligned} & 9332 \\ & \mathrm{SRC} \\ & 1234.5678 \end{aligned}$ | Dermition | Fer result code definitions: see C 5 Card 4 Resull Code and Proha: ility $\%$ |
| :---: | :---: | :---: |
| 91 41.83 .1 | Imbalid read write sequencer parameter | A29) |
| 91x 03313 | lonalid parameter for sel serbor parameter sequencer | A299 |
| 91 10.3 C | Intalid mone ISN we content stome parameter | A299 |
| 91>x (0,30) | Imsatid eylinder head byes found in besor 11) for locate berity | A290 |
| 91 $\times 1036$ | Insadid nector number found in sector 11) Pom locate verify | A299 |
| リ1 \03F | Insalid defect sector number fonnd in defert lise of sector II) for locate berify | A299 |
| 9101040 | lansald diaplacement fond in secem 11) for focate serify | 1290 |
| 91ハ 0.44 | Number of sectors loaned was abowe maximum allowed daring track Characterization | 1209 |
| 9110.0 | Number of sectom loaned was a nezative result during tatack characterization | 1290 |
| 913 1048 | Imbadid dinplacement and loaned combination found during next track chatacterization | A299 |
| $91 \times 84$ | Timer conmer () balue has run out of balid bounds during sector setup | 1299 |
| $91 \times 8$ | Invalid deep IDRP restore side sector parameter | A299 |
| 91xx 80413 | Command recoliced from IP for Jown Pacility | A299 |
| 91x. x04C | Insaide control store RAM code vectors not setup in RAM | A29) |


| $\begin{aligned} & 9332 \\ & \text { SRC } \\ & 12345678 \end{aligned}$ | Detinition | For result code delinitions, see C's Card 4 Result Code aud Probalitity " $\%$ |
| :---: | :---: | :---: |
| 913x 3040 | Inder counter found ta be oun of eyne with disk when verifying lormat | 1205 1305 |
| 91ax 604 E | Initial microcode load check sum error | A20\% |
| $91 \times 8.8041$ | Invalid DFC command conde found during command decode | A2\% |
| $914 \mathrm{x} \times 050$ | Invalid sector history for activity byte reportad during RWOPS adjust tepe: for activity harning a revoluton | 1203 |
| 914x 3051 | In alid sector history for atelivity by te reperted during, RWOPS adjust und fior activity not burning a rewolution | A2\% |
| 91181052 | Inatid actor hintory for activity byte repored during RWOPS adjust type 2 | A2\% |
| 914.8053 | Invalid defect information found when rerifying format | . 1299 |
| $914 x .8054$ | limer counter() baluc has run out of valid bounds during syne index uperation | A299 |
| 91~. 3055 | Next track chatacterization found target sector to be on the following track | A299 |
| 91xx 8057 | Investid sector ID or pad value found in SAT map or grown defeet map during disk initialization | A299 |
|   <br> $91 \times x$ 058 | Invalid command to give to serve precensor | A299 |
| $91 \times 8.059$ | Invalid device command code found during command decode | A299 |
| $91 \mathrm{xx} \times 05 \mathrm{D}$ | Invalid cylinder/head parameter | A299 |
| 91 xx . 055 E | Abort read write operation due to attention line | A345 A240) 13115 |


| $\begin{aligned} & 93.32 \\ & \mathrm{SRC} \\ & 12.34 \quad 5678 \end{aligned}$ | Definition | For result code detinitions, see C'5 Cand 4 <br> Result Code and Probability $\%$ |
| :---: | :---: | :---: |
| 勺1 $\times$, 05 F | Nested Sp efror catmed R W ementrol seconery procedure to lail | A395 A20413101 |
| $91 \times 1000$ | Illegal error code reported to E:RP | A29) |
| $91 \times 3.61$ | Feror from read write operation adjust Function during FP ERP | A29\% |
| 913x 40131$)$ | Syac byte not found during read uperation in lirst 3 eylinders of the disk - not reallowatable | D)180 A 315 A 20.5 |
| 91x (013E | 1:CC not on the my erfor from error interrupt routine in lirst 3 cylinders of the disk mot reallocatable | 1) 180 A 31.5 \20.5 |
| $914 \times 101$ | I:CC on the tly error not corrected by single hurst code in lirst 3 eylinders of the disk not reallocatable | 1) 180 - 315 1205 |
| $91 \times 8101$ | Attention line from Servo Cards 0 and 1 both actice | A2851321.5 |
| 91×x 10103 | Altention line from Servo Card I active | A455 A24+13201 |
| $91 \times 104$ | Cluck Check crror from Servo Cards () and! | A290 13210 |
| 91× $1 \times 106$ | Clock Check error from Servo Card 1 | A490 A209 13201 |
| $91 \times 107$ | Sector checker error | A470 A229 13201 |
| $91 \times 8 \times 108$ | Wrap Pllllo 10 crror | A295 A404 13201 |
| $91 \times 8 \times 109$ | Rewal and write both active to encode decode | A290 A408 D201 B201 |
| $91 \times 1013$ | Sync byte not found during write operation | 1299 |
| $91 \times 8 \times 10 C$ | Command sequence error | A299 |
| $91 \times 8 \times 10 \mathrm{D}$ | LRC error | A299 |


| $\begin{aligned} & 9332 \\ & \text { SRC } \\ & 12345678 \end{aligned}$ | Dectinution | For result code definitions, see C5 Card 4 <br> Result Code and Probability \% |
| :---: | :---: | :---: |
| y $1 \times \times$. 10E | Parity error on encode decode ECC bus | A29) |
| 918x 810F: | Sector pube iond error | A29) |
| 918x $\times 110$ | Hardwate detected mag byte micoompare | A299 |
| 91xx $\times 111$ | Power cupplies failing | P170 A228 13202 |
| 918. $\times 112$ | Read and Write both active in command buffer | A299 |
| 91:x $\times 113$ | ECC hardware ertor | A2\% |
| 918x $\times 114$ | Read write counter parity error | A299 |
| 915: 1115 | Operatiom too long | A270) A430 |
| $91 \times x \times 116$ | By te clowk checker | A299 |
| 91818818 | Encode decoste ertor | A299 |
| $91 \times 8 \times 119$ | No R W hardware cerror found active in R W control error registers | A299 |
| $91 \times 8 \times 114$ | Hardware detected sector number miscompare (index counter out of syne with (isk) | A290 A408 B202 |
| $91 \times \mathrm{x} \times 11 \mathrm{~B}$ | Index counter found to be out of sync with disk during locate verify | A295 $\mathbf{1 4 0 5}$ |
| $91 \times x \times 11 \mathrm{C}$ | FP software injetted error | A299 |
| 918x 8111) | Sync byie not found during read operation | A450 A230 D220 |
| $91 \times \mathrm{x} \times 11 \mathrm{E}$ | ECC not on the fly error from error interrupt routine | A4.0 A230, D220 |
| $91 \times x \times 11 \mathrm{~F}$ | ECC on the fly error not corrected by single burst code | A450 A230 D220 |


| $\begin{aligned} & 9332 \\ & \operatorname{SRC} \\ & 12345078 \end{aligned}$ | Delinition | For result code definitions, see ('5 Courd 4 <br> Resull Code and I'robability \% |
| :---: | :---: | :---: |
| O1u . 120 | Max metrio attempted for locate verify | 1400 \2341)20.513201 |
| 91x 1121 | IP write inhibit cron | 120 |
| प1 1122 | Read inhibat dimeora before lowate serils | Af193 12051)20113201 |
| 91 1123 | Sictor pulse tamenut betone locate urrify | 12:0 $1+1013210$ |
| 1)11 1124 |  locate serify | 1470.1230 |
| 1) $1 \times 1 \times 127$ |  batt of read write operation | 1280 141013210 |
| ソ1 \128 | Read inhibit timentat alter accemo m lead shift command from seyuencer module | 1493, 120.51)201 13201 |
| 410129 | Read inhibit timenot alter herdo <br>  intialization | 140゙, 120113201 |
| $91 \times 3$ (120 | Iomalid partial Tank Control Block (ICB) | 120) |
| 915x 12 L | Negative number of ypares in relurn trach parametors | 12\% |
| 918s $\quad 121$ | Invalid displacement or defeed lint found in RIP datis during locate ypare command | \29\% |
| $91 \times 8 \times 130$ | Invalid RIP data foumd during reallocate with parametor command | A209 |
| $91 \times \mathrm{x} \times 131$ | Invalid sectur interlease factor | 129) |
| $91 \times \mathrm{x} \times 132$ | Read imhbit timeout before format locate berify | A403 12051)201 13201 |
| $91 \times \times 133$ | Sector pulse timeont before format bocate verify | 1280 A-10 13210 |


| 9332 <br> SRC <br> $123+5678$ | Definition | For result code detimitions, see C5 Card 4 <br> Result Code and Prohalitity \% |
| :---: | :---: | :---: |
| $91 \times \mathrm{x} \times 134$ | Wrong servo processor serial port ceho or reaponse | A4 45 A230 31325 |
| $91 \times x \quad \times 135$ | Correct track berification failed when berifying format | A470 A23) 1)201 |
| $91 \times \times 136$ | Intalid locate spare parameter | A299 |
| $91 \mathrm{xx} \times 137$ | Sector pulse timegut for syne index operation | A296 A40313201 |
| $91 \times \mathrm{x} \times 138$ | insalid read modulo parameter | A2\% |
| 11x $\times 139$ | Servo processor serial port echo timeout | A445 A230) 13225 |
| $91 \mathrm{cx} \times 13 \mathrm{~A}$ | Invald read write sequencer parameter | A29\% |
| ${ }^{9} 1 \times x \quad \times 13 B$ | Invalicl paramter ©n set servo parameters sequencer | A29\% |
| $91 \mathrm{xx} \times 13 \mathrm{C}$ | Invalid move TSM to control stere parameter | A29\% |
| $91 \mathrm{xx} \times 13 \mathrm{D}$ | Invalid cylinder, head bytes found in sector ID for locate verify | A299 |
| $91 \times x \times 13 \mathrm{E}$ | Invalid sector number found in sector ID for lecate verify | A29) |
| 91xx $\times 13 \mathrm{~F}$ | Invalid defect sector number found in defect hist of sector ID for locate verify | A299 |
| $91 \times \times 140$ | invalid displacement found in seeter ID for locate verify | A299 |
| $91 \times x \times 144$ | Nurnber of sectors loaned was above maximum allowed during track characterizat:on | A299 |
| $91 \mathrm{xx} \times 145$ | Number of sectors loaned was a negative result during track characterization | A299 |


| $93.32$ <br> SRC <br> 12345678 | D)ctinition | For result code definitions, see C5 Card 4 <br> Result Code and Probability \% |
| :---: | :---: | :---: |
| 9118x $\times 148$ | Invalid displacement and baned combination found during next track characterization | A 299 |
| $9148 \times 149$ | Timer counter () value has run out of valid boumds during sector setup | A299 |
| $91 \times x \times 14$ | Invalid deep DRP restore side sectors parameter | A299 |
| 914 8143 | Command received lrom IP for down Pacility | A299 |
| 91x $\times 14 \mathrm{C}$ | Invalid onntrol store RAM conle - vectors not setup in RAM | A299 |
| 91x : 141) | Index conancer found to be out of syac with disk when verifying format | A295 A40+13201 |
| $91 \mathrm{xx} \times 14 \mathrm{E}$ | Initial microcode load check sum error | A299 |
| 9) $1 \times \mathrm{x} \times 14$ | Invalid DFC command found durmg command decode | A299 |
| 9) $1 \times \times 150$ | lavalid sector history for activity byte eported during RWOPS adjust type I for activity burning a revolution | A299 |
| $91 \times 8 \times 151$ | Invalid sector history for activity byte reportad during RWOPS adjust type for activity not burning a revolution | A299 |
| (9) $1 \times \times 152$ | Invalid sector history for activity byte reported during RWOPS adjust type 2 | A299 |
| ${ }^{9} 1 \times \times 153$ | Invalid defect information found when verifying format | A299 |
| ${ }_{91 \times x} \times 154$ | Timer/counter) value has run out of valid bounds during sync index operation | A299 |
| $91 \times \times 155$ | Nexit:ack characterization found target sector to be on the following track | A299 |


| $\begin{aligned} & 933.32 \\ & \mathrm{SiRC} \\ & 12.34 \quad 5678 \end{aligned}$ | Derlinition | For result code delinitions， see（ 5 Cand 4 <br> Resull code and robability＂品 |
| :---: | :---: | :---: |
| $91 \times 157$ | Imatid sector（1）（m pad value foumd in SAT man or geown defer map during diak inibialization | 12ツ |
| ソ1， 1158 | Imaticl command．fine 10 serto procerom | 120\％ |
| $41 \times 159$ | Imbald device command code fomme during command deconde | 1299 |
| $91 \times 8 \times 15$ | lmatide eytinder head parameter | 1209 |
| $91 \times 15 \mathrm{E}$ | Abort read write aperation dee 6 Atcontion line | A4．5 12.1013215 |
| $91 \times 10^{\prime \prime}$ | Nestad SP ermor cansed R W wntrol rewory procedure to fail | A405 $\mathbf{1 2 0 + 1 3 2 0 1}$ |
| $91 \times 160$ | Hllesal eron code reported to E：R1） | A209 |
| 91 18.161 | I：rom from read write operaton adjust function during FFP E：RP | 12\％ |
| $91 \times 8$ | Syme bye not found during dead uperation in 「ant 3 cytinders of the dink－not realiocatable | D） $280 \times 1.5$ 1205 |
| $91 \times x \quad 1314$ | ECC not on the lly error from ermor internupt routine in liest 3 cylinder of the disk－not reallocatable | 1） 280.14 .5120 .5 |
| $91 \times \mathrm{x}$（131 | ECC on the dy error not corsected by single bumt code in first 3 cylinder o the diak－not reallocatable | D）200 $\mathbf{1}+1.5120 .5$ |
| $92 \times x \quad 001$ | tall error | ［31501）140 ${ }^{\text {A }} 310$ |
| $92 \times x \quad 0002$ | VCM driver unsate | 13160 A340 |
| $92 \times x \quad 0003$ | DAC bus parity error | A350 B150 |
| $92 \mathrm{xx} \quad \mathrm{x} 004$ | Shorted Data Channel control | A360 DI30 A 205 BI 105 |


| 9332 <br> SRC <br> 12345678 | Definition | For result code definitions， see C5 Card 4 <br> Result Code and Probability \％ |
| :---: | :---: | :---: |
| 932 61005 | 1．04s of syan | A360）D125 B115 |
| リ2 310006 | Brake system error | 13190．A310 |
| प2い 1007 | SID read inhibit error | D） $180 \mathrm{~A} 310 \mathrm{~A} 208 \mathrm{B1} 102$ |
| 92510008 | Sth）write inhbit error | D158 B130 A310 A202 |
| 92い 1009 | Chisc real inhibit eror | D） 55 13130 A310 A205 |
| 9231100 A | CMAC write inhibit crror | D155 13130 A310 A205 |
| り2い（0013 | Write gate，AE level check，and wrap test | A350）D150 |
| 1220 100 0 | Detector module error | A380 A21813102 |
| $92318001)$ | Error in safety control line combination | A290 A308 $\mathrm{Bl}^{102}$ |
| y2\x（00）L | Sll）byte counter parity error | A399 |
| 92 xa ， 100 F | Altention without SID detected error | A380 A21： 13102 |
| $\mathrm{y}^{2 \times \mathrm{S}}$ ， 01010 | AE AGC error | A399 |
| 92い 01011 | AGC error－open write I select line | A390 D110 |
| リ2x． 01012 | Fitult in AGC write current source or DE | A375 D125 |
| 92xx $\times 013$ | AE／AGC error | A360 D125 A210 B105 |
| $92 \mathrm{xx} \times 014$ | AE，AGC error | A245 A345 B110 |
| 9 $2 \times x$ 8015 | Open write data line | A340 A225 B125 r： 10 |
| $92 \times 8$ 98016 | AE／AGC error | A245 B135 A320 |
| 92xs $\times 017$ | AE／AGC crror | D165 A320 A 2 10 B105 |
| $92 \times x \quad 8018$ | AE module error | D185 A315 |


| $\begin{aligned} & 9332 \\ & \text { SRC } \\ & 12345678 \\ & \hline \end{aligned}$ | Detinition | For resull code delinitions, see C5 Cand 4 <br> Result Code and Probability \% |
| :---: | :---: | :---: |
| 92ax 1019 | AE moxlule error | D) $195 \mathrm{A305}$ |
| 92 x , 0101 C | S(1) attertion with unknown error | A399 |
| 92\x 0221 | SII) status 5 read error | A399 |
| 923 102? | S(1) status A read error | A399) |
| $924 \times 8023$ | Missing seetor htuck | A399) |
| $92 \backslash \times 1024$ | Sectur stuck atctive betore SID enable | A399 |
| 92xa a 025 | 11) in unde suack active before SID ciable | A399 |
| $92 \times 8029$ | CAIAC mginter read wite ermor | A370 B120 D) 110 |
| 9218.0 .021 | CMAC RAM crmi | A399 |
| $92 \times 102 \mathrm{~B}$ | CMAC MAC emor | A399 |
| $92 \mathrm{xx} \times 102 \mathrm{C}$ | CMAC SAR error | A399 |
| $92 \mathrm{x} \times 031$ | Demod 1's wrap error | A399 |
| $92 \mathrm{cx} \times 032$ | Demedd 0's wrap error | A399 |
| $92 \mathrm{xx} \times 039$ | CMAC predriver low gain 00 wrap error | B160 A340 |
| $92 \times . .103 A$ | CMAC predriver low gain 50 wrap error | B160 A340 |
| $92 \mathrm{xx} \times 0313$ | CMAC predriver low gain A8 wrap error | B160 A340 |
| 92x - 03C | CMAC predriver high gain 00 wran error | B160 A340 |
| 92xx . 0031) | CNAC predriver high gain 50 wrap error $\qquad$ | B160 A34) |
| 92x $\times 1.03 \mathrm{E}$ | CMAC predriver high gain A8 wrap error | B160 A340 |


| $\begin{aligned} & 93.32 \\ & 5126 \\ & 12345678 \end{aligned}$ | Hedinition | For result conde delininions, see C5 Card of <br> Result Conte and Prohability \% |
| :---: | :---: | :---: |
| $92 \times 1.042$ | Notor liozen error | B150 ${ }^{\circ}$ ) 1.10 A310 |
| 9210.043 | Notor acceleration too low | 13150 1)140 $\mathbf{N 3 1 0}^{1}$ |
| (12) 10.44 | Motor unable to reach speed | 13150 1)140, 310 |
| 12 21.1045 | S1') unable lo come into yyuc | 13501)14013110 |
| 92\x 3046 | Actuator was not retracted | 1390 D)110 |
| $92 \times 1.047$ | Actuator was not locked | B190 D110 |
| 92\x . 048 | Guard band not at OD during recalibrate | D) 100 1312!) $\mathbf{1 3 2 0}^{\text {a }}$ |
| (1) 31040 | Track () mot found (guard band doenn't disappear at OD) | 1)160 1330 13110 |
| $92 \times 104$ | Notor (1)er speeds on bring-up | 13180 1320 |
| 92\. 104C | Unable to syne at OD during recalibrate | A34013130 1)130 |
| $929 \times 1061$ | Unsupported command | A250 A34913101 |
| ,2x. 0.062 | Invalid head | A250 A3-4 13101 |
| $92 \mathrm{xx} \times 063$ | Invalis track | A250 A349 13101 |
| 92 xx .064 | Invalid command mode | A250 A349 Bioi |
| $92 \mathrm{xx} \times 065$ | Head shift exceeds maximum amount | A250 A349 [310] |
| บ2xx . 066 | Didn't receive serial port reset as first command | A250 A 349 13101 |
| 92x. $\times 067$ | Head offset not zero when entering command mode 0 or 1 | A250 A349 13101 |
| $92 \times 8 \times 069$ | Invalid spindle motor current levels | i) $140 \mathrm{B140} \mathrm{A320}$ |
| $92 \mathrm{xx} \times 070$ | Inside sne track to go for too long | 15:50 D140 A310 |


| 9335 <br> SRC <br> 12345678 | Definition | For result code definitions, see A14 thri A16 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $83 \mathrm{xx} \times 11 \mathrm{C}$ |  | $\begin{aligned} & \text { E1 B2 B6 } \\ & \text { E1 B3 B7 } \end{aligned}$ |
| 83 xx x 11 D |  | $\begin{array}{\|l\|} \hline \text { B4 } \\ \hline \end{array}$ |
| $83 \mathrm{xx} \times 11 \mathrm{E}$ |  | $\begin{aligned} & \text { B2 B4 } \\ & \text { B3 B5 } \end{aligned}$ |
| 83 xx x 11 F |  | $\begin{array}{\|l\|l\|} \hline \text { B2 B4 } \\ \text { B3 B5 } \\ \hline \end{array}$ |
| $83 \mathrm{xx} \times 120$ |  | $\begin{array}{\|llll} \mathrm{B} 2 & \mathrm{~B} 4 & \mathrm{~B} 6 & \mathrm{E} 1 \\ \mathrm{~B} 3 & \mathrm{~B} 5 & \mathrm{~B} 7 & \mathrm{E} 1 \\ \hline \end{array}$ |
| $83 \mathrm{xx} \times 121$ |  | $\begin{aligned} & \text { E1 B4 B2 B6 } \\ & \text { E1 B5 B3 B7 } \\ & \hline \end{aligned}$ |
| $83 \mathrm{xx} \times 122$ |  | $\begin{aligned} & \text { B2 B4 } \\ & \text { B3 B5 } \end{aligned}$ |
| $83 \mathrm{xx} \times 123$ |  | $\begin{aligned} & \mathrm{B} 2 \mathrm{~B} 4 \mathrm{E} 1 \\ & \mathrm{~B} 3 \mathrm{~B} 5 \mathrm{E} 1 \\ & \hline \end{aligned}$ |
| $83 \mathrm{xx} \times 124$ |  | $\begin{array}{\|l\|l\|l\|} \hline \text { E1 B2 B4 } \\ \text { E1 B3 B5 } \\ \hline \end{array}$ |
| $83 \mathrm{xx} \times 125$ |  | $\begin{array}{\|l\|} \hline \text { B4 } \\ \text { B5 } \\ \hline \end{array}$ |
| $83 \mathrm{xx} \times 12.6$ |  | $\begin{array}{\|l\|} \hline \text { B8 } \\ \hline \end{array}$ |
| $83 \mathrm{xx} \times 127$ |  | $\begin{array}{\|l\|} \hline \text { B2 } \\ \text { B3 } \\ \hline \end{array}$ |

## AOI Card 4

9335 SRC Table

| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & 1234: 5678 \\ & \hline \end{aligned}$ | Delinition | For result code definitions, see Al4 thru A 16 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $83 \mathrm{xx} \times 128$ |  | $\begin{array}{\|lll\|} \hline \text { B8 B4 B2 } \\ \text { B9 B5 B3 } \\ \hline \end{array}$ |
| $83 \mathrm{xx} \times 129$ |  | P2 S6 C2 C3 |
| $83 \mathrm{xx} \times 12 \mathrm{~A}$ |  | $\begin{array}{\|lllll} \hline \text { M1 B2 B4 } & \text { P1 } & \text { S } 2 & \text { C5 } \\ \text { B0 } & & & \\ \text { M1 B3 B5 P1 } & \text { S } 2 & \text { C5 } \\ \text { B0 } & & & \\ \hline \end{array}$ |
| $83 \mathrm{xx} \times 1213$ |  | M1 M2 M1 |
| 83xx 812C |  | P1 C3 |
| $83 \times 8 \times 12 \mathrm{D}$ |  | M2 M1 E1 L1 |
| $83 \mathrm{xx} \times 12 \mathrm{E}$ |  | $\begin{aligned} & \text { B1 B4 B2 } \\ & \text { B1 B5 B3 } \end{aligned}$ |
| $83 \mathrm{x} \times \times 12 \mathrm{~F}$ |  | $\begin{aligned} & \text { B2 } \\ & \text { B3 } \end{aligned}$ |
| $83 \mathrm{xx} \times 130$ |  | E1 C3 |
| $83 \mathrm{xx} \times 131$ |  | $\begin{array}{\|llll\|} \hline \text { B8 B4 B6 B2 } & \text { C3 } \\ \text { B9 B5 B7 B3 } & \text { C3 } \\ \hline \end{array}$ |
| $83 \mathrm{xx} \times 132$ |  | $\begin{array}{\|l\|l\|} \hline \text { P1 B8 B4 } \\ \text { Pi B9 B5 } \\ \hline \end{array}$ |
| $83 \mathrm{xx} \times 133$ |  | B2 B3 B4 B5 B6 B7 |
| $83 \mathrm{xx} \times 1.34$ |  | E1 B2 B4 B6 <br> E1 B3 B5 B7 |
| $83 \times x \times 135$ |  | $\begin{aligned} & \mathrm{B} 4 \mathrm{~B} 5 \mathrm{~B} 2 \mathrm{~B} 3 \quad \text { B6 B7 } \\ & \mathrm{E} 1 \end{aligned}$ |

A02 Card 4

| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & 123+5678 \\ & \hline \end{aligned}$ | Delinition | For restit! code definitions, see Al4 thri: A16 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $83 \mathrm{xx} \times 136$ |  | B2 134 E1 B6 B3 B5 E1 B7 |
| $83 \mathrm{xx} \times 137$ |  | P1 |
| $83 \mathrm{xx} \quad \times 138$ |  | M1 B4 <br> M1 135 |
| $83 \mathrm{xx} \times 139$ |  | B1 A2 A1 |
| $83 \mathrm{xx} \times 13 \mathrm{~A}$ |  | $\begin{aligned} & \text { B2 I34 } \\ & \text { B3 B5 } \end{aligned}$ |
| $83 \mathrm{xx} \times 13 \mathrm{~B}$ |  | $\begin{aligned} & \text { E1 B2 B4 B6 } \\ & \text { A2 } \\ & \text { B1 A1 } \\ & \text { E1 } 133 \\ & \text { A2 } \end{aligned}$ |
| $83 \mathrm{xx} \times 13 \mathrm{C}$ |  | $\begin{array}{lllll} \hline \text { E1 } & \text { B2 } & \text { B6 } \\ \text { E1 } & \text { B3 } & 137 \end{array}$ |
| $83 \mathrm{xx} \times 13 \mathrm{D}$ |  |  |
| $83 \mathrm{xx} \times 13 \mathrm{~F}$ |  | A1 B1 |
| $83 \mathrm{xx} \times 140$ |  | $\begin{array}{\|l} \mathrm{B} 2 \mathrm{~B} 6 \\ \text { B3 B7 } \\ \hline \end{array}$ |
| $83 \mathrm{xx} \times 141$ |  | B 2 31 B 6 Al <br> B 4 E A 2  <br> B 3 B 1 B 7 A 1 E 1 <br> B A 2   <br> B 5    |

9335 SRC Table

| $\begin{array}{\|l\|l\|} \hline 9335 \\ \text { SRC } \\ 1234 & 5678 \\ \hline \end{array}$ | Definition | For result code definitions, see A 14 thru A 16 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $83 \mathrm{xx} \times 142$ |  | E1 136 <br> E1 137 |
| $83 \mathrm{xx} \times 143$ |  | $\begin{aligned} & \text { P1 B4 } \\ & \text { P1 B5 } \end{aligned}$ |
| $83 \mathrm{xx} \times 144$ |  | $\begin{aligned} & \text { B8 B4 } \\ & \text { B9 135 } \end{aligned}$ |
| $83 \mathrm{xx} \times 145$ |  | $\begin{array}{\|l\|} \hline \text { B6 } \\ \hline \end{array}$ |
| $83 \mathrm{xx} \times 146$ |  | $\begin{aligned} & \text { B2 B4 } \\ & \text { B3 B5 } \end{aligned}$ |
| $83 \mathrm{xx} \times 147$ |  | $\begin{array}{\|llll} \mathrm{Z} 2 & \mathrm{~B} 4 & \mathrm{~B} 1 \mathrm{~B} 2 & \mathrm{~A} 2 \\ \mathrm{Z} 2 & \mathrm{~B} 5 & \mathrm{~B} 1 & \mathrm{~B} 3 \\ \mathrm{~A} 2 \end{array}$ |
| $83 \mathrm{xx} \times 148$ |  | $\begin{aligned} & \text { P1 B2 B4 B6 } \\ & \text { P1 } 1 \text { B3 } 35 \text { B7 } \end{aligned}$ |
| $83 \mathrm{xx} \times 149$ |  | $\begin{array}{lllll} \text { M1 P1 } & \text { B4 } & Z 2 \\ \text { M1 P1 } & \text { P5 } & \text { Z1 } \end{array}$ |
| $83 \mathrm{xx} \times 14 \mathrm{~A}$ |  | $\begin{array}{\|l\|} \hline \text { B4 } \\ \hline \text { B5 } \\ \hline \end{array}$ |
| $83 \mathrm{xx} \times 14 \mathrm{~B}$ |  | $\begin{aligned} & \text { B4 B6 } \\ & \text { B5 B7 } \end{aligned}$ |
| 83 xx x 14 C |  | $\begin{aligned} & \text { B2. B4 B6 } \\ & \text { B3 B5 B7 } \end{aligned}$ |
| $83 \mathrm{xx} \times 14 \mathrm{D}$ |  | $\begin{aligned} & \mathrm{B} 8 \mathrm{~B} 2 \mathrm{B4} \\ & \mathrm{~B} 9 \mathrm{~B} 3 \mathrm{~B} 5 \end{aligned}$ |


| $\begin{array}{\|l\|l\|} \hline 9335 & \\ \text { SRC } \\ 1234 & 5678 \\ \hline \end{array}$ | Definition | For result code detinitions, see A14 thru A16 Card 4 <br> Result Code <br> by Priority |
| :---: | :---: | :---: |
| $83 \mathrm{xx} \times 14 \mathrm{E}$ |  | $\begin{array}{llll} \hline \text { P1 B8 } & \text { B4 } \\ \text { P1 } & \text { B9 } & \text { B5 } \end{array}$ |
| $83 \mathrm{xx} \times 14 \mathrm{~F}$ |  | $\begin{aligned} & \text { P1 B4 B1 A2 } \\ & \text { P1 B5 B1 A2 } \end{aligned}$ |
| $83 \mathrm{xx} \times 150$ |  | $\begin{aligned} & \text { B4 A1 B1 } \\ & \text { B5 A1 B1 } \\ & \hline \end{aligned}$ |
| $83 \times x \times 151$ |  |  |
| $83 \mathrm{xx} \times 1 \mathrm{~A} 0$ |  | $\begin{aligned} & \mathrm{B} 1 \mathrm{~B} 4 \mathrm{~A} 2 \mathrm{~A} 1 \\ & \mathrm{~B} 1 \mathrm{~B} 5 \mathrm{~A} 2 \mathrm{~A} 1 \end{aligned}$ |
| $83 \mathrm{xx} \mathrm{x1A1}$ |  | $\begin{array}{\|l} \mathrm{B} 1 \mathrm{~B} 4 \mathrm{~A} 2 \mathrm{~A} 1 \\ \mathrm{~B} 1 \mathrm{~B} 5 \mathrm{~A} 2 \mathrm{~A} 1 \\ \hline \end{array}$ |
| $83 \mathrm{xx} \mathrm{x1A2}$ |  | $\begin{array}{\|l} \mathrm{B} 1 \mathrm{~B} 4 \mathrm{~A} 2 \mathrm{~A} 1 \\ \mathrm{~B} 1 \mathrm{~B} 5 \mathrm{~A} 2 \mathrm{~A} 1 \\ \hline \end{array}$ |
| $83 \mathrm{xx} \times 1 \mathrm{~A} 3$ |  | $\begin{array}{\|lll} \mathrm{B} 1 \mathrm{~B} 2 \mathrm{~B} 4 \mathrm{~A} 2 & \mathrm{~A} 1 \\ \mathrm{~B} 1 \mathrm{~B} 3 & \mathrm{~B} 5 & \mathrm{~A} 2 \end{array}$ |
| $83 \mathrm{xx} \mathrm{x1A4}$ |  |  |
| $83 \mathrm{xx} \times 1 \mathrm{~A} 5$ |  | $\begin{array}{\|ll} \mathrm{B} 1 \mathrm{~B} 4 \mathrm{~A} 2 \mathrm{~A} 1 \\ \mathrm{~B} 1 \mathrm{~B} 5 \mathrm{~A} 2 \mathrm{~A} 1 \\ \hline \end{array}$ |
| $83 \mathrm{xx} \times 1 \mathrm{~A} 6$ |  | $\begin{aligned} & \mathrm{B} 1 \mathrm{~B} 4 \mathrm{~A} 2 \mathrm{~A} 1 \\ & \mathrm{~B} 1 \mathrm{~B} 5 \mathrm{~A} 2 \mathrm{~A} 1 \end{aligned}$ |

9335 SRC Table

| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & 12345678 \end{aligned}$ | Definition | For result code definitions, see A14 thru A16 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $83 \mathrm{xx} \times 1 \mathrm{~A} 7$ |  | B1 B4 A2 A1 <br> B1 B5 A2 A1 |
| $83 \mathrm{xx} \times 1 \mathrm{~A} 8$ |  | On-site Service Required. |
| 83 xx x1A9 |  | B1 B4 A2 A1 <br> B1 B5 A2 A1 |
| 83 xx x 1 AA |  | $\begin{array}{\|lll} \mathrm{B} 1 & \mathrm{~B} 4 & \mathrm{~B} 2 \\ \mathrm{~A} 2 & \mathrm{~A} 1 \\ \mathrm{~B} 1 & \mathrm{~B} 5 & \mathrm{~B} 3 \\ \mathrm{~A} & \mathrm{~A} & \mathrm{~A} 1 \end{array}$ |
| 83 xx x1FD | The device is not operational. Check that file has not been switched off. | On-site Service Required. |
| $83 \mathrm{xx} \times 1 \mathrm{FE}$ |  | B1 B4 B5 A3 |
| $84 \mathrm{xx} \times 401$ | Data check in data field B | $\begin{aligned} & \text { B6 B1 A1 } \\ & \text { B7 B1 A1 } \end{aligned}$ |
| $84 \times x \times 402$ | Data check in ID field | $\begin{aligned} & \text { B6 B1 A1 } \\ & \text { B7 B1 A1 } \end{aligned}$ |
| $84 \mathrm{xx} \times 403$ | Data check in data field A | $\begin{aligned} & \text { B6 B1 A1 } \\ & \text { B7 B1 A1 } \end{aligned}$ |
| $84 \times x \times 404$ | Sync on ID field unsuccessful | A1 |
| $84 \times x-405$ | Sync on data field A unsuccessful | A1 |
| $84 \mathrm{xx} \times 406$ | Sync on data field B unsuccessful | A1 |
| $84 \mathrm{xx} \times 407$ | Read-back check on data field A or B failed | A1 |
| $84 \mathrm{xx} \times 409$ | Data check threshold exceeded | $\begin{array}{\|lll} \mathrm{B} 6 & \mathrm{~B} 1 & \mathrm{~A} 1 \\ \mathrm{~B} 7 \mathrm{~B} 1 & \mathrm{~A} 1 \end{array}$ |
| $84 \mathrm{xx} \times 418$ | Sector retry counter excceded | $\begin{array}{ll} \text { B2 B4 } \\ \text { B3 } \end{array}$ |


| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & 12345678 \end{aligned}$ | Definition | For result code definitions, see A14 thru A16 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $84 \times \mathrm{x} \times 19$ | Logging complete for data check threshold exceeded | B6 B1 A1 |
|  |  | B7 B1 A1 |
| $84 \mathrm{xx} \times 4 \mathrm{FF}$ | Invalid format 4 incssage. | A3 |
| $85 \mathrm{xx} \times 102$ |  | A3 |
| $85 \mathrm{xx} \times 288$ |  | A3 |
| $85 \mathrm{xx} \times 2 \mathrm{FF}$ | No system adapter or microcode error found | A3 |
| $85 \times x \times 401$ |  | A3 A1 |
| $85 \mathrm{xx} \times 407$ |  | A3 |
| $85 \mathrm{xx} \times 408$ |  | A3 |
| $85 \mathrm{xx} \times 40 \mathrm{~A}$ |  | A3 |
| $85 \mathrm{xx} \times 40 \mathrm{C}$ |  | A3 |
| $85 \mathrm{xx} \times 40 \mathrm{E}$ |  | A3 |
| $85 \mathrm{xx} \times \mathbf{4 1 0}$ |  | A3 |
| $85 \mathrm{xx} \times 412$ |  | A3 |
| $85 \mathrm{xx} \times 414$ |  | A3 A1 |
| $85 \mathrm{xx} \times 415$ |  | A3 |
| $85 \mathrm{xx} \times 416$ |  | A3 |
| $85 \mathrm{xx} \times 417$ |  | A3 |
| $85 \times \mathrm{x} \times 418$ |  | A3 A1 |
| $85 \mathrm{xx} \times 419$ |  | A3 A1 |
| $85 \times x \times 41 A$ |  | A3 A1 |

9335 SRC 「able

| $\begin{array}{\|l\|} \hline 9335 \\ \text { SRC } \\ 1234 \\ \hline 12678 \\ \hline \end{array}$ | Definition | For result code definitions, see Al4 thru A16 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $85 \mathrm{xx} \times 11 \mathrm{~B}$ |  | A2 A3 |
| $85 \mathrm{xx} \times 41 \mathrm{C}$ |  | A3 |
| $85 \mathrm{xx} \times 41 \mathrm{E}$ |  | A3 |
| $885 \mathrm{xx} \times 420$ |  | A3 |
| $85 \mathrm{xx} \times 422$ |  | A3 |
| $85 \times \mathrm{xx} \times 424$ |  | A3 |
| $85 \times \times \times 426$ |  | A3 |
| $85 \times x \times 428$ |  | A1 A3 |
| $85 \mathrm{xx} \times 429$ |  | A3 |
| $885 \mathrm{xx} \times 42 \mathrm{~B}$ |  | A1 A3 |
| $85 \mathrm{xx} \times 42 \mathrm{C}$ |  | A3 |
| $85 \mathrm{xx} \times 242 \mathrm{E}$ |  | A3 |
| 85xx $\times 430$ |  | A3 |
| 85 xx $\times 432$ <br> 85 x  |  | A1 A3 |
| 855x $\times 481$ | . | A 1 |
| 85 xx x 811 <br> 858  |  | A1 A2 |
| $85 \mathrm{xx} \times 812$ |  | A3 A1 A2 |
| 85\%x $\times .832$ |  | A3 |
| $85 \mathrm{xx} \times 842$ |  | A3 |
| $85 \times 8 \times 82$ |  | A3 |
| $85 \mathrm{xx} \times 871$ |  | A3 |


| 9335 <br> SRC <br> 12345678 | Definition | For resull code definitions, see Alt thro A16 Card 4 <br> Result Code <br> by Priority |
| :---: | :---: | :---: |
| $85 \mathrm{xx} \times 872$ |  | A3 |
| $85 \mathrm{xx} \times 880$ |  | A3 |
| $85 \times<\times 8 \mathrm{E} 1$ |  | A 3 |
| $85 \mathrm{x} \times \mathrm{x} 8 \mathrm{E} 2$ |  | A3 |
| $86 \mathrm{xx} \times 000$ | No information available | A3 |
| $86 \mathrm{xx} \times 005$ | Attributes not loaded | On-site Service Required. |
| $86 \times \times \times 00 \mathrm{~A}$ | RAM load-level mismatch | On-site Service Required. |
| $86 \times 8 \times 00 \mathrm{C}$ | Reallocate falled - no atarnate space available | On-site Servicr Required. |
| $86 \times x \times 010$ |  | A3 |
| $86 \times x \times 014$ | Alternate space almost exhausted | A3 |
| $87 \mathrm{xx} \times 001$ |  | $\begin{aligned} & B 4 B 1 A 2 \\ & B 5 B 1 A 2 \end{aligned}$ |
| $87 \mathrm{xx} \times 100$ |  | A2 A3A1 |
| $87 \mathrm{xx} \times 101$ |  | $\begin{aligned} & \text { A2 B1 B4 } \\ & \text { A2 B1 B5 } \\ & \hline \end{aligned}$ |
| $87 \mathrm{xx} \times 102$ |  | A2B1 B4 |
| $87 \mathrm{xx} \times 104$ |  | A2B1 B5 |
| $87 \mathrm{xx} \times 108$ |  | A2B1 B4 |
| $87 \mathrm{xx} \times 110$ |  | A2 B1 35 |
| $87 \mathrm{xx} \times 120$ |  | A2B1B4 |
| $87 \mathrm{xx} \times 140$ |  | A2 131 135 |

9335 SRC Table

| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & 123+5678 \end{aligned}$ | Definition | For result code detinitions, see Alt thru A16 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $87 \times \mathrm{xx} \times 180$ |  | A2 131 34 |
| $87 \mathrm{xx} \times 1 \mathrm{FF}$ |  | A2 A3 A1 |
| $87 \times \times 201$ |  | $\begin{array}{llll} \text { A2 B1 } 34 \\ \text { A } 2 & \text { B1 } 135 \end{array}$ |
| $87 \times x \times 301$ |  | $\begin{array}{ll} 131 & 134 \\ 131 & \text { A } 22 \end{array}$ |
| $87 \times x-401$ |  | $\begin{aligned} & \mathrm{B} 1 \mathrm{~B} 4 \mathrm{~A} \\ & \mathrm{~B} 1 \mathrm{~B} 5 \mathrm{~A} 2 \end{aligned}$ |
| $87 \times 8 \times 601$ |  | $\begin{array}{llll} \text { B2 B } 1 & \text { A } & 12 \\ 133 & \text { B1 } & \text { A } \end{array}$ |
| $87 \times 8 \times 1$ |  |  |
| $87 \mathrm{xx} \times 801$ |  | $\begin{aligned} & \text { B2 B1 A1 A2 } \\ & \text { B3 B1 A1 A2 } \end{aligned}$ |
| $87 \times 8 \times 1$ |  | $\begin{aligned} & \mathrm{B} 2 \mathrm{~B} 1 \mathrm{~A} 1 \mathrm{~A} 2 \\ & \mathrm{~B} 3 \mathrm{~B} 1 \mathrm{~A} 1 \mathrm{~A} 2 \end{aligned}$ |
| $87 \mathrm{xx} \times \mathrm{A} 01$ |  | $\begin{aligned} & \mathrm{B} 2 \mathrm{~B} 1 \mathrm{~A} 1 \mathrm{~A} 2 \\ & \mathrm{~B} 3 \mathrm{~B} 1 \mathrm{~A} 1 \mathrm{~A} 2 \end{aligned}$ |
| $87 \mathrm{xx} \times 1301$ |  | D1 D3 D2 BI A2 |
| $87 \mathrm{xx} \times \mathrm{CO1}$ |  | $\begin{array}{l\|l} \mathrm{B} 4 \mathrm{~B} 1 \text { A2 } \\ \mathrm{B5} \text { B1 A2 } \end{array}$ |
| 87 xx , 1001 |  | 134135131 A2 |
| $87 \mathrm{xx} \times \mathrm{CO1}$ |  | A2 B1 |


| 9335 <br> SRC <br> 12345678 | Definition | For result code dedinitions, see Alt thru AlG Card 4 <br> Result Code by Priosity |
| :---: | :---: | :---: |
| $87 \mathrm{xx} \times \mathrm{F} 01$ |  | $\begin{aligned} & \mathrm{A} 2 \mathrm{~B} 1 \mathrm{~B} 4 \\ & \mathrm{~A} 2 \mathrm{~B} 1 \mathrm{~B} \end{aligned}$ |
| $888 \times 8082$ |  | A1 A3 A2 |
| 88 xx . 0083 |  | ```A3 A1 A2 134 132 B1 B6 E1 A3 A1 A2 B5 B3 B1 137 E1``` |
| $88 \times 8.8091$ |  | A1 A3 |
| $88 \times x$ |  | 12 |
| ¢88x $\times 130$ |  | A2 A3 |
| $\begin{array}{ll}88 \mathrm{x} & \times 131\end{array}$ | 1)erice hecame "not available" | P1 22131 |
| 888x $\times 133$ |  | A2 A3 |
| $88 \times 8$ | Device error-invalid format 8 | $\begin{array}{lllllll} 134 & \text { B } 2 & 131 & 136 \\ \text { B5 } & 133 & \text { B1 } & \text { B7 } \end{array}$ |
| $88 \mathrm{cx} \times 13 \mathrm{E}$ |  | A1 A2 |
| $88 \times 8 \times 14$ |  | A2 |
| $88 \times 8 \times 143$ |  | A2 |
| $888 \times 145$ |  | A2 A3 |
| $888 \mathrm{xx} \times 146$ |  | 131 A2 |
| $88 \times \mathrm{x} \times 148$ |  | A2 |
| $\begin{array}{ccc}88 \mathrm{cx} & 14 \mathrm{E}\end{array}$ |  | A2 |
| $88 \mathrm{xx} \times 172$ |  | A2 |

## 9335 SRC Table

| $\begin{aligned} & 9335 \\ & 5 R C \\ & 12345678 \end{aligned}$ | Delinition | Fur result code delintitions, see Alt thru AlG Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $88 \times x \times 176$ |  | A2 |
| $88 \mathrm{xx} \times 177$ |  | A2 |
| $88 \mathrm{xx} \times 17 \mathrm{~L}$ |  | A2 |
| $88 \mathrm{xx} \times 181$ | 10 mismath - invalid format 8 | A1 13 |
| $88 \times 8 \times 18 \mathrm{~L}$ |  | A1 A2131 |
| $88 \times 8 \times 20 \mathrm{~L}$ |  |  |
| $88 \times 5 \times 20$ | Data check or no-sync-byte-found failure - invalse format 8 | A1 13 |
| $88 \times 8 \times 240$ |  | A1 |
| $88.8 \times 241$ |  | A1 |
| $88 \times x \quad 242$ |  | A1 |
| $88 \times 243$ |  | A1 |
| $88 \mathrm{xx} \times 245$ |  | A1 |
| $88.4 \times 245$ |  | A1 |
| $88 \times 8.52$ |  | A1 |
| $88 \times x \times 253$ |  | A1 |
| $88 \cdots \times 254$ |  | A1 13 |
| $88 \mathrm{xx} \times 256$ |  | A1 $\mathbf{A B}$ |
| $88 \times 25 \mathrm{~L}$ |  | A1 |


| $\begin{array}{\|l\|} \hline 9335 \\ \text { SRC } \\ 1234 \\ \hline \end{array}$ | Definition | For result code definitions, see Al4 thru A16 Card 4 <br> Result Code <br> by Priority |
| :---: | :---: | :---: |
| 88 xx $\times 860$ |  | A 1 A3 A2 |
| $88 \mathrm{xx} \times 261$ |  | A1 A3 |
| 88xx $\times 262$ |  | A1 A3 |
| 88xx $\times 263$ |  | A3 |
| $88 \times \mathrm{xx} \times 26 \mathrm{E}$ |  | A1 |
|  |  | A1 A3 A2 |
| 888x $\times 271$ |  | A1 A3 |
| 88xx $\times 273$ |  | A1 A3 |
| $88 \mathrm{xx} \times 27 \mathrm{E}$ |  | A1 |
| $88 \mathrm{xx} \times 403$ |  | A1 A2 |
| $888 \mathrm{xx} \times 405$ |  | A 1 |
| $88 \mathrm{xx} \times 406$ |  | A1 A3 |
| $\begin{array}{ll}88 \mathrm{cx} & \times 407\end{array}$ |  | A1. A3 |
| $88 \mathrm{xx} \times \mathrm{x} 408$ |  | A1 A3 |
| 88 xx $\times 409$ <br> 88  |  | A1 A3 |
| 88xx 8 840A |  | A! A3 |
| 88 xx $\times 40 \mathrm{~B}$ <br> 8 c  |  | A1 A3 |
| $88 \mathrm{xx} \times 40 \mathrm{E}$ |  | A2 |
| $88 \mathrm{xx} \times 410$ |  | A1 A2 A3 |
| $88 \mathrm{xx} \times 411$ |  | A1 A2 |
| $88 \times x$ |  | $\begin{aligned} & \mathrm{A} 1 \mathrm{~B} 2 \mathrm{~B} 1 \mathrm{~A} 2 \\ & \mathrm{~A} 1 \mathrm{~B} 3 \mathrm{~B} 1 \mathrm{~A} 2 \end{aligned}$ |


| $\begin{array}{\|l} \hline 9335 \\ \text { SRC } \\ 123+5678 \\ \hline \end{array}$ | Definition | For result code defintions, see Alt thru Alf Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| 888x $\times 113$ |  | Al |
| $88 \mathrm{xx} \times 522$ |  | B1 A2 |
| $88 \mathrm{xx} \times 8 \mathrm{FF}$ | No device adapter hardware or mierocode error found - invalid format 8 | A1 A2 A3 |
| $89 \mathrm{xx} \times 902$ |  | A3 |
| 89xx $\times 904$ |  | A3 |
| 89xx $\times 906$ |  | A3 A1 A2 |
| 89xx 9908 |  | A2 |
| $89 \mathrm{xx} \times 90 \mathrm{C}$ |  | A2 |
| $89 \mathrm{xx} \times 910$ |  | A1 |
| 89 xx <br> 8914 |  | A1 A3 |
| 89xx 8924 |  | A3 |
| $89 \mathrm{xx} \times 93 \mathrm{E}$ |  | A3 A1 A2 |
| $89 \mathrm{xx} \times 9 \mathrm{AC}$ | IML download is needed. | On-site Scrvice Required. |
| $89 \mathrm{xx} \times 9 \mathrm{B0}$ | Correct level IML download is needed. | On-site Service Required. |
| 8 Exx xEEE |  | A3 |
| $8 \mathbf{F}_{\mathrm{xx}} \times \mathrm{FD} 0$ | The Model A0l cannot communicate with the using system | A3 |
| $8 \mathrm{~F}_{\mathrm{xx}} \times \mathrm{xFD1}$ | The using system is unable to degate the Model A01 | A3 |
| $8 \mathbf{F}_{\mathrm{xx}} \times \mathrm{FFD} 2$ | The using system diagnosed a Model A01 internal DFCl cable failure | D3 |
| $8 \mathbf{F}_{\mathrm{xx}} \times \mathrm{F} \mathbf{F} 3$ | DFCl error reported by the Model A0I but not detected by the using system | A3 |


| $\begin{aligned} & 9335 \\ & \text { SRC } \\ & \text { 1234 } 5678 \end{aligned}$ | Definition | For result code definitions, see A14 thru A16 Card 4 <br> Result Code by Priority |
| :---: | :---: | :---: |
| $8 \mathrm{~F}_{\mathrm{xx}} \times \mathbf{F E} \mathbf{0}$ | The Model A01 detected a sequence error | A3 |
|  | T. Model A01 detected a DFCl pasity error | A3 |
| $8 \mathbf{F}^{\text {x }}$ x $\times \mathbf{F} \mathbf{E} 2$ | The Model A01 detected a "sync in / sync out" error | A3 |
| $8 \mathrm{~F}_{\mathrm{xx}} \times \mathbf{F E} 3$ | The Model A0I detected a sequence crror | A3 |
| $8 \mathbf{F x x}^{\text {x }}$ - $\mathbf{F} \mathbf{F} \mathbf{0}$ |  | A3 A2 A1 |
| $8{ }^{\text {r }} \mathrm{x} \times \mathrm{x}$ - F | Unexpected errer when running diagnostic test programs | A3 |
| $8 \mathrm{~F} x \times \mathrm{FF} 4$ | The Model A01 cannot communicate with the using system | A3 |

## 9335 RC Table

| 9335 <br> Result Code | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| A1 | (A) Device adapter read/write card (0\|A-A|A!) | 59X6842 |
| A2 | (A) Device adapter interface card (0\|A-A|B|) | 59X6847 |
| A3 | (A) System adapter SAl card (0) A-AIC5) | 59X6855 |
| B0 | (B) Logic board (Model B01) 0\|A-Al | 8223640 |
| B1 | (B) Device interface card ( $01 \mathrm{~A}-\mathrm{A} \mid \mathrm{C} 1$ ) | 8233646 |
| B2 | (3) Demoduiator 0 card (01A-A\|A5) | 6243737 |
| 133 | (B) Demodulator 1 card (0\|A-A|A|) | 6243737 |
| B4 | (B) Servo 0 card (0\|A-A|B5) | 59X6839 |
| B5 | (B) Servo $\mid$ card ( $0\|\mathrm{~A}-\mathrm{A}\| \mathrm{BI}$ ) | 59X6839 |
| 136 | (B) Read detect 0 card (01A-FiAl) | 6243740 |
| 137 | (B) Read detect I card (01A-F1A2) | 6243740 |
| 138 | (B) Actuator driver 0 card (01A-GIAI) | 59X6835 |
| 139 | (B) Actuator driver 1 card (01A-GIA2) | 59X6835 |
| C0 | (A) Cable form, AC suppiy (Model A01) | 6200817 |
| C1 | (A) Cable form DC supply (Model A01) | 6200818 |
| C2 | (B) Cable form AC supply (Model B01) | 6200571 |
| C3 | (B) Cable form DC supply (Model B01) | 8232226 |


| $\begin{aligned} & 9335 \\ & \text { Result } \\ & \text { Code } \end{aligned}$ | Definition | P/N |
| :---: | :---: | :---: |
| C4 | (B) Power regulator cable | 6200572 |
| C5 | (A) Remote 'ower switch cable | 6200573 |
| C6 | (B) Read detect 0 cable (0\|A-F|A1) | 6243557 |
| C7 | (B) Read detect I cable (01A-F1A2) | 6243558 |
| C8 | (B) Actuator driver 0 cable (01A-G1Ai) | 8232355 |
| C9 | (B) Actuator driver 1 cable <br> (01A-G1A2) | 8232228 |
| D1 | (B) Device interface cable $0\|\mathrm{~A}-\mathrm{A}\| \mathrm{Cl}$ | 8232231 |
| D2 | (B) External adapter to device cable | 8232301 |
| D3 | (A) Device adaptor interface cable | 6200819 |
| E1 | (B) Disk enclosure | 8232220 |
| F1 | (A) Cooling fan | 6200886 |
| F2 | (B) Blower | 8232365 |
| 11 | (B) Interposer 01A-A\| 31-W | 6243604 |
| 12 | (B) Interposer 01A-A1B5-W | 6243604 |
| 13 | (B) Interposer 01A-A1B3-Y | 6243604 |
| I4 | (B) Interposer 01A-A1 B7-Y | 6243604 |
| 15 | $\begin{aligned} & \text { (B) Cross-card connector } \\ & \text { (01A-A1A2-X) } \\ & \hline \end{aligned}$ | 8232386 |
| 16 | (B) Cross-card connector ( $01 \mathrm{~A}-\mathrm{A} \mid \mathrm{A} 6-\mathrm{X}$ ) | 8232386 |
| L1 | (B) Spindle lock assembly | 6200720 |
| M1 | (B) Motor driver box (0IA-CIAI) | 8232196 |
| M2 | (B) Motor stator | 8222163 |


| $\begin{array}{\|l} \hline 9335 \\ \text { Result } \\ \text { Code } \end{array}$ | Definition | $\mathrm{P} / \mathrm{N}$ |
| :---: | :---: | :---: |
| P1 | (B) Power control card (01A-AIC5) | 59X6853 |
| P2 | (B) Prower regulator card (01A-EIA1) | 8223595 |
| P3 | (A) Switched-mode power supply | 6200890 |
| P4 | (B) Switched-mode power supply (0\|A-D|A1) | 8232239 |
| P5 | (B) AC power box | 6200583 |
| S! | (A) Power on/off switch | 6200575 |
| S2 | (B) Power on/off switch | 6200561 |
| S3 | (B) Power supply thermal switch | 6200580 |
| S4 | (A) Card-gate thermal switch | 6200639 |
| 55 | (B) Circuit breaker CB3 | 8232314 |
| S6 | (B) Circuit breaker CB4 | 8232316 |
| S7 | (B) Circuit breaker CB5 | 8232315 |
| 11 | (B) Transformei Tl | 8232246 |
| T2 | (B) Trarisformer T2 | 8232245 |
| Z1 | (A) Control panel (01A-ClAI) | 6200884 |
| Z2 | (B) Control pancl (01A-BIAI) | 8223710 |

